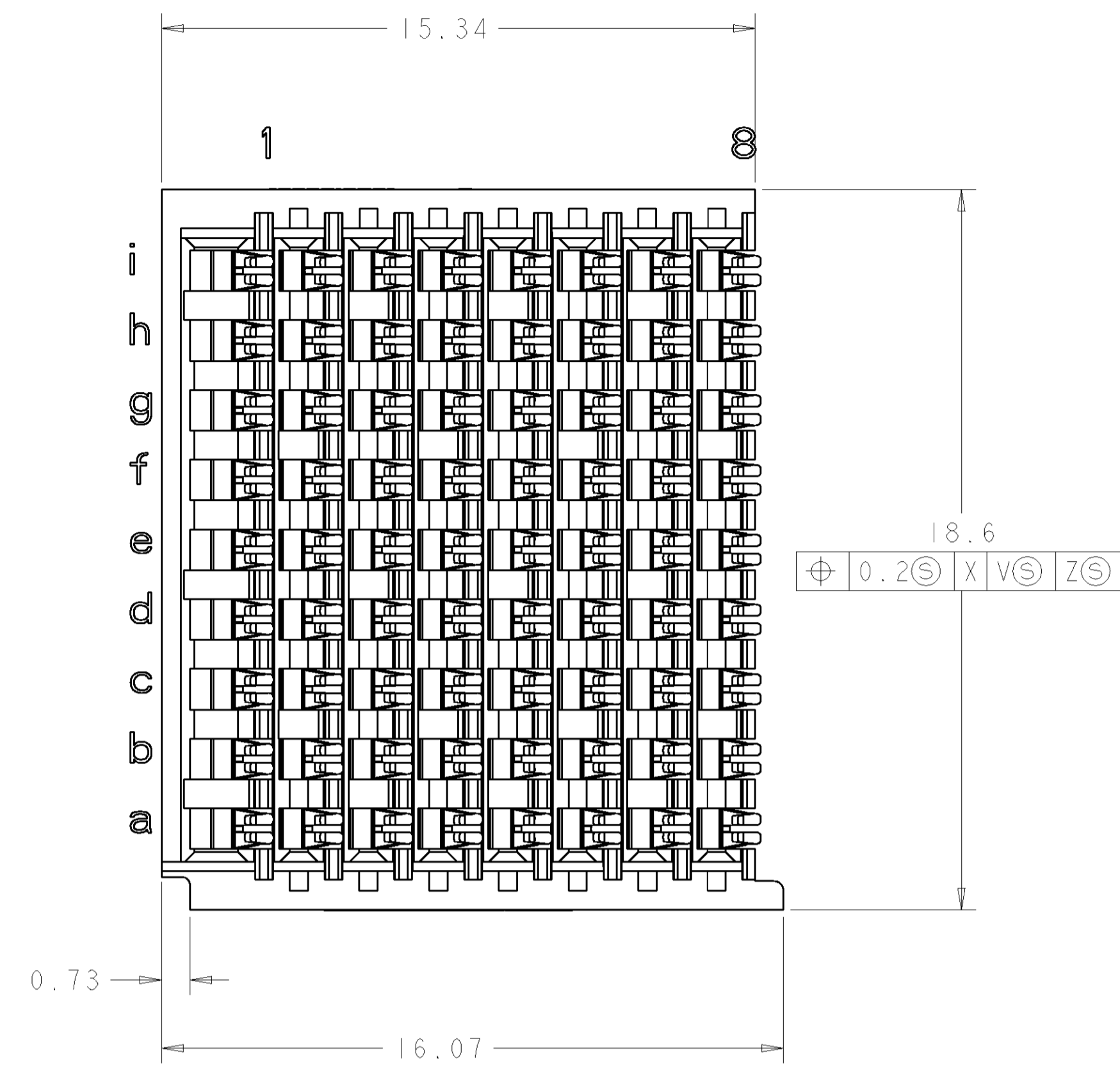
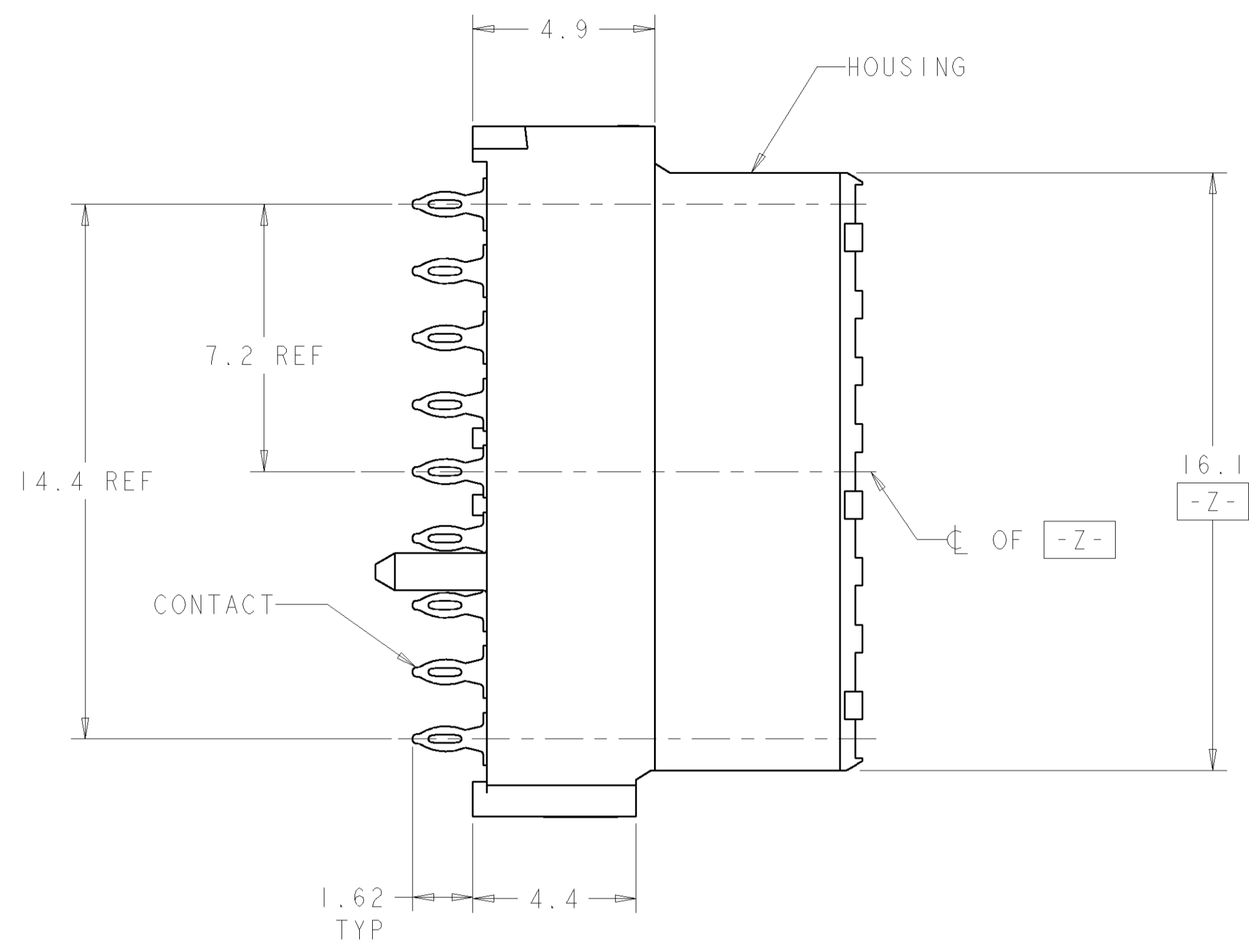
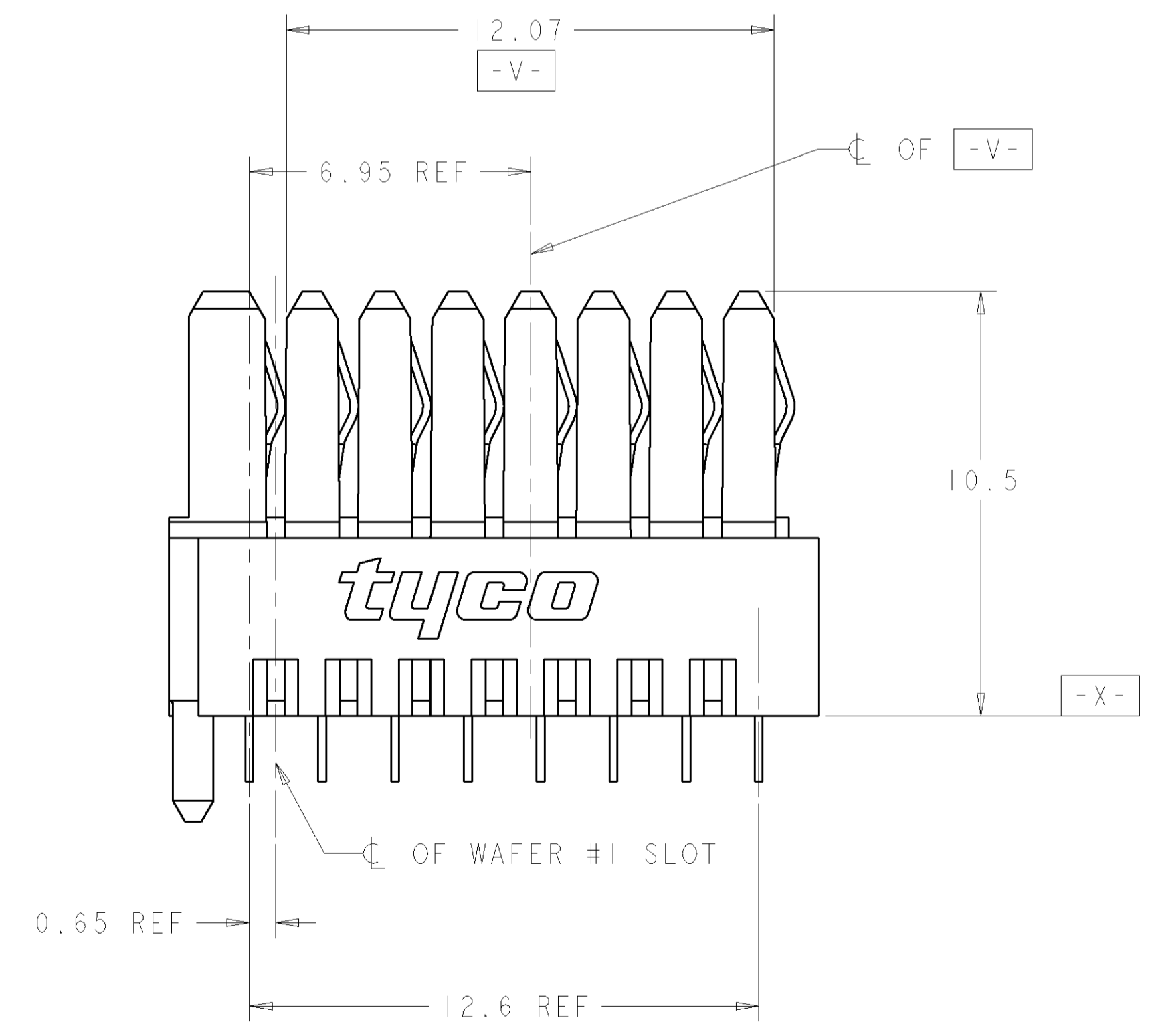
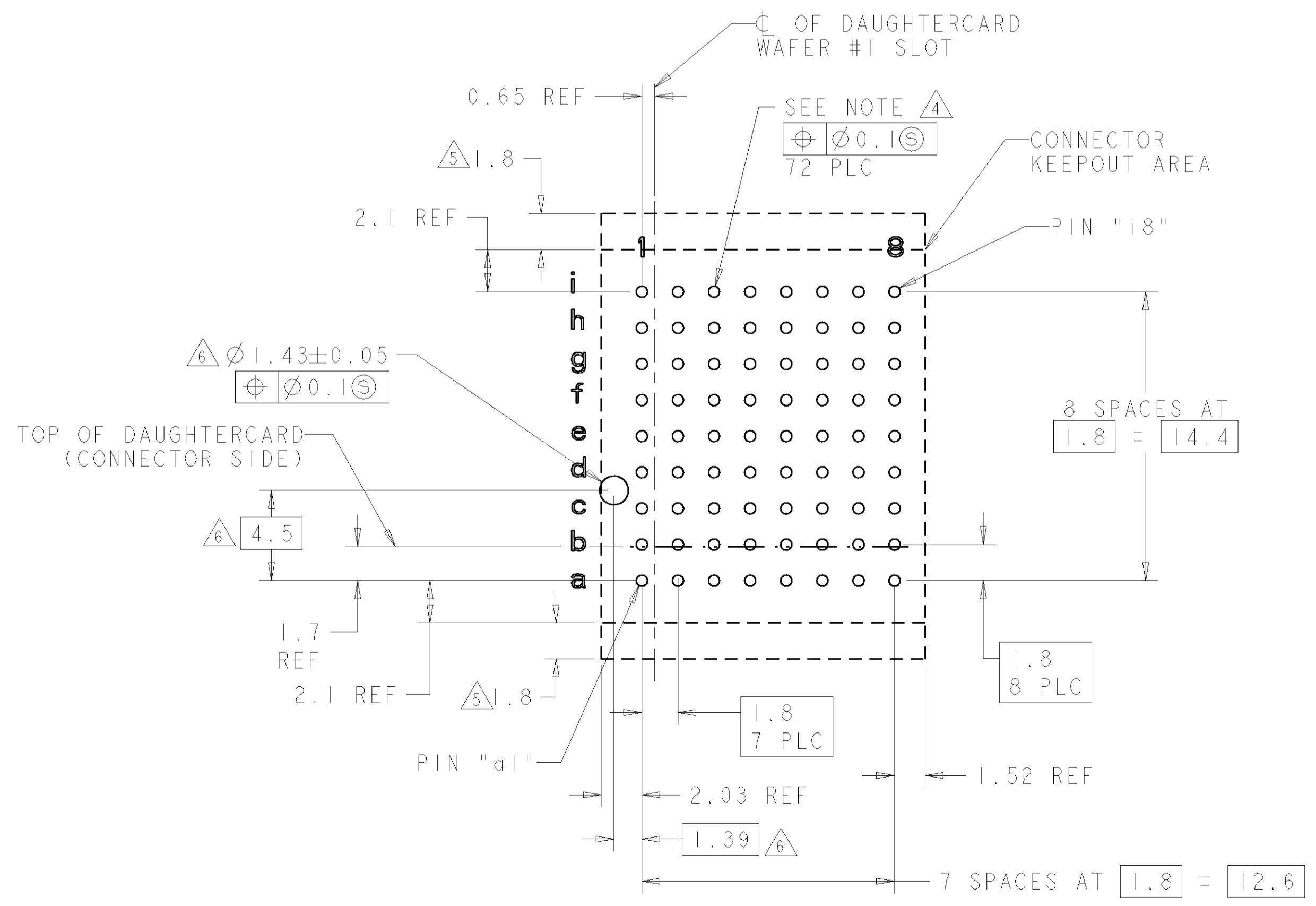


LOC		DIST		REVISIONS			
GP	00	P	LTR	DESCRIPTION	DATE	DWN	APVD
		01		PROPOSED	30SEP2003	LS	JJC
		02		ADDED TIN-PLATED P/N	23DEC2003	LS	JJC
		03		NOTES REMOVED	09JUL2004	RT	JJC
		04		ADDED PRELIMINARY	26AUG2004	LS	JJC



- 1 HOUSING: LCP, UL94V0, COLOR: BLACK. CONTACT: PHOSPHOR BRONZE.
- 2 FINISH: 1.27 μ m GOLD IN CONTACT AREA 1.27 μ m MIN. TIN-LEAD ON PCB TAIL OVER 3.81 μ m MIN. NICKEL OVER ALL.
- 3 FINISH: 1.27 μ m GOLD IN CONTACT AREA 1.27 μ m MIN. TIN ON PCB TAIL OVER 3.81 μ m MIN. NICKEL OVER ALL.
- 4 MANUFACTURING TOLERANCE FOR:
 $\varnothing 0.56 \pm 0.05$ FINISHED HOLE WITH Sn PB PLATING:
 DRILLED HOLE = $\varnothing 0.65 \pm 0.02$
 COPPER PLATING = 0.025-0.050
 Sn PB PLATING = 0.0038-0.0124
 OR
 $\varnothing 0.575 \pm 0.050$ FINISHED HOLE WITHOUT Sn PB PLATING:
 DRILLED HOLE = $\varnothing 0.65 \pm 0.02$
 COPPER PLATING = 0.025-0.050
- 5 ADDITIONAL CONNECTOR-SIDE KEEPOUT AREA REQUIRED IF CONNECTOR-SIDE EXTRACTION TOOL IS TO BE USED.



PC BOARD LAYOUT (CONNECTOR SIDE)
SCALE 5:1

PRELIMINARY	1410186-2
PLATING	1410186-1
	PART NO

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DIMENSIONS: mm		EL SIZE	30SEP2003				
TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC \pm 1 PLC ± 0.5 2 PLC ± 0.13 3 PLC \pm 4 PLC \pm ANGLES $\pm 1^{\circ}$		CHK	J CONSOLI	NAME CONNECTOR ASSEMBLY, BACKPLANE VERTICAL RECEPTACLE, HALF, LEFT END, 20.3mm, MultiGig RT 2			
MATERIAL: \triangle		APVD	30SEP2003				
SEE TABLE		PRODUCT SPEC	108-2072	SIZE	CAGE CODE	DRAWING NO	RESTRICTED TO
		APPLICATION SPEC	114-13056	A1	00779	C=1410186	
		WEIGHT		CUSTOMER DRAWING		SCALE	2:1
		FINISH				SHEET	1 OF 1
						REV	04