STRUCTURE SILICON MONOLITHIC INTEGRATED CIRCUIT

FUNCTION GROUND SENSE QUAD VOLTAGE COMPARATORS

PRODUCT SERIES **BA10339F**
BA10339FV

FEATURES • Open collector output

○ABSOLUTE MAXIMUM RATINGS(Ta=25[℃])

Parameter	Symbol		Rating	Unit
Supply Voltage	VCC-VEE		+36	V
Power dissipation	Pd	BA10339F	490(*1)(*3)	mW
		BA10339FV	700(*2)(*3)	mW
Differential Input Voltage (*4)	Vid		(VCC-VEE)	V
Input Common-mode Voltage Range	Vicm		(VEE-0.3)～VCC	V
Operating Temperature	Topr		-40～+85	℃
Storage Temperature Range	Tstg		-55～+125	℃
Maximum junction Temperature	Tjmax		125	℃

• This IC is not designed for protection against radioactive rays.
(*1) To use at temperature above Ta=25[℃] reduce 4.9[mW]/[℃].
(*2) To use at temperature above Ta=25[℃] reduce 7.0[mW]/[℃].
(*3) Mounted on a glass epoxy PCB(70[mm]×70[mm]×1.6[mm])
(*4) The voltage difference between inverting input and non-inverting input is the differential input voltage. Then input terminal voltage is set to more than VEE.

○OPERATING CONDITION(Ta=-40～+85[℃])

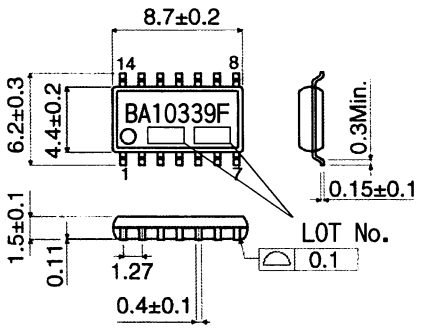
Parameter	Symbol	Rating	Unit
Supply Voltage	VCC	+3.0～+36.0 (Single Supply)	V
		±1.5～±18.0 (Split Supply)	

○ELECTRICAL CHARACTERISTICS (unless otherwise specified VCC=+5[V]、VEE=0[V])

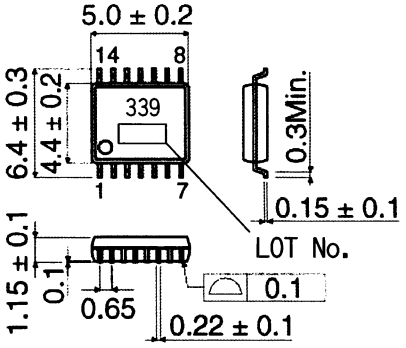
Parameter	Symbol	Temperature Range	Guaranteed Limit			Unit	Condition
			Min.	Typ.	Max.		
Input Offset Voltage (*5)	Vio	25℃	-	1	5	mV	VOUT=1.4[V]
Input Offset Current (*5)	Iio	25℃	-	5	50	nA	VOUT=1.4[V]
Input Bias Current (*6)	Ib	25℃	-	50	250	nA	VOUT=1.4[V]
Input Common-mode Voltage Range	Vicm	25℃	0	-	VCC-1.5	V	-
Large Signal Voltage Gain	AV	25℃	93	106	-	dB	VCC=15[V], VOUT=1.4～11.4[V], RL=15[kΩ], VRL=15[V]
Supply Current	ICC	25℃	-	0.8	2	mA	VOUT=open
Output Sink Current	IOL	25℃	6	16	-	mA	VIN+=0[V], VIN-=1[V], VOL=1.5[V]
Output Saturation Voltage (Low level Output Voltage)	VOL	25℃	-	250	400	mV	VIN+=0[V], VIN-=1[V], IOL=4[mA]
Output Leakage Current (High Level Output Current)	Ileak1	25℃	-	0.1	-	nA	VIN+=1[V], VIN-=0[V], VOH=5[V]
	Ileak2		-	-	1	μA	VIN+=1[V], VIN-=0[V], VOH=36[V]
Response Time	Tre	25℃	-	1.3	-	μs	RL=5.1[kΩ], VRL=5[V], VIN=100[mVp-p], overdrive=5[mV]

(*5) Absolute value.
(*6) Since first input stage is composed with PNP transistor, input bias current flows out of IC.

○Physical Dimensions

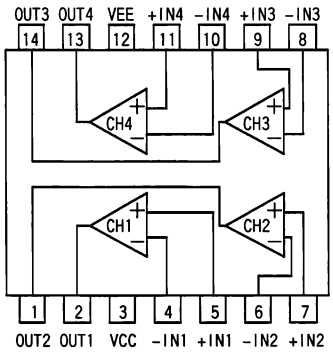


BA10339F(SOP14) (Unit:[mm])



BA10339FV(SSOP-B14) (Unit:[mm])

○Block diagram



○Pin No. • Pin Name

Pin No.	Pin Name
1	OUT2
2	OUT1
3	VCC
4	-IN1
5	+IN1
6	-IN2
7	+IN2
8	-IN3
9	+IN3
10	-IN4
11	+IN4
12	VEE
13	OUT4
14	OUT3

○Application example

(1) Absolute maximum ratings
Absolute maximum ratings are the values which indicate the limits, within which the given voltage range can be safely charged to the terminal. However, it does not guarantee the circuit operation.

(2) The example of disabled circuit application
When there is a circuit not in use, it is recommended to make the non-inverting input terminal be the potential in the common-mode input voltage range like in Fig.1.
Circuit operation is guaranteed within "Operating Conditions".

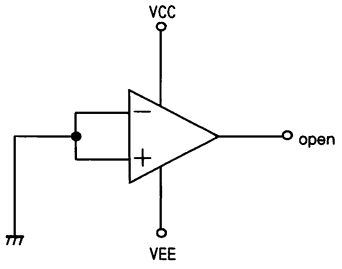


Fig.1 The example of disabled circuit

(3) Operating power supply (split power supply/single power supply)
The Comparator operates if a given level of voltage is applied between VCC and VEE. Therefore, the Comparator can be operated under single power supply or split power supply.

(4) Power dissipation(Pd)
If the IC is used under excessive power dissipation. An increase in the chip temperature will cause deterioration of the radical characteristics of IC. For example, reduction of current capability. Take consideration of the effective power dissipation and thermal design with a sufficient margin. Pd is reference to the provided power dissipation curve.

(5) Short circuits between pins and incorrect mounting
Short circuits between pins and incorrect mounting when mounting the IC on a printed circuits board, take notice of the direction and positioning of the IC. If IC is mounted erroneously, It may be damaged. Also, when a foreign object is inserted between output, between output and VCC terminal or VEE terminal which causes short circuit, the IC may be damaged.

(6) Output short circuit
If short circuit occurs between the output terminal and VCC terminal, excessive in output current may flow and generate heat, causing destruction of the IC. Take due care.

(7) Using under strong electromagnetic field
Be careful when using the IC under strong electromagnetic field because it may malfunction.

(8) Usage of IC
When stress is applied to the IC through warp of the printed circuit board, The characteristics may fluctuate due to the piezo effect. Be careful of the warp of the printed circuit board.

(9) Testing IC on the set board
When testing IC on the set board, in cases where the capacitor is connected to the low impedance, make sure to discharge per fabrication because there is a possibility that IC may be damaged by stress. When removing IC from the set board, it is essential to cut supply voltage. As a countermeasure against the static electricity, observe proper grounding during fabrication process and take due care when carrying and storage it.

(10) The IC destruction caused by capacitive load
The transistors in circuits may be damaged when VCC terminal and VEE terminal is shorted with the charged output terminal capacitor. When IC is used as a comparator or as an application circuit, where oscillation is not activated by an output capacitor, the output capacitor must be kept below 10[μF] in order to prevent the damage mentioned above.

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