


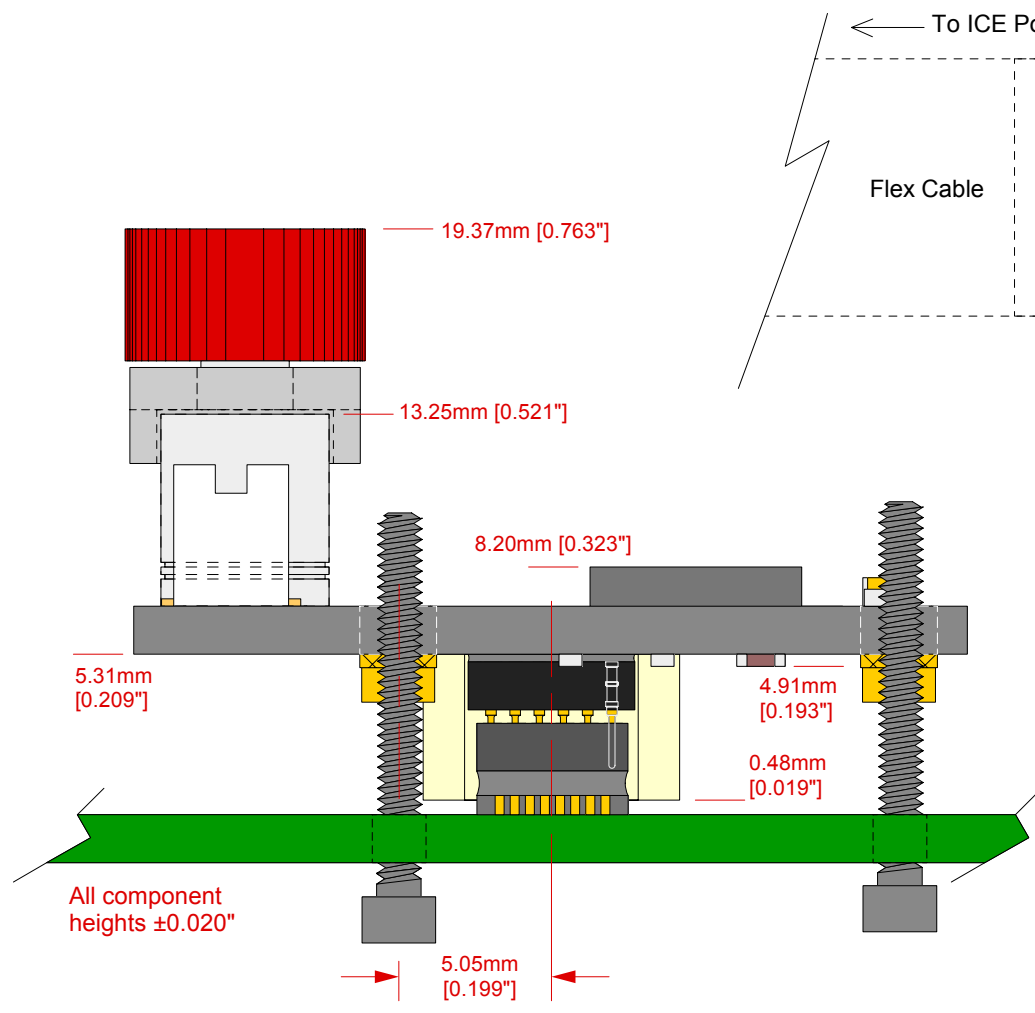
Description: CY3250-20434QFN POD
 48 pin QFN OCD with Hirose cable connector (P/N: DF12-5.0-20DP-0.5V-81) with retention mechanism included.
 Compatible with:
 SF-QFN32A-L-02, QFN foot, 32 pin, 0.5mm pitch, 5mm body
 XT-PSoC-15-20H-10-01

	EA-8C20000-Q32-01 Drawing		Status: Preliminary	Scale: 3:1	Rev: B
	© 2006 IRONWOOD ELECTRONICS, INC. PO BOX21151 ST. PAUL, MN 55121 Tele: (651) 452-8100 www.ironwoodelectronics.com		Drawing: M. Tully	Date: 4/11/06	
			File: EA-8C20000-Q32-01 Dwg	Modified: 1/11/07, MT	

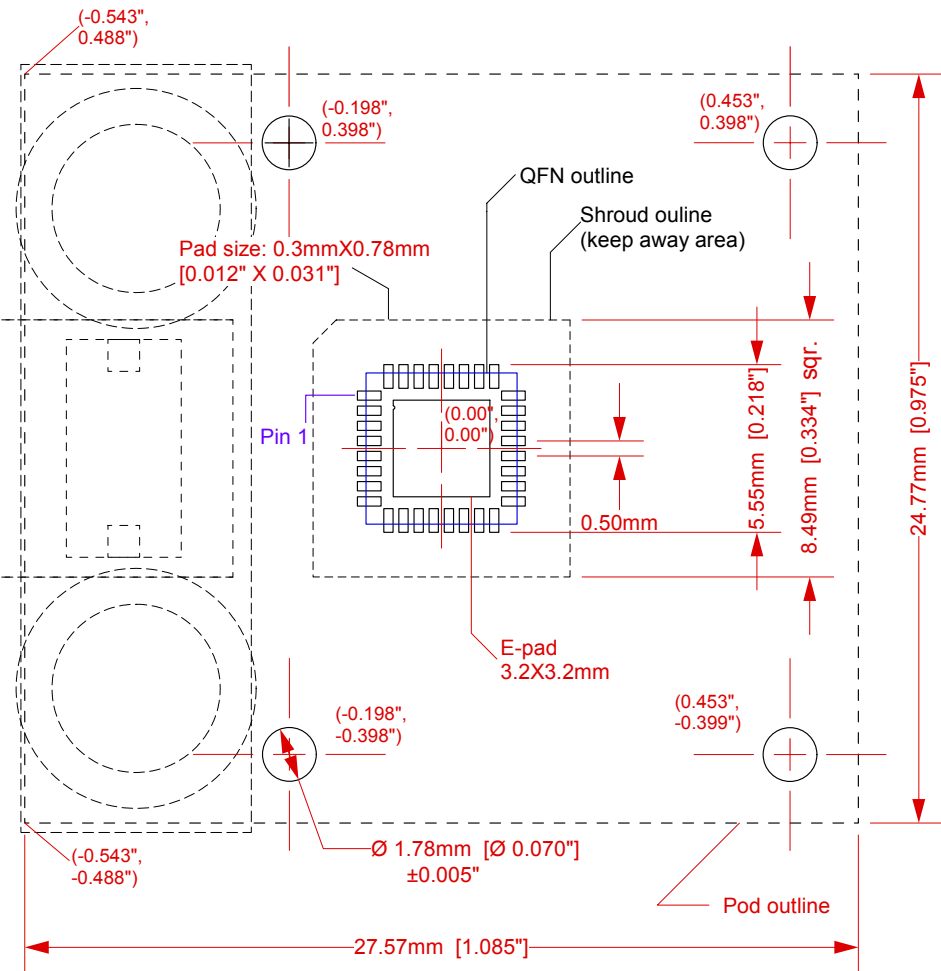
Tolerances: diameters $\pm 0.03\text{mm}$ [$\pm 0.001"$],
 PCB perimeters $\pm 0.13\text{mm}$ [$\pm 0.005"$],
 PCB thicknesses $\pm 0.18\text{mm}$ [$\pm 0.007"$],
 pitches (from true position) $\pm 0.08\text{mm}$ [$\pm 0.003"$],
 all other tolerances $\pm 0.13\text{mm}$ [$\pm 0.005"$]
 unless stated otherwise. Materials and specifications are subject to change without notice.

Strain Relief Hardware mounting directions

If the target board can be designed to accommodate the strain relief hardware, layout the PCB with the pattern shown on the right. The layout shows the location of non-plated holes through the board relative to the QFN land pattern. If holes can not be added to the target board, the screws can be used as standoffs and sit on the top surface of the target PCB.




Side view: CY3250-20434QFN Pod on QFN32 foot



Top View: Target Board component layout

notes:

- 1) Do not place components within the shroud outline unless they have a 0.035" height or less off the target board and are outside the QFN package outline
- 2) All components under the POD outline must have a 0.190" height or less from the top of target board to avoid components on the bottom of the POD board.

	EA-8C2000-Q32-01 Drawing © 2006 IRONWOOD ELECTRONICS, INC. PO BOX21151 ST. PAUL, MN 55121 Tele: (651) 452-8100 www.ironwoodelectronics.com	Status: Preliminary Drawing: M. Tully File: EA-8C2000-Q32-01 Dwg	Scale: 4:1 Date: 4/11/06 Modified: 1/11/07, MT	Rev: B
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Tolerances: diameters ±0.03mm [±0.001"], PCB perimeters ±0.13mm [±0.005"], PCB thicknesses ±0.18mm [±0.007"], pitches (from true position) ±0.08mm [±0.003"], all other tolerances ±0.13mm [±0.005"] unless stated otherwise. Materials and specifications are subject to change without notice.