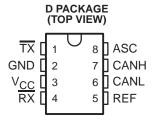
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- SN75LBC031 Meets Standard ISO/DIS 11898 (up to 500 k Baud)
- Driver Output Capability at 50 mA
- Wide Positive and Negative Input/output **Bus Voltage Range**
- **Bus Outputs Short-Circuit-Protected to Battery Voltage and Ground**
- **Thermal Shutdown**
- **Available in Q-Temp Automotive** 
  - HighRel Automotive Applications
  - Configuration Control/Print Support
  - Qualification to Automotive Standards

### description

The SN75LBC031 is a CAN transceiver used as an interface between a CAN controller and the physical bus for high speed applications of up to 500 kBaud. The device provides transmit capability to the differential bus and differential receive capability to the controller. The transmitter outputs (CANH and CANL), feature internal transition regulation to provide controlled symmetry resulting in low EMI emissions. Both



#### **TERMINAL FUNCTIONS**

TERMINAL	DESCRIPTION					
TX	Transmitter input					
GND	Ground					
VCC	Supply voltage					
RX	Receiver output					
REF	Reference output					
CANL	Low side bus output driver					
CANH	High side bus output driver					
ASC	Adjustable slope control					

#### **FUNCTION TABLE**

TX	CANH	CANL	BUS STATE	RX
L	Н	L	Dominant	L
High or floating	Floating	Floating	Recessive	Н

transmitter outputs are fully protected against battery short circuits and electrical transients that can occur on the bus lines. In the event of excessive device power dissipation the output drivers are disabled by the thermal shutdown circuitry at a junction temperature of approximately 160°C. The inclusion of an internal pullup resistor on the transmitter input ensures a defined output during power up and protocol controller reset. For normal operation at 500 kBaud the ASC terminal is open or tied to GND. For slower speed operation at 125 kBaud the bus output transition times can be increased to reduce EMI by connecting the ASC terminal to V<sub>CC</sub>. The receiver includes an integrated filter that suppresses the signal into pulses less than 30 ns wide.

The SN75LBC031 is characterized for operation from -40°C to 85°C. The SN65LBC031 is characterized for operation from -40°C to 125°C. The SN65LBC031Q is characterized for operation over the automotive temperature range of -40°C to 125°C.

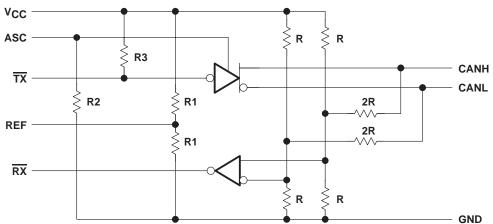


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## logic diagram



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### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Logic supply voltage, V <sub>CC</sub> (see Note 1)	7 V
Bus terminal voltage	
Input current at TX and ASC terminal, I <sub>1</sub>	±10 mA
Input voltage at TX and ASC terminal, V <sub>I</sub>	2 × V <sub>CC</sub>
Operating free-air temperature range, T <sub>A</sub> : SN65LBC031, SN65LBC031Q	–40°C to125°C
SN75LBC031	–40°C to 85°C
Operating juncation range, T <sub>J</sub>	–40°C to 150°C
Continuous total power dissipation at (or below) 25°C free-air temperature So	ee Dissipation Rating Table
Storage temperature range, T <sub>stq</sub>	–65°C to 150°C
Case temperature for 10 sec T <sub>C</sub> , D package	260°C

<sup>†</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: All voltage values, except differential bus voltage, are measured with respect to GND.

### **DISSIPATION RATING TABLE**

$\begin{array}{cc} & & & T_{\mbox{\scriptsize A}} \leq 25^{\circ}\mbox{\scriptsize C} \\ & & \mbox{\scriptsize POWER RATING} \end{array}$		OPERATING FACTOR ABOVE T <sub>C</sub> = 25°C	T <sub>C</sub> = 125°C POWER RATING
D	725 mW	5.8 mW/°C	145 mW

### **DISSIPATION DERATING CURVE**

### vs

### FREE-AIR TEMPERATURE

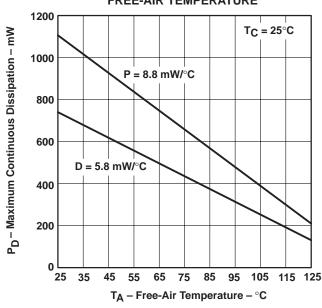


Figure 1

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### recommended operating conditions

		MIN	NOM	MAX	UNIT
Logic supply voltage, V <sub>CC</sub>	4.5	5	5.5	V	
Voltage at any bus terminal (separate	ly or common mode), V <sub>I</sub> or V <sub>IC</sub> (see Note 3)	-2		7	V
High-level input voltage, VIH	TX	2		VCC	V
Low-level input voltage, V <sub>IL</sub>	TX	0		0.8	V
High level output current leve	Transmitter			-50	mA
High-level output current, IOH	Receiver			-400	μΑ
Low lovel output ourrent lov	Transmitter			50	A
Low-level output current, IOL	Receiver			1	mA
Operating free air temperature T.	SN75LBC031	-40		85	°C
Operating free-air temperature, T <sub>A</sub>	SN65LBC031, SN65LBC031Q	-40		125	-0

NOTES: 2. All voltage values, except differential bus voltage, are measured with respect to the ground terminal.

3. For bus voltages from -5 V to -2 V and 7 V to 20 V the receiver output is stable.

### **SYMBOL DEFINITION**

DATA SHEET PARAMETER	DEFINITION
VO(CANHR)	CANH bus output voltage (recessive state)
VO(CANLR)	CANL bus output voltage (recessive state)
VO(CANHD)	CANH bus output voltage (dominant state)
VO(CANLD)	CANL bus output voltage (dominant state)
VO(DIFFR)	Bus differential output voltage (recessive state)
V <sub>O(DIFFD)</sub>	Bus differential output voltage (dominant state)
V <sub>I</sub> (ASC)	Adjustable slope control input voltage

# electrical characteristics over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted)

	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
VO(REF)	Reference source output voltage	I <sub>REF</sub> = ±20 μA	0.45 V <sub>CC</sub>		0.55 V <sub>CC</sub>	V
R <sub>O(REF)</sub>	Reference source output resistance		5		10	kΩ
ICC(REC)	Logic supply current, recessive state	See Figure 2, S1 closed		12	20	mA
ICC(DOM)	Logic supply current, dominant state	See Figure 2, ST Closed		55	80	IIIA

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## transmitter electrical characteristics over recommended ranges of supply and operating free-air temperature (unless otherwise noted)

	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
VO(CANHR) VO(CANLR)	Output voltage (recessive state)	See Figure 2, S1 open	2	0.5V <sub>CC</sub>	3	V
V <sub>O(DIFFR)</sub>	Differential output voltage (recessive state)		-500	0	50	mV
VO(CANHD)	Output voltage (dominant state)		2.75	3.5	4.5	
VO(CANLD)	Output voltage (dominant state)	See Figure 2, S1 closed	0.5	1.5	2.25	V
VO(DIFFD)	Differential output voltage (dominant state)		1.5	2	3	
lu con o	High-level input current (TX)	V <sub>IH</sub> = 2.4 V		-100	-185	^
I <sub>IH</sub> (TX)	righ-lever input current (1%)	VIH = VCC			±2	μΑ
lu va ees	High-level input current (ASC)	V <sub>IH</sub> = 2.4 V		100	165	μΑ
IH(ASC)	riigh-leverinput current (ASC)	V <sub>IH</sub> = V <sub>CC</sub>		200	340	μΑ
I <sub>IL(TX)</sub>	Low-level input current $(\overline{TX})$	V <sub>IL</sub> = 0.4 V		-180	-400	μΑ
IL(ASC)	Low-level input current (ASC)	V <sub>IL</sub> = 0.4 V		15	25	μΑ
C <sub>I(TX)</sub>	TX input capacitance			8		pF
I <sub>O(ssH)</sub>	CANH short circuit output current	$V_{O(CANH)} = -2 \text{ V to } 20 \text{ V}$		-95	-200	mA
I <sub>O(ssL)</sub>	CANL short circuit output current	$V_{O(CANL)} = 20 \text{ V to } -2 \text{ V}$		140	250	mA

NOTE 2: All voltage values, except differential bus voltage, are measured with respect to the ground terminal.

## transceiver dynamic characteristics over recommended operating free-air temperature range and $V_{CC}$ = 5 V

	PARAMETER	TEST	CONDITIONS	MIN	TYP	MAX	UNIT
	Loop time	See Figures 2 and 3, S1 closed,	VI(ASC) = 0 V or open circuit, S2 open			280	ns
<sup>t</sup> (loop)	Loop time	See Figures 2 and 3, S1 closed,	VI(ASC) = V <sub>CC</sub> , S2 closed			400	ns
CD (==)	Differential-output slew rate	See Figures 2 and 4, S1 closed,	V <sub>I</sub> (ASC) = 0 or open circuit, S2 open		35		V/μs
SR <sub>(RD)</sub>	(recessive to dominant)	See Figures 2 and 4, S1 closed,	VI(ASC) = VCC, S2 closed	10			V/μs
CD	Differential-output slew rate (dominant to recessive)	See Figures 2 and 4, S1 closed,	V <sub>I</sub> (ASC) = 0 or open circuit, S2 open		10		V/μs
SR <sub>(DR)</sub>		See Figures 2 and 4, S1 closed,	VI(ASC) = VCC, S2 closed		10		V/μs
t <sub>d</sub> (RD)	Differential output delay time	San Figure 2	S1 closed		55		ns
td(DR)	Differential-output delay time	See Figure 2,	ST Closed		160		ns
tpd(RECRD)	Receiver propagation delay	See Figures 2 and 5			90		ns
tpd(RECDR)	time	See Figures 2 and 5			55		ns

NOTE 4: Receiver input pulse width should be >50 ns. Input pulses of <30 ns are suppressed.

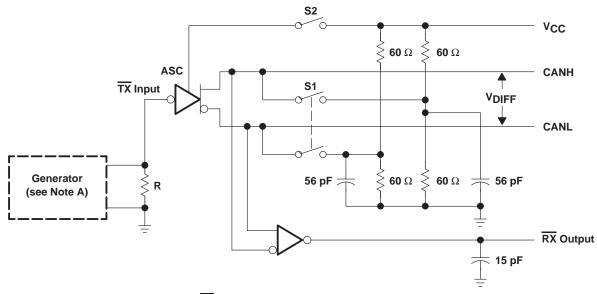
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## receiver electrical characteristics over recommended ranges of common-mode input voltage, supply voltage, and operating free-air temperature (unless otherwise noted)

	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
VIT(REC)	Differential input threshold voltage for recessive state	V <sub>IC</sub> = -2 V to 7 V			500	mV
VIT(DOM)	Differential input threshold voltage for dominant state	VIC = -2 V 10 7 V	900			IIIV
V <sub>hys</sub>	Recessive-dominant input hysteresis		100	180		mV
VOH(RX)	High-level output voltage	$V_{O(DIFF)} = 500 \text{ mV},$ $I_{OH} = -400 \mu\text{A}$	V <sub>CC</sub> -0.5 V		VCC	V
V <sub>OL(RX)</sub>	Low-level output voltage	V <sub>O(DIFF)</sub> = 900 mV, I <sub>OL</sub> = 1 mA	0		0.5	V
rI(REC)	CANH and CANL input resistance in recessive state	dc, no load	5		50	kΩ
rI(DIFF)	Differential CANH and CANL input resistance in recessive state	dc, no load	10		100	kΩ
Ci	CANH and CANL input capacitance			20		pF
C <sub>i(DHL)</sub>	Differential CANH and CANL input capacitance			10	, and the second	pF

NOTE 2: All voltage values, except differential bus voltage, are measured with respect to the ground terminal.

### PARAMETER MEASUREMENT INFORMATION



NOTE A: The input pulse is supplied to  $\overline{TX}$  by a generator having a  $t_f$  and  $t_f = 5$  ns.

Figure 2. Test Circuit

### PARAMETER MEASUREMENT INFORMATION

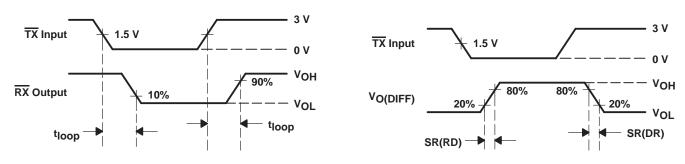
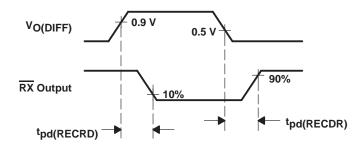


Figure 3. Loop Time

Figure 4. Slew Rate

NOTE A: The input pulse is supplied to  $\overline{TX}$  by a generator having a  $t_{\Gamma}$  and  $t_{f} = 5$  ns.



NOTE A: The input pulse is supplied as  $V_{DIFF}$  using CANH and CANL respectively by a generator having a  $t_r$  and  $t_f = 5$  ns.

Figure 5. Receiver Delay Times

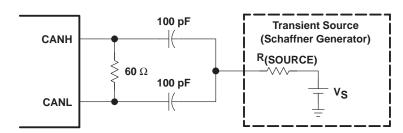


Figure 6. Transient Stress Capability Test Circuit

### PARAMETER MEASUREMENT INFORMATION

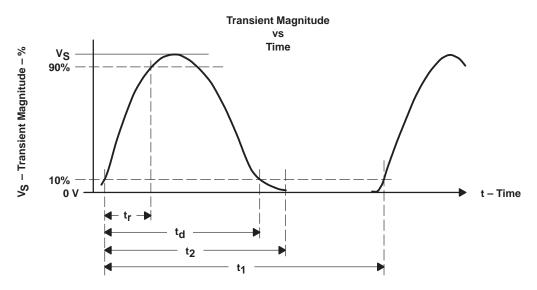


Figure 7. Transient Stress Capability Waveform

**Table 1. Test Circuit Results According to DIN 40839** 

TEST PULSE	TRANSIENT MAGNITUDE VS	SOURCE IMPEDANCE RSOURCE	PULSE WIDTH t <sub>d</sub> (see Note 5)	PULSE RISE TIME, t <sub>r</sub> (see Note 6)	PULSE TIME, t <sub>2</sub> (see Figure 7)	REPETITION PERIOD, t <sub>1</sub> (see Figure 7)	NUMBER OF PULSES
1	–100 V	10 Ω	2 ms	1 μs	200 ms	5 s	5000
2	100 V	10 Ω	50 μs	1 μs	200 ms	5 s	5000
3a	−150 V	50 Ω	0.1 μs	5 ns	100 μs	100 μs	See Note 7
3b	100 V	50 Ω	0.1 μs	5 ns	100 μs	100 μs	See Note 7
5	60 V	1 Ω	400 ms	5 ms	_	_	1

NOTES: 5. Measured from 10% on rising edge to 10% on falling edge

- 6. Measured from 10% to 90% of pulse
- 7. Pulse package for a period of 3600 s, 10 ms pulse time, 90 ms stop time

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### **APPLICATION INFORMATION** 5 V 💳 100 nF 3 120 $\Omega$ $\lesssim$ 10 k $\Omega$ 10 kΩ § 8 **VCC VCC** 8 ASC TL7705B **CANH** 7 SENSE SN75LBC031 RESIN RESET GND CANL REF **GND** $c_{\text{in}}$ TX REF RX 120 $\Omega$ 5 0.1 μF4 **CAN Microcontroller**

Figure 8. Typical SN75LBC031 Application

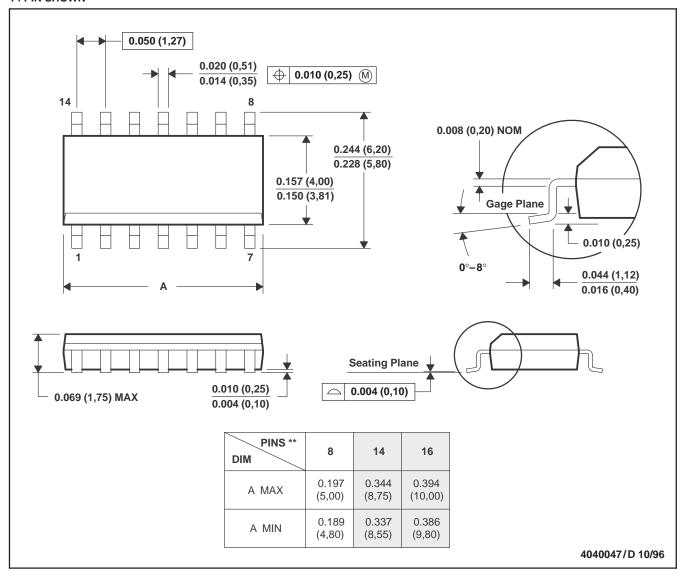
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### **MECHANICAL DATA**

### D (R-PDSO-G\*\*)

#### 14 PIN SHOWN

### PLASTIC SMALL-OUTLINE PACKAGE



NOTES: A. All linear dimensions are in inches (millimeters).

B. This drawing is subject to change without notice.

C. Body dimensions do not include mold flash or protrusion, not to exceed 0.006 (0,15).

D. Falls within JEDEC MS-012





i.com 21-Apr-2008

### PACKAGING INFORMATION

Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins	Package Qty	Eco Plan <sup>(2)</sup>	Lead/Ball Finish	MSL Peak Temp <sup>(3)</sup>
SN65LBC031D	ACTIVE	SOIC	D	8	75	TBD	CU NIPDAU	Level-1-220C-UNLIM
SN65LBC031DG4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN65LBC031DRG4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN65LBC031P	OBSOLETE	PDIP	Р	8		TBD	Call TI	Call TI
SN75LBC031D	ACTIVE	SOIC	D	8	75	TBD	CU NIPDAU	Level-1-220C-UNLIM
SN75LBC031DR	ACTIVE	SOIC	D	8	2500	TBD	CU NIPDAU	Level-1-220C-UNLIM
SN75LBC031P	OBSOLETE	PDIP	Р	8	•	TBD	Call TI	Call TI

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LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

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(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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## D (R-PDSO-G8)

## PLASTIC SMALL-OUTLINE PACKAGE



NOTES:

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- B. This drawing is subject to change without notice.
- Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed .006 (0,15) per end.
- Body width does not include interlead flash. Interlead flash shall not exceed .017 (0,43) per side.
- E. Reference JEDEC MS-012 variation AA.



### P (R-PDIP-T8)

### PLASTIC DUAL-IN-LINE



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- B. This drawing is subject to change without notice.
- C. Falls within JEDEC MS-001

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