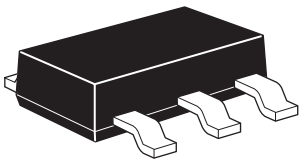
ZXMP7A17G
70V P-channel enhancement mode MOSFET

Summary



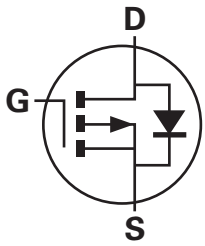
V_{DSS}=70V : $R_{DS(on)}$=0.16Ω

I_D=3.7A

Description

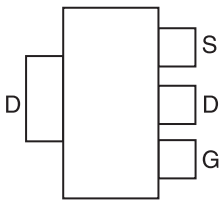
This new generation of trench MOSFETs from Zetex utilizes a unique structure that combines the benefits of low on-resistance with fast switching speed. This makes them ideal for high efficiency, low voltage power management applications.

Features



- Low on-resistance
- Fast switching speed
- Low threshold
- Low gate drive
- SOT223 package

Applications



- DC-DC converters
- Power management functions
- Disconnect switches
- Motor control
- Class D audio output stages

Ordering information

Device	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXMP7A17GTA	7	12	1,000
ZXMP7A17GTC	13	12	4,000

Device marking

ZXMP
7A17

Absolute maximum ratings

Parameter	Symbol	Limit	Unit
Drain-source voltage	V_{DSS}	-70	V
Gate-source voltage	V_{GS}	±20	V
Continuous drain current @ V_{GS}=10V; T_A=25°C (b)	I_D	-3.7	A
@ V_{GS}=10V; T_A=25°C (b)		-2.9	
@ V_{GS}=10V; T_A=25°C (a)		-2.6	
Pulsed drain current (c)	I_{DM}	-9.6	A
Continuous source current (body diode) (b)	I_S	-4.8	A
Pulsed source current (body diode) (c)	I_{SM}	-9.6	A
Power dissipation at T_A =25°C (a) Linear derating factor	P_D	2 16	W mW/°C
Power dissipation at T_A =25°C (b) Linear derating factor	P_D	3.9 31	W mW/°C
Operating and storage temperature range	T_j, T_{stg}	-55 to +150	°C

Thermal resistance

Parameter	Symbol	Limit	Unit
Junction to ambient (a)	$R_{\Theta JA}$	62.5	°C/W
Junction to ambient (b)	$R_{\Theta JA}$	32	°C/W

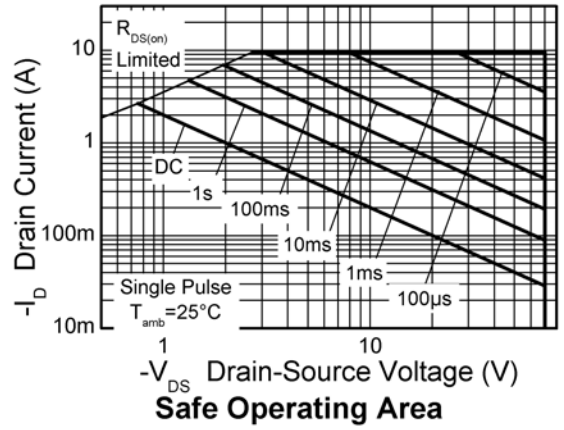
NOTES:

(a) For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.

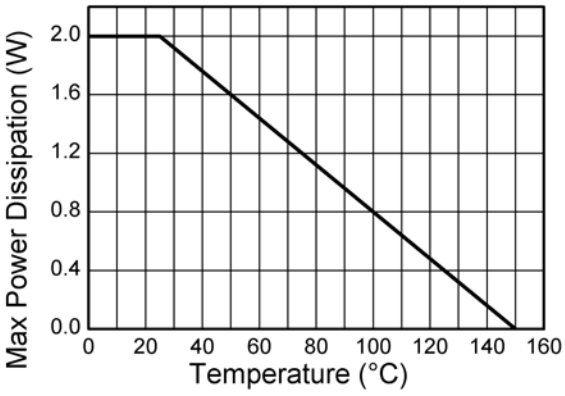
(b) For a device surface mounted on FR4 PCB measured at t ≤5 sec.

(c) Repetitive rating 25mm x 25mm FR4 PCB, D=0.05 pulse width=10μs - pulse width limited by maximum junction temperature.

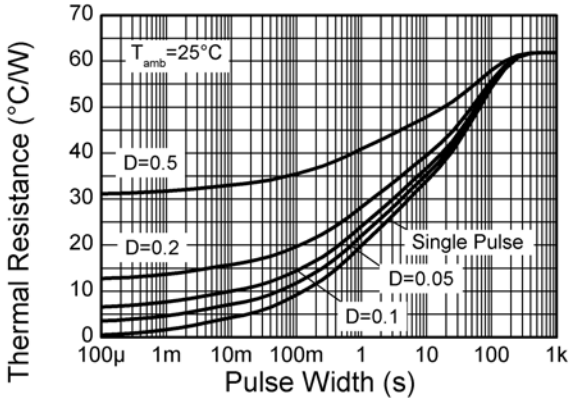
Characteristics



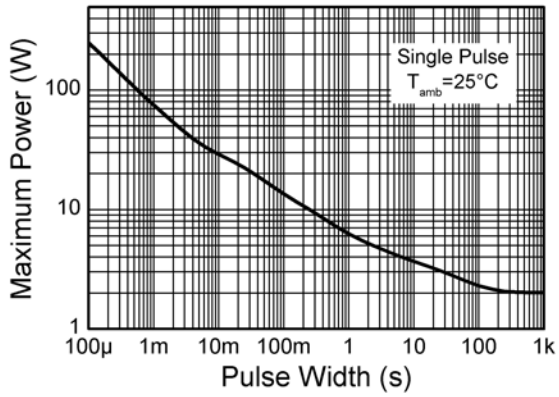
Safe Operating Area



Derating Curve



Transient Thermal Impedance



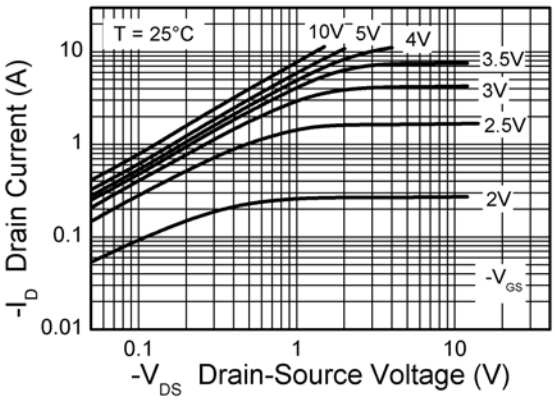
Pulse Power Dissipation

ELECTRICAL CHARACTERISTICS (at Tamb = 25°C unless otherwise stated)

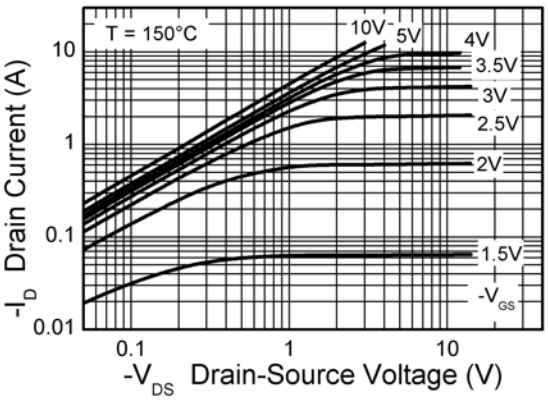
Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Static						
Drain-source breakdown voltage	$V_{(BR)DSS}$	-70			V	I_D= -250μA, V_{GS}=0V
Zero gate voltage drain current	I_{DSS}			-1	μA	V_{DS}= -70V, V_{GS}=0V
Gate-body leakage	I_{GSS}			100	nA	V_{GS}=±20V, V_{DS}=0V
Gate-source threshold voltage	$V_{GS(th)}$	-1.0			V	I_D= -250μA, V_{DS}=V_{GS}
Static drain-source on-state resistance (*)	$R_{DS(on)}$			0.16	Ω	V_{GS}= -10V, I_D= -2.1A
				0.25	Ω	V_{GS}= -4.5V, I_D = -1.7A
Forward transconductance(*)(‡)	g_{fs}		4.4		S	V_{DS}= -15V, I_D= -2.1A
Dynamic(‡)						
Input capacitance	C_{iss}		635		pF	V_{DS}= -40V, V_{GS}=0V f=1MHz
Output capacitance	C_{oss}		52		pF	
Reverse transfer capacitance	C_{rss}		42.5		pF	
Switching (†) (‡)						
Turn-on-delay time	$t_{d(on)}$		2.5		ns	V_{DD}= -35V, I_D= -1A R_G≅6.0Ω, V_{GS}= -10V
Rise time	t_r		3.4		ns	
Turn-off delay time	$t_{d(off)}$		27.9		ns	
Fall time	t_f		8		ns	
Total gate charge	Q_g		9.6		nC	V_{DS}= -35V, V_{GS}= -5V I_D= -2.1A
Total gate charge	Q_g		18		nC	V_{DS}= -35V, V_{GS}= -10V I_D= -2.1A
Gate-source charge	Q_{gs}		1.77		nC	
Gate drain charge	Q_{gd}		3.66		nC	
Source-drain diode						
Diode forward voltage(*)	V_{SD}		-0.85	-0.95	V	T_j=25°C, I_S= -2.0A, V_{GS}=0V
Reverse recovery time(‡)	t_{rr}		29.8		ns	T_j=25°C, I_S= -2.1A, di/dt=100A/μs
Reverse recovery charge(‡)	Q_{rr}		38.5		nC	

NOTES:
(*) Measured under pulsed conditions. Pulse width ≤ 300μs; duty cycle ≤ 2%.
(†) Switching characteristics are independent of operating junction temperature.
(‡) For design aid only, not subject to production testing.

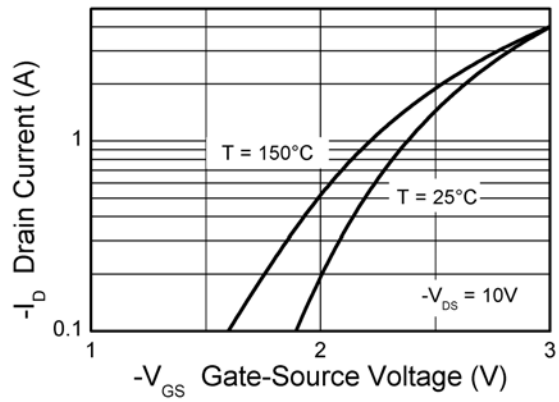
Typical characteristics



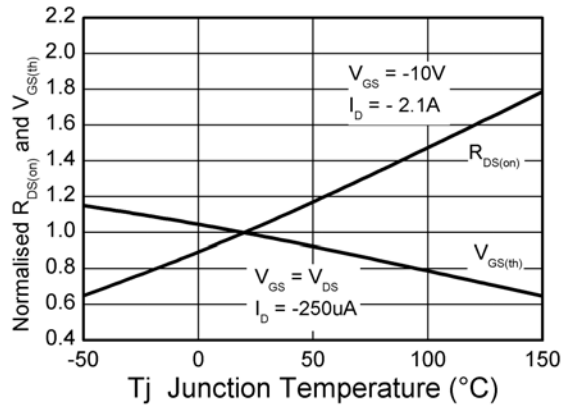
Output Characteristics



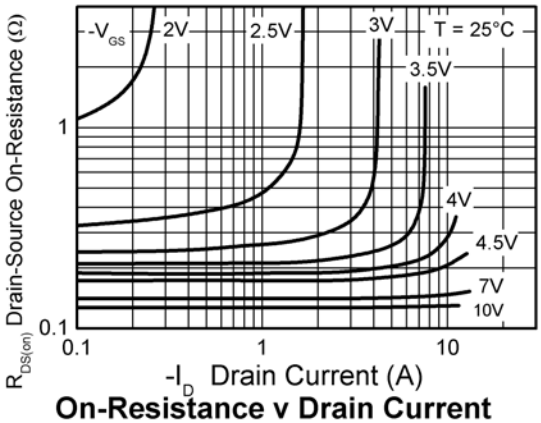
Output Characteristics



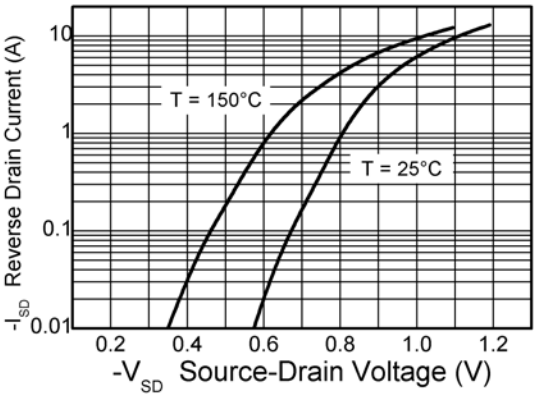
Typical Transfer Characteristics



Normalised Curves v Temperature

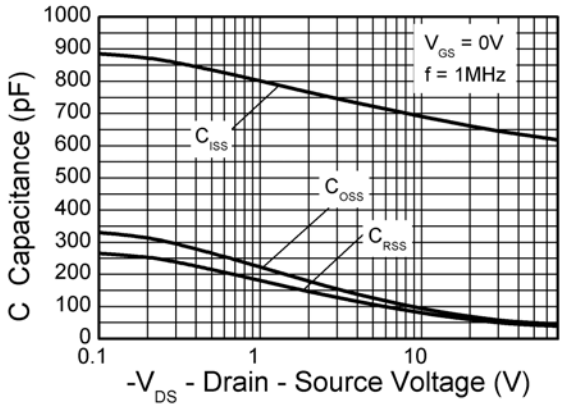


On-Resistance v Drain Current

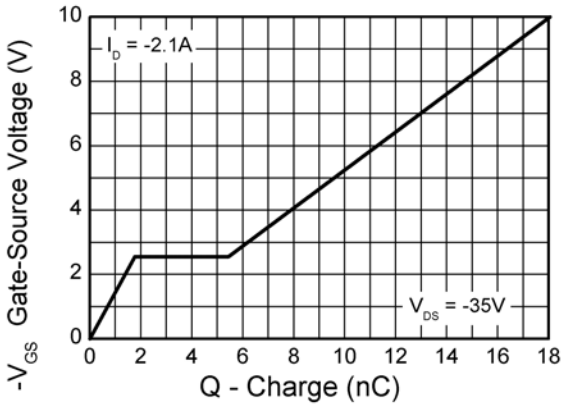


Source-Drain Diode Forward Voltage

Typical characteristics



Capacitance v Drain-Source Voltage



Gate-Source Voltage v Gate Charge

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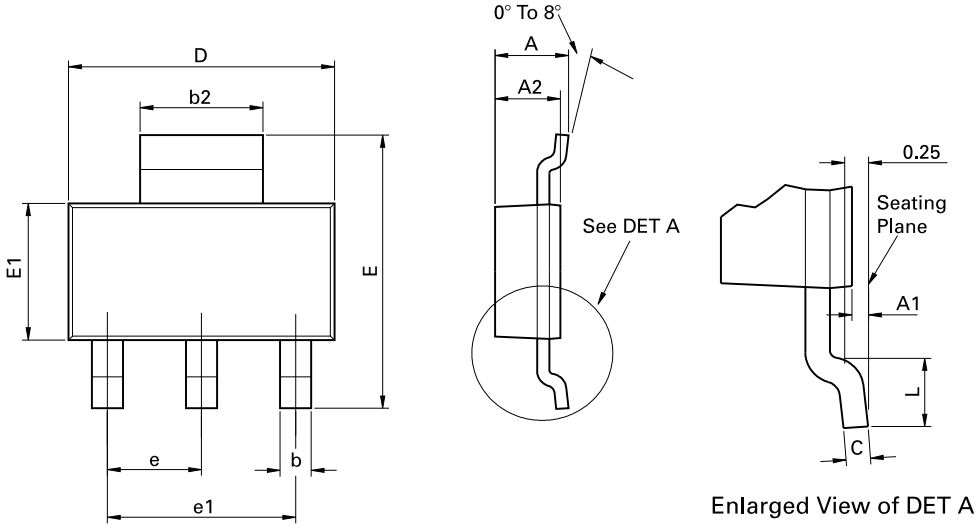
Package outline - SOT223



Conforms to JEDEC TO-261 AA Issue B

DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min	Max	Min	Max		Min	Max	Min	Max
A	-	1.80	-	0.071	e	2.30 BSC		0.0905 BSC	
A1	0.02	0.10	0.0008	0.004	e1	4.60 BSC		0.181 BSC	
b	0.66	0.84	0.026	0.033	E	6.70	7.30	0.264	0.287
b2	2.90	3.10	0.114	0.122	E1	3.30	3.70	0.130	0.146
C	0.23	0.33	0.009	0.013	L	0.90	-	0.355	-
D	6.30	6.70	0.248	0.264	-	-	-	-	-

Note: Controlling dimensions are in millimeters. Approximate dimensions are provided in inches

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