High Speed, Single Channel, Power MOSFET Driver

The EL7104 is a matched driver IC that improves the operation of the industry-standard TC-4420/29 clock drivers. The Elantec version is a very high speed driver capable of delivering peak currents of 1A into highly capacitive loads. The high speed performance is achieved by means of a proprietary “Turbo-Driver” circuit that speeds up input stages by tapping the wider voltage swing at the output. Improved speed and drive capability are enhanced by matched rise and fall delay times. These matched delays maintain the integrity of input-to-output pulse-widths to reduce timing errors and clock skew problems. This improved performance is accompanied by a 10-fold reduction in supply currents over bipolar drivers, yet without the delay time problems commonly associated with CMOS drivers.

The EL7104 is available in 8-pin SO and 8-pin PDIP packages and is specified for op

Absolute Maximum Ratings (T_A = 25°C)

Supply (V+ to GND) 16.5V
Input Pins . . . . . -0.3V to +0.3V above V+
Peak Output Current 4A
Ambient Operating Temperature . . . . . .-40°C to +85°C
Storage Temperature Range . . . . . .-65°C to +150°C
Operating Junction Temperature . . . . . .+125°C
Power Dissipation
SO 570mW
PDIP 1050mW

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

IMPORTANT NOTE: All parameters having Min/Max specifications are guaranteed. Typ values are for information purposes only. Unless otherwise noted, all tests are at the specified temperature and are pulsed tests, therefore: $T_J = T_C = T_A$

DC Electrical Specifications

V+ = 15V, T_A = 25°C unless otherwise specified.

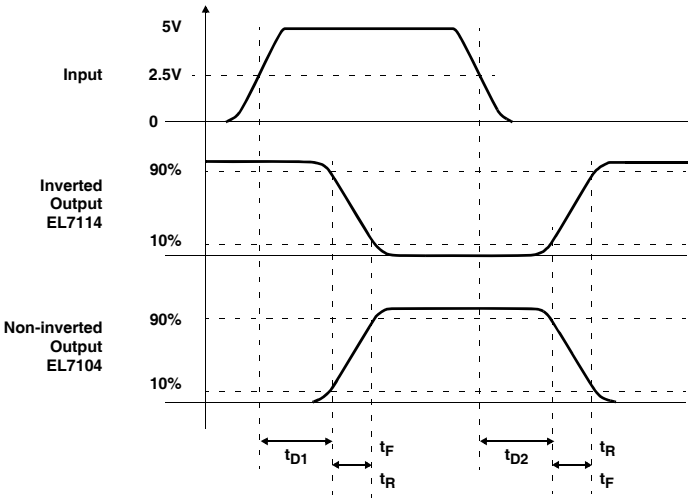
PARAMETER	DESCRIPTION	CONDITIONS	MIN	TYP	MAX	UNIT
INPUT						
V_{IH}	Logic "1" Input Voltage		2.4			V
I_{IH}	Logic "1" Input Current	@V+		0.1	10	µA
V_{IL}	Logic "0" Input Voltage				0.8	V
I_{IL}	Logic "0" Input Current	@0V		0.1	10	µA
V_{HVS}	Input Hysteresis			0.3		V
OUTPUT						
R_{OH}	Pull-Up Resistance	I_{OUT} = -100mA		1.5	4	Ω
R_{OL}	Pull-Down Resistance	I_{OUT} = +100mA		2	4	Ω
I_{OUT}	Output Leakage Current	V+/GND		0.2	10	µA
I_{PK}	Peak Output Current	Source/Sink		4.0		A
I_{DC}	Continuous Output Current	Source/Sink	200			mA
POWER SUPPLY						
I_S	Power Supply Current	Input = V+		4.5	7.5	mA
V_S	Operating Voltage		4.5		16	V

AC Electrical Specifications

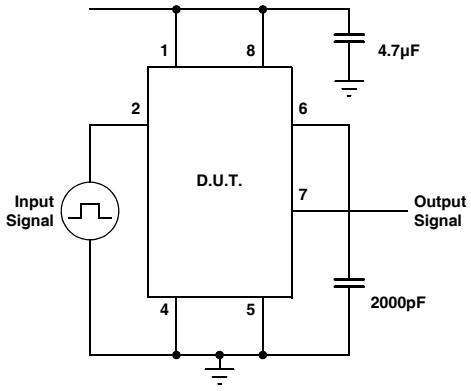
V = 15V, T_A = 25°C unless otherwise specified.

PARAMETER	DESCRIPTION	CONDITIONS	MIN	TYP	MAX	UNIT
SWITCHING CHARACTERISTICS (V_{DD} = V_H = 12V; V_L = -3V)						
t_R	Rise Time	C_L = 1000pF		7.5		ns
		C_L = 2000pF		10	20	ns
t_F	Fall Time	C_L = 1000pF		10		ns
		C_L = 2000pF		15	20	ns
t_{D-ON}	Turn-On Delay Time	See Timing Table		18	25	ns
t_{D-OFF}	Turn-Off Delay Time	See Timing Table		18	25	ns

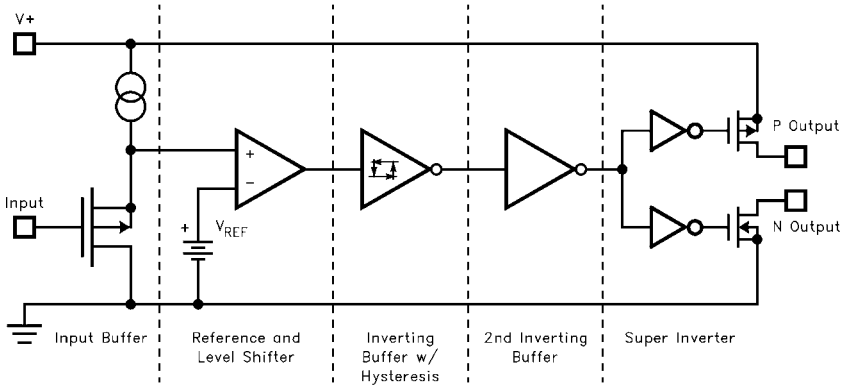
Timing Table



Standard Test Configuration

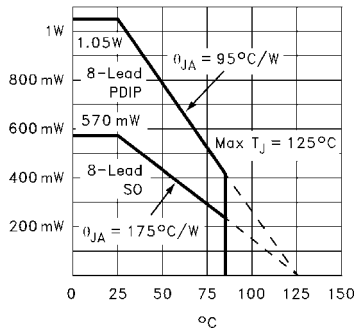


Simplified Schematic

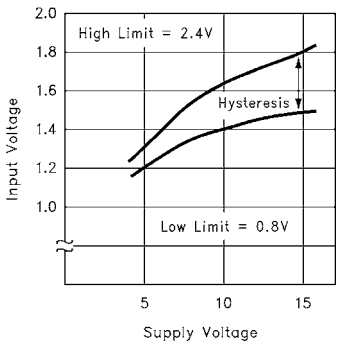


Typical Performance Curves

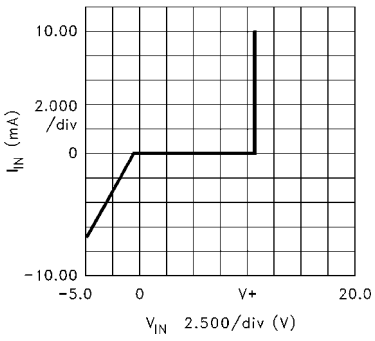
MAX POWER/DERATING CURVES



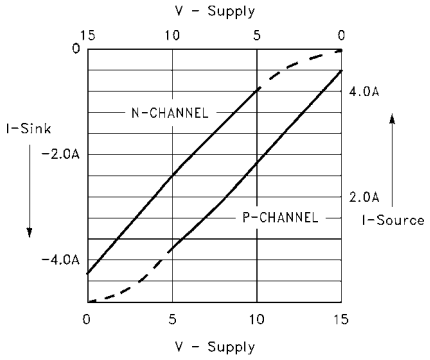
SWITCH THRESHOLD vs SUPPLY VOLTAGE



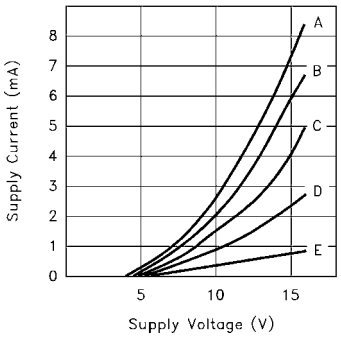
INPUT CURRENT vs VOLTAGE



PEAK DRIVE vs SUPPLY VOLTAGE



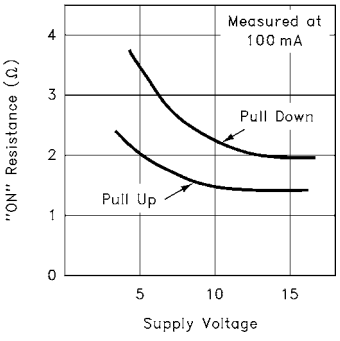
QUIESCENT SUPPLY CURRENT



CASE:

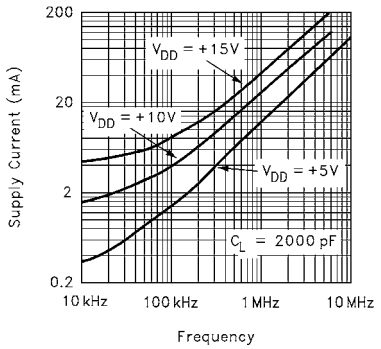
Device	Input Level	Curve
EL7104	GND	A
EL7104	V+	C
EL7114	GND	C
EL7114	V+	E

"ON" RESISTANCE vs SUPPLY VOLTAGE

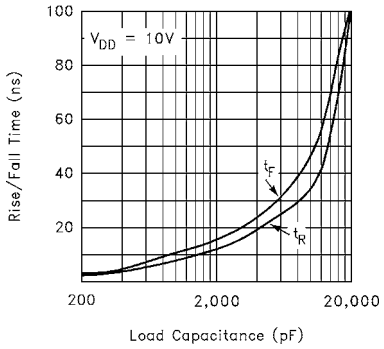


Typical Performance Curves (Continued)

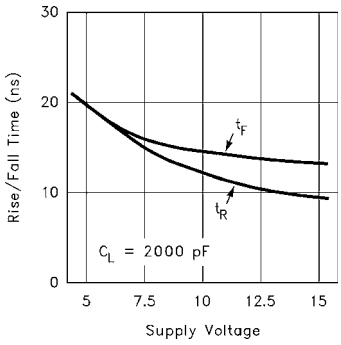
AVERAGE SUPPLY CURRENT vs VOLTAGE AND FREQUENCY



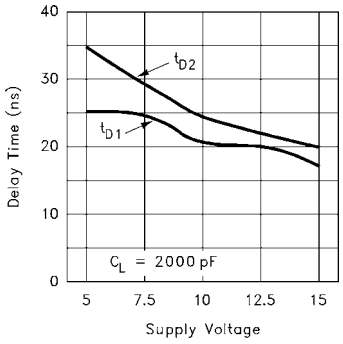
RISE/FALL TIME vs LOAD



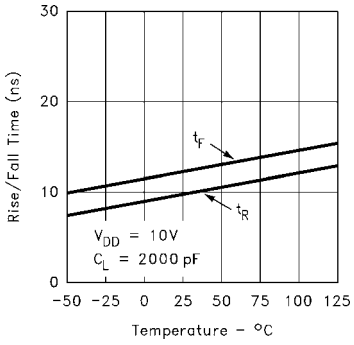
RISE/FALL TIME vs SUPPLY VOLTAGE



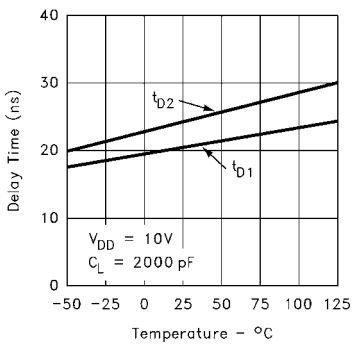
PROPAGATION DELAY vs SUPPLY VOLTAGE



RISE/FALL TIME vs TEMPERATURE



RISE/FALL TIME vs TEMPERATURE



Plastic Dual-In-Line Packages (PDIP)

MDP0031

PLASTIC DUAL-IN-LINE PACKAGE

SYMBOL	PDIP8	PDIP14	PDIP16	PDIP18	PDIP20	TOLERANCE	NOTES
A	0.210	0.210	0.210	0.210	0.210	MAX	
A1	0.015	0.015	0.015	0.015	0.015	MIN	
A2	0.130	0.130	0.130	0.130	0.130	±0.005	
b	0.018	0.018	0.018	0.018	0.018	±0.002	
b2	0.060	0.060	0.060	0.060	0.060	+0.010/-0.015	
c	0.010	0.010	0.010	0.010	0.010	+0.004/-0.002	
D	0.375	0.750	0.750	0.890	1.020	±0.010	1
E	0.310	0.310	0.310	0.310	0.310	+0.015/-0.010	
E1	0.250	0.250	0.250	0.250	0.250	±0.005	2
e	0.100	0.100	0.100	0.100	0.100	Basic	
eA	0.300	0.300	0.300	0.300	0.300	Basic	
eB	0.345	0.345	0.345	0.345	0.345	±0.025	
L	0.125	0.125	0.125	0.125	0.125	±0.010	
N	8	14	16	18	20	Reference	

Rev. B 2/99

NOTES:

1. Plastic or metal protrusions of 0.010” maximum per side are not included.
2. Plastic interlead protrusions of 0.010” maximum per side are not included.
3. Dimensions E and eA are measured with the leads

D mensio eB ise m(asur)-467(ed w)Tf1 465382 0