

SPEC	IFICATIONS	
CUSTOMER	: OKAYA	
SAMPLE CODE (Ver.)	:	
MASS PRODUCTION CODE (Ver.)	PC0802LF	RS-AWA-B-Q
DRAWING NO. (Ver.)	PC 95016(Ve	r.A)
	Da	ate:
Approved QC	Da Confirmed	ate: Designer
Approved QC		
Approved QC	Confirmed	
For A	Confirmed	
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RECORDS OF REVISION

Date	Rev.	Description	Note	Page
2005/07/26	0	NEW SAMPLE		

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Note : For detailed information please refer to IC data sheet : <u>ST7066U</u>

POWERTIP

1. SPECIFICATIONS

1.1 Features

Item	Standard Value
Display Type	8*2 Characters
LCD Type	STN Gray Positive Transflective Normal Temp.
Driver Condition	LCD Module: 1/16 Duty, 1/4 Bias
Viewing Direction	12O'clock
Backlight	YG LED B/L
Weight	22 g
Interface	-
Other	_

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	58.0(L) * 32.0(w) * 14.0mm(H)(Max)	mm
Viewing Area	38.0(L) * 16.0(w)	mm
Active Area	27.81(L) * 11.5 (w)	mm
Dot Size	0.56(L) * 0.66(w)	mm
Dot Pitch	0.60(L) * 0.70(w)	mm

Note : For detailed information please refer to LCM drawing

1.3 Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
Power Supply Voltage	V _{DD}	_	-0.3	7.0	V
LCD Driver Supply Voltage	V _{LCD}	_	VDD-10.0	VDD+0.3	V
Input Voltage	V_{IN}	_	-0.3	V _{DD} +0.3	V
Operating Temperature	T _{OP}	Excluded B/L	0	50	°C
Storage Temperature	T _{ST}	Excluded B/L	-20	70	°C
Storage Humidity	H _D	Ta<40 °C	-	90	%RH



1.4 DC Electrical Characteristics

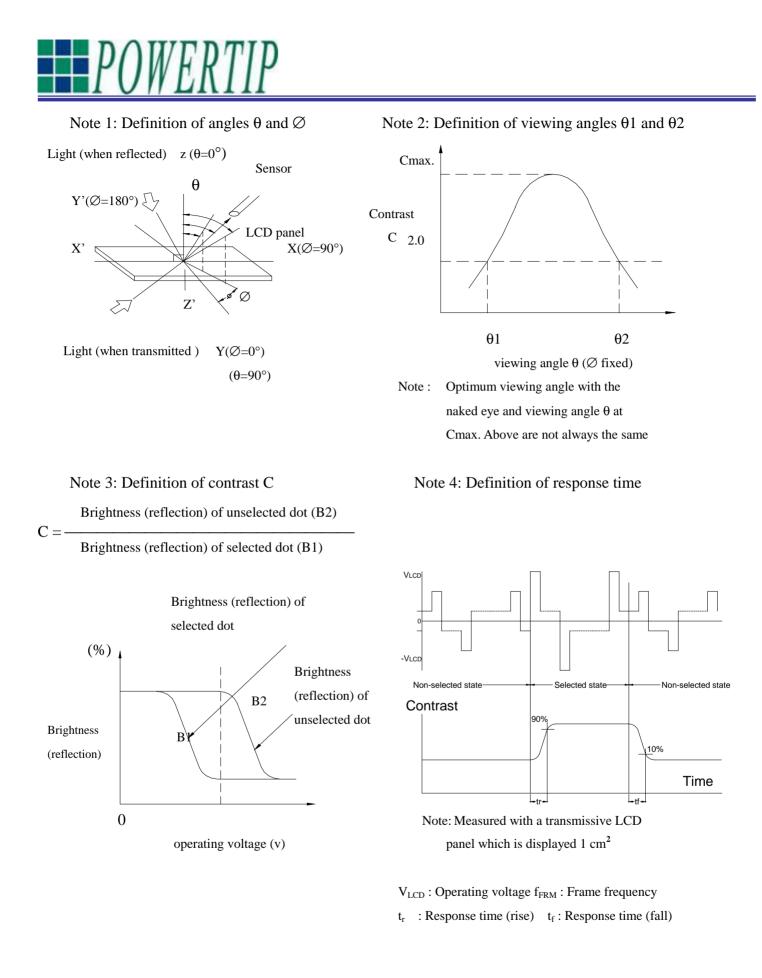
$V_{DD}\!=5.0~V\pm10\%$, $V_{SS}\!=0V$, $Ta\!=\!25^\circ\!C$						
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Logic Supply Voltage	V _{DD}	_	4.5	5.0	5.5	V
"H" Input Voltage	V _{IH}	—	0.7 Vdd	-	Vdd	V
"L" Input Voltage	V _{IL}	—	-0.3	-	0.6	V
"H" Output Voltage	V _{OH}	IOH=-0.1mA	3.9	-	Vdd	V
"L" Output Voltage	V _{OL}	IOL=0.1mA	-	-	0.4	V
Supply Current	I _{DD}	$V_{DD} = 5.0 \text{ V}$	-	1.3	3.0	mA
		0°C	-	-	-	
LCM Driver Voltage	V _{OP}	25°C *1	4.1	4.3	4.5	V
		50°C	-	-	-	

Note.1*The Vop test point is V_{DD} -Vo.

1.5 Optical Characteristics

LCD Panel : 1/16 Duty , 1/4 Bias , V_{LCD} =4.4 V , Ta = 25 °C

Item	Symbol	Conditions	Min.	Тур.	Max.	Reference
View Angle	θ	C \geq 2.0, Ø = 0°	40°	-	-	Notes 1 & 2
Contrast Ratio	С	$\theta = 5^{\circ}, \emptyset = 0^{\circ}$	5	7	-	Note 3
Response Time(rise)	tr	$\theta = 5^{\circ}, \ \emptyset = 0^{\circ}$	-	150 ms	-	Note 4
Response Time(fall)	tf	$\theta = 5^{\circ}, \ \emptyset = 0^{\circ}$	-	330 ms	-	Note 4





1.6 Backlight Characteristics

LCD Module with LED Backlight

Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
Forward Current	IF	Ta =25°C	-	120	mA
Reverse Voltage	VR	Ta =25°C	-	8	V
Power Dissipation	РО	Ta =25°C	-	0.48	W
Operating Temperature	T _{OP}	-	-20	70	°C
Storage Temperature	T _{ST}	-	-40	80	°C
Solder Temp. for 3 Second	-	-	-	260	°C

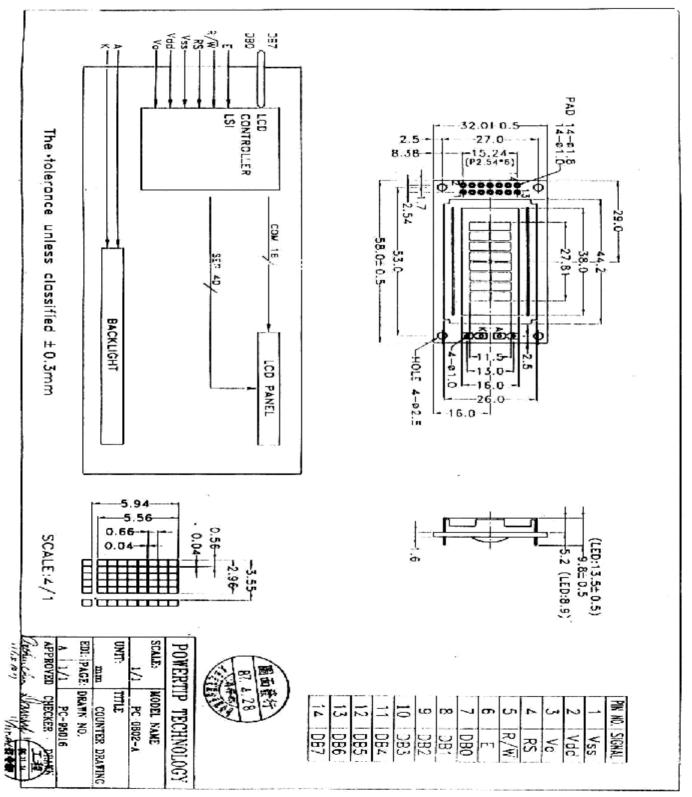
Electrical / Optical Characteristics

					Ta =2	25℃
Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Voltage	VF	IF=110 mA	4.0	4.2	4.4	V
Reverse Current	00;IR	VR=8V	-	-	0.2	mA
Average Brightness (with LCD)	IV	IF=110mA	45.6	-	-	cd/m ²
Wavelength	λp	IF=110 mA	569	-	574	nm
Luminous Intensity (without LCD)	Iv	IF=110 mA		100		cd/m ²
Color	Yellow-green					



2. MODULE STRUCTURE

2.1 Counter Drawing

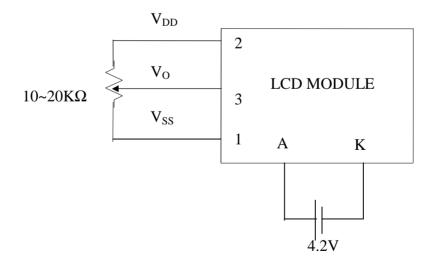




2.2 Interface Pin Description

Pin No.	Symbol	Signal Description
1	Vss	Power Supply (Vss=0)
2	VDD	Power Supply (V _{DD} >V _{SS})
3	Vo	Operating voltage (LCD Driver)
		Register Selection input
4	RS	High = Data register
4	KS	Low = Instruction register (for write)
		Busy flag address counter (for read)
5		Read/Write signal input is used to select the read/write mode
5	R/W	High = Read mode, Low = Write mode
6	Е	Start enable signal to read or write the data
		Four low order bi-directional three-state data bus lines. Use
7~10	DB0 ~ DB3	for data transfer between the MPU and the LCD module.
		These four are not used during 4-bit operation.
		Four high order bi-directional three-state data bus lines. Used
11~14	DB4 ~ DB7	for data transfer between the MPU and the LCD module.
		DB7 can be used as a busy flag.

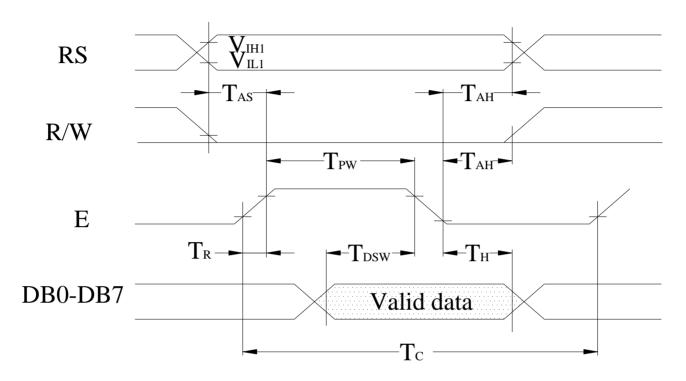
Contrast Adjust



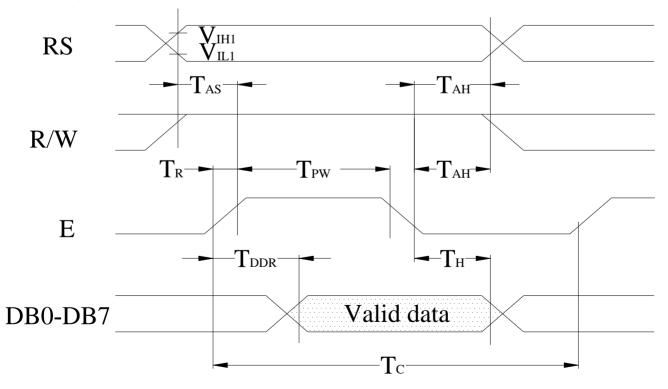


2.3 Timing Characteristics

• Writing data from MPU to ST7066U



• Reading data from ST7066U to MPU





• Write Mode (Writing data from MPU to ST7066U)

				(Vcc = -	+5V,Ta=25°
Characteristics	Test Condition	Min.	Тур.	Max.	Unit
Enable Cycle Time	Pin E	1200	-	-	ns
Enable Pulse Width	Pin E	140	-	-	ns
Enable Rise / Fall Time	Pin E	-	-	25	ns
Address Setup Time	Pins: RS , RW,E	0	-	-	ns
Address Hold Time	Pins :RS,RW,E	10	-	-	ns
Data Setup Time	Pins:DB0~DB7	40	-	_	ns
Data Hold Time	Pins:DB0~DB7	10	-	-	ns
	Enable Cycle Time Enable Pulse Width Enable Rise / Fall Time Address Setup Time Address Hold Time Data Setup Time	Enable Cycle TimePin EEnable Pulse WidthPin EEnable Rise / Fall TimePin EAddress Setup TimePins: RS , RW,EAddress Hold TimePins :RS,RW,EData Setup TimePins:DB0~DB7	Enable Cycle TimePin E1200Enable Pulse WidthPin E140Enable Rise / Fall TimePin E-Address Setup TimePins: RS , RW,E0Address Hold TimePins :RS,RW,E10Data Setup TimePins: DB0~DB740	Enable Cycle TimePin E1200Enable Pulse WidthPin E140Enable Rise / Fall TimePin E-Address Setup TimePins: RS , RW,E0Address Hold TimePins :RS,RW,E10Data Setup TimePins: DB0~DB740	CharacteristicsTest ConditionMin.Typ.Max.Enable Cycle TimePin E1200Enable Pulse WidthPin E140Enable Rise / Fall TimePin E25Address Setup TimePins: RS , RW,E0Address Hold TimePins :RS,RW,E10Data Setup TimePins: DB0~DB740

• Read Mode (Reading data from ST7066U to MPU)

				(Vcc = +5V	(,Ta=25°C)
Symbol	Characteristics	Test Condition	Min.	Тур.	Max.	Unit
T _C	Enable Cycle Time	Pin E	1200	-	-	ns
T_{PW}	Enable Pulse Width	Pin E	140	-	-	ns
T_R, T_F	Enable Rise / Fall Time	Pin E	-	-	25	ns
T _{AS}	Address Setup Time	Pins: RS , RW,E	0	-	-	ns
T _{AH}	Address Hold Time	Pins :RS,RW,E	10	-	-	ns
T _{DDR}	Data Setup Time	Pins:DB0~DB7	-	-	100	ns
T _H	Data Hold Time	Pins:DB0~DB7	10	-	-	ns



2.4 Display Command

					Instru	iction	Code			Description		
Instructions	RS	R/W	DB 7	DB 6	DB 5	DB 4	DB 3	DB DI 2 1		DB 0	Description	Time (270KHz)
Clear Display	0	0	0	0	0	0	0	0	0	1	Write "20H" to DDRAM. and set DDRAM address to "00H" from AC.	1.52ms
Return Home	0	0	0	0	0	0	0	0	1	×	Set DDRAM address to "00H" from AC and return cursor to it's original position if shifted. The contents of DDRAM are not changed.	1.52ms
Entry Mode Set	0	0	0	0	0	0	0	1	I/D	S	Sets cursor move direction and specifies display shift. These operations are performed during data write and read.	37us
Display ON/OFF	0	0	0	0	0	0	1	D	С	В	D=1 : entire display on C=1 : cursor on B=1 : cursor position on	37µs
Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	×	×	Set cursor moving and display shift control bit, and the direction, without changing of DDRAM data.	37µs
Function Set	0	0	0	0	1	DL	N	F	×	×	DL: interface data is 8/4 bits NL: number of line is 2/1 F: font size is 5×11/5×8	37µs
Set CGRAM Address	0	0	0	1	AC 5	AC 4	AC 3	AC 2	AC 1	AC 0	Set CGRAM address in address counter.	37µs
Set DDRAM Address	0	0	1	AC 6	AC 5	AC 4	AC 3	AC 2	AC 1	AC 0	Set DDRAM address in address counter.	37µs



Read Busy Flag and Address	0	1	BF	AC 6	AC 5	AC 4	AC 3	AC 2	AC 1	AC 0	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.	0µs
Write Data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data into internal RAM (DDRAM/CGRAM).	37µs
Read Data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read data from internal RAM (DDRAM/CGRAM).	37µs

Note:

Be sure the ST7066U is not in the busy state (BF=0) before sending an instruction from the MPU to the ST7066U

If an instruction is sent without checking the busy flag, the time between the first instruction and next instruction will take much longer than the instruction time itself.

Refer to Instruction Table for the list of each instruction execution time .

POWERTIP

2.5 Character Pattern

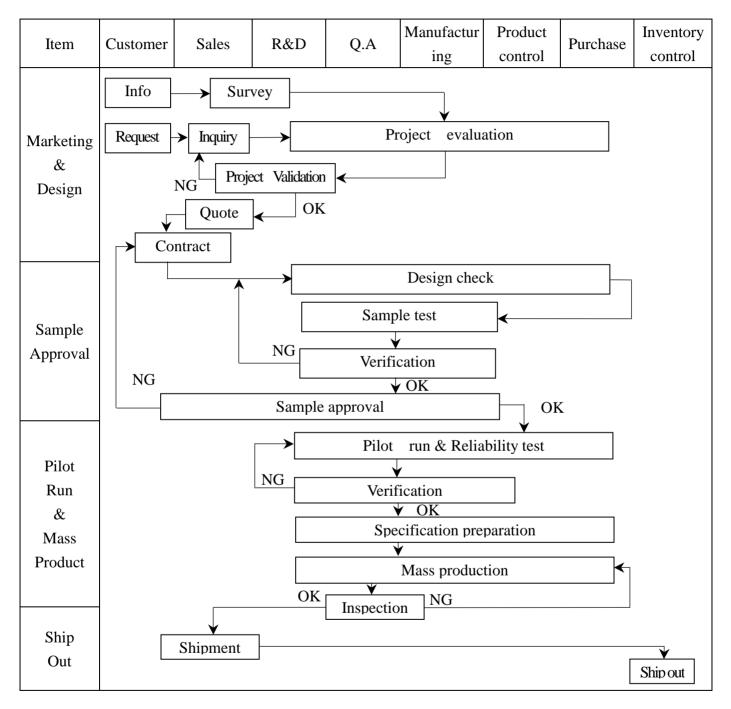
CHARACTER PATTERN(SO/HO/EA,WA)

Upper 4 Bits 4 Bits	0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
xxxx0000	CG RAM (1)			Ø	3	[•••					•••••	-53	₩.	œ	
xxxx0001	(2)		:	1			-::: :					<u> </u>	<u>э</u> .	ć.,		
xxxx0010	(3)		::	2		FR:	Ŀp	ŀ			1	-:{ ^{-*}	• !]	.:-: [*]	 ===	<u> </u>
xxxx0011	(4)		#		<u> </u>	<u></u>	€	: <u>.</u> .				r)	7	1	æ.	
xxxx0100	(5)		:	::].	D			t .			•		ŀ.	- ->	[]	<u>.</u>
xxxx0101	(6)		:-:- :-::		<u> </u>		::::•	II				7	;)		::: :	ü
xxxx0110	(7)		8.	e,	['	U,	÷	ц.)				<u>_</u>			p	2
xxxx0111	(8)		3	7	G	IJ	-	I,.,I				=	32		9	31
xxxx1000	(1)		¢	8	$\left\ \cdot \cdot \right\ $	×	ŀ'n)×:			:[[*]	-71	:#:	IJ		33
xxxx1001	(2))	9	Ι	ц) Y	i	•!				÷Ţ		11.	:	<u>ا</u>
xxx1010	(3)		:-[-:			2		2					i ¹ 1	L	Ĵ	÷
xxx1011	(4)		[3	К	Ľ	k	4			:4°	Ţ				
xxx1100	(5)		:•		.	46]	I			·[·:·			· _]	¢	F
xxxx1101	(6)		•••••		[*·]]	[·]	3					·~.,		ŧ	-÷-
xxxx1110	(7)			>	ŀ··l		ŀ"1	j-		1		12		••••	P	
xxxx1111	(8)			?			\square	÷÷••		:		<u>.</u> ,			<u>.</u>	

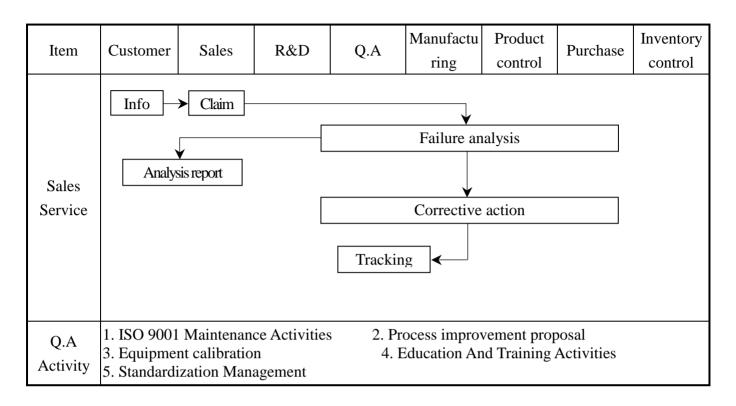


3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart









3.2 Inspection Specification

Inspection Standard : MIL-STD-105E Table Normal Inspection Single Sampling Level II • Equipment : Gauge • MIL-STD • Powertip Tester • Sample •

IQC Defect Level : Major Defect AQL 0.4; Minor Defect AQL 1.5 °

FQC Defect Level: 100% Inspection •

OUT Going Defect Level : Sampling $\,{}^\circ$

Specification :

NO	Item	Specification	Judge	Level
1	Part Number	The part number is inconsistent with work order of production	N.G.	Major
2	Quantity	The quantity is inconsistent with work order of production	N.G.	Major
	Electronic	The display lacks of some patterns.	N.G.	Major
	characteristics of	Missing line.	N.G.	Major
3	LCM	The size of missing dot, A is $> 1/2$ Dot size	N.G.	Major
	$A=(L+W)\div 2$	There is no function.	N.G.	Major
		Output data is error	N.G.	Major
		Material is different with work order of production	N.G.	Major
		LCD is assembled in inverse direction	N.G.	Major
		Bezel is assembled in inverse direction	N.G.	Major
		Shadow is within LCD viewing area + 0.5 mm	N.G.	Major
	Appearance of	The diameter of dirty particle, A is $>$ 0.4 mm	N.G.	Minor
	$LCD = (L + W) \div 2$	Dirty particle length is $>$ 3.0mm, and 0.01mm $<$ width \leq 0.05mm	N.G.	Minor
4	~	Display is without protective film	N.G.	Minor
	Dirty particle	Conductive rubber is over bezel 1mm	N.G.	Minor
	(Including	Polarizer exceeds over viewing area of LCD	N.G.	Minor
	scratch v bubble)	Area of bubble in polarizer, $A > 1.0$ mm, the number of		
		bubble is >1 piece.	N.G.	Minor
		0.4mm < Area of bubble in polarizer, A < 1.0mm, the number of bubble is >4 pieces.	N.G.	Minor
		Burned area or wrong part number is on PCB	N.G.	Major
		The symbol, character, and mark of PCB are unidentifiable.	N.G	Minor
		The stripped solder mask , A is > 1.0mm	N.G.	Minor
	Appearance of	0.3 mm < stripped solder mask or visible circuit, A < 1.0mm, and the number is ≥ 4 pieces	N.G.	Minor
5	PCB	There is particle between the circuits in solder mask	N.G	Minor
	$A=(L+W)\div 2$	The circuit is peeled off or cracked	N.G	Minor
		There is any circuits risen or exposed.	N.G	Minor
		$0.2 \text{mm} < \text{Area of solder ball}, \text{A is } \leq 0.4 \text{mm}$ The number of solder ball is ≥ 3 pieces	N.G	Minor
		The magnitude of solder ball, A is > 0.4 mm.	N.G	Minor

POWERTIP

NO	Item	Specification	Judge	Level
		The shape of modeling is deformed by touching.	N.G.	Major
	Appearance of	Insufficient epoxy: Circuit or pad of IC is visible	N.G.	Minor
6	molding A=(L+W)÷2	Excessive epoxy: Diameter of modeling is >20 mm or height is >2.5 mm	N.G.	Minor
		The diameter of pinhole in modeling, A is >0.2 mm.	N.G.	Minor
		The folding angle of frame must be $>45^{\circ} +10^{\circ}$	N.G.	Minor
7	Appearance of frame	The area of stripped electroplate in top-view of frame, A is > 1.0 mm.	N.G.	Minor
/	A=(L+W)÷2	Rust or crack is (Top view only)	N.G.	Minor
		The scratched width of frame is >0.06 mm. (Top view only)	N.G.	Minor
	Flastrical	The color of backlight is nonconforming	N.G.	Major
	Electrical characteristic of	Backlight can't work normally.	N.G.	Major
8	backlight	The LED lamp can't work normally	N.G.	Major
0	A= $(L + W) \div 2$	The unsoldering area of pin for backlight, A is $> 1/2$ solder joint area.	N.G.	Minor
	$\Pi = (\Pi + \Pi)^2 \Sigma$	The height of solder pin for backlight is >2.0 mm	N.G.	Minor
		The mark or polarity of component is unidentifiable.	N.G.	Minor
		The height between bottom of component and surface of the PCB is floating >0.7 mm	N.G.	Minor
10	Assembly parts A=(L+W)÷2	D > 1/4W W $D\downarrow\downarrow\downarrow\downarrow\downarrow\downarrow\downarrow\downarrow$	N.G.	Minor
	(- · · ·) -	End solder joint width, D' is $>50\%$ width of component termination or width of pad	N.G.	Minor
		Side overhang, D is $>25\%$ width of component termination.	N.G.	Minor
		Component is cracked, deformed, and burned, etc.	N.G.	Minor
		The polarity of component is placed in inverse direction.	N.G.	Minor
		Maximum fillet height of solder extends onto the component body or minimum fillet height is <0.5 mm.	N.G.	Minor



4. RELIABILITY TEST

4.1 Reliability Test Condition

NO	Item	Test Co	ondition					
1	High Temperature Storage	Storage at 80 $\pm 2^{\circ}$ C 96~100 hrs Surrounding temperature, then storage at normal condition 4hrs						
2	Low Temperature Storage	Storage at -30 $\pm 2^{\circ}$ C 96~100 hrs Surrounding temperature, then storage at normal condition 4hrs						
3	High Temperature /Humidity Storage	 1.Storage 96~100 hrs 60±2°C, 90~95%RH surrounding temperature, then storage at normal condition 4hrs. (Excluding the polarizer). or 2.Storage 96~100 hrs 40±2°C, 90~95%RH surrounding temperature, then storage at normal condition 4 hrs. 						
4	Temperature Cycling	$-20^{\circ}C \rightarrow 25^{\circ}C \rightarrow 70^{\circ}C \rightarrow 25^{\circ}C$ $(30 \text{mins}) (5 \text{mins}) (30 \text{mins}) (5 \text{mins})$ 10 Cycle						
5	Vibration	10~55Hz (1 minute) 1.5mm X,Y and Z direction * (each 2hrs)						
6	ESD Test	Air Discharge: Apply 6 KV with 5 times discharge for each polarity +/- Testing location: Around the face of LCD	Contact Discharge: Apply 250V with 5 times discharge for each polarity +/- Testing location: 1.Apply to bezel. 2.Apply to Vdd, Vss.					
7	Drop Test	Packing Weight (Kg) 0 ~ 45.4 45.4 ~ 90.8 90.8 ~ 454 Over 454	Drop Height (cm) 122 76 61 46					



5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks , be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module , be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So, please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands , this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is $320\pm10^{\circ}$ C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM .

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is 25° C $\pm 5^{\circ}$ C and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush , shake , or jolt the module.

5.4 TERMS OF WARRANTY

5.4.1 Applicable warrant period

The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.