## Type EDL Electric Double Layer Supercapacitors

## Ultra High Capacitance, Small Case Size Options



Specifications

Type EDL electric double layer supercapacitors offer extremely high capacitance values (farads) in a variety of packaging options that will satisfy, low profile, surface mount, through hole and high density assembly requirements. The EDL is a cut above the standard electrolytic capacitor in that it can act as a battery without having to deal with the environmental or hazardous material issues that batteries entail.

## Highlights

- Unlimited charging and discharging capability
- Recycling is not necessary
- Long Life- 15 years
- Low ESR
- Will extend battery life up to 1.6 times
- First class performance with economy pricing

| Capacitance Range | 0.22 F to 70 F |  |
| :--- | :--- | :---: |
| Rated Voltage | 2.1 Vdc to $5.5^{\mathrm{Vdc}}$ |  |
| Operating Temperature Range | $-25^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ |  |
| Case Types | Radial Leaded, <br> Stacked Coin, SMT |  |
| RoHS Compliant |  |  |

## Electric Double Layer Supercapacitor Construction



## Type EDL Electric Double Layer Supercapacitors

## Ratings

| Catalog Part Number | Capacitance | Voltage <br> (Vdc) | Max. <br> Resistance <br> @ 1 kHz <br> ( $\Omega)$ | $\begin{aligned} & \text { Case } \\ & \text { Type } \end{aligned}$ | Case Dia. (mm) | Case <br> Length <br> (mm) | Style |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| EDLHW335D2R3R | 3.3 F | 2.3 | 0.3 | Radial Lead | 12.5 | 23 | $\begin{aligned} & \hline \mathrm{HW} \\ & 0 \\ & 0 \\ & 0 \\ & 0 \end{aligned}$ |
| EDLHW475D2R3R | 4.7 F |  | 0.3 |  | 12.5 | 23 |  |
| EDLHW106D2R3R | 10 F |  | 0.2 |  | 12.5 | 35 |  |
| EDLHW226D2R3R | 22 F |  | 0.1 |  | 18 | 35 |  |
| EDLHW306D2R3R | 30 F |  | 0.1 |  | 18 | 35 |  |
| EDLHW506D2R3R | 50 F |  | 0.1 |  | 18 | 40 |  |
| EDLHW706D2R1R | 70 F | 2.1 | 0.1 |  | 18 | 50 |  |
|  |  |  |  |  |  |  |  |
| EDLF473A5R5C | 0.047 F | 5.5 | 120 | Stacked Coin | 13.5 | 9.5 |  |
| EDLF104A5R5C | 0.10 F |  | 100 |  | 13.5 | 9.5 |  |
| EDLF474B5R5C | 0.47 F |  | 75 |  | 21.5 | 9.5 |  |
| EDLF684B5R5C | 0.68 F |  | 50 |  | 21.5 | 9.5 |  |
| EDLF105B5R5C | 1.00 F |  | 50 |  | 21.5 | 9.5 |  |
|  |  |  |  |  |  |  |  |
| EDLNF104A5R5C | . 10 F | 5.5 | 75 | Stacked Coin | 13.5 | 7.5 |  |
| EDLNF224A5R5C | . 22 F |  | 75 |  | 13.5 | 7.5 |  |
| EDLNF474B5R5C | . 47 F |  | 30 |  | 21.5 | 8.0 |  |
| EDLNF105B5R5C | 1.0 F |  | 30 |  | 21.5 | 8.0 |  |
| EDLNF155B5R5C | 1.5 F |  | 30 |  | 21.5 | 8.0 |  |
|  |  |  |  |  |  |  |  |
| EDLSG474V5R5C | . 47 F | 5.5 | 30 | Stacked Coin | 19 | 5.0 |  |
| EDLSG105V5R5C | 1.0 F |  | 30 |  | 19 | 5.0 |  |
| EDLSG155V5R5C | 1.5 F |  | 30 |  | 19 | 5.0 |  |
| EDLSG474H5R5C | . 47 F | 5.5 | 30 | Stacked Coin | 20 | 6.0 |  |
| EDLSG105H5R5C | 1.0 F |  | 30 |  | 20 | 6.0 |  |
| EDLSG155H5R5C | 1.5 F |  | 30 |  | 20 | 6.0 |  |
|  |  |  |  |  |  |  |  |
| EDLSD223V5R5C | . 022 F | 5.5 | 150 | Stacked Coin | 10.5 | 5.0 |  |
| EDLSD473V5R5C | . 047 F |  | 120 |  | 10.5 | 5.0 |  |
| EDLSD104V5R5C | . 10 F |  | 75 |  | 10.5 | 5.0 |  |
| EDLSD224V5R5C | . 22 F |  | 75 |  | 10.5 | 5.0 |  |
| EDLSD334V5R5C | . 33 F |  | 75 |  | 10.5 | 5.0 |  |
| EDLSD223H5R5C | . 022 F | 5.5 | 150 | Stacked Coin | 11.5 | 5.5 |  |
| EDLSD473H5R5C | . 047 F |  | 120 |  | 11.5 | 5.5 |  |
| EDLSD104H5R5C | . 10 F |  | 75 |  | 11.5 | 5.5 |  |
| EDLSD224H5R5C | . 22 F |  | 75 |  | 11.5 | 5.5 |  |
| EDLSD334H5R5C | . 33 F |  | 75 |  | 11.5 | 5.5 |  |
|  |  |  |  |  |  |  | EN |
| EDLEN204A3R3S | . 20 F | 3.3 | 200 | SMT Wide Lead | 6.8 | 1.8 |  |
| EDLEN204RL3R3S | . 20 F | 3.3 | 200 | SMT Radial Lead | 6.8 | 1.8 |  |

## Type EDL Electric Double Layer Supercapacitors

## Outline Drawings



## Style F $85^{\circ} \mathrm{C}$ Stacked Coin



| Case <br> code | Size |  |
| :---: | :---: | :---: |
|  | 13.5 | L |
| B | 21.5 | 9.5 |

Style NF $70^{\circ} \mathrm{C}$ Stacked Coin


| Case <br> code | Size |  |
| :---: | :---: | :---: |
|  | D | L |
| B | 21.5 | 7.5 |

## Style SG $70^{\circ} \mathrm{C}$ Stacked Coin

Terminal V


Terminal H


## Type EDL Electric Double Layer Supercapacitors

## Outline Drawings

Style SD $70^{\circ} \mathrm{C}$ Stacked Coin


Style EN $60^{\circ} \mathrm{C}$ Surface Mount


## Type EDL Electric Double Layer Supercapacitors

## Applications and Recommended Series



## Type EDL Electric Double Layer Supercapacitors

## How to Select an Electric Double Layer Supercapacitor

## Estimated initial back-up time

Back-up time for Type EDL Electric Double Layer Supercapacitors decreases with use and over time especially when the current is large or operating at high temperature. Be sure to specify extra back-up time initially to allow for product changes.

## Select the optimum supercapacitor according to applied current.

The internal resistance of the supercapacitor prevents drawing high discharge currents. Select the supercapacitor capable of delivering the peak current at switchover to back-up mode using the following table.

| Series | Maximum Operating (Discharge) Current |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\mathbf{0 . 0 4 7} \mathbf{~ F}$ | $\mathbf{0 . 1 ~ F}$ to 0.33 F | $\mathbf{0 . 4 7} \mathbf{F}$ to 1.5 F | $\mathbf{3 . 3} \mathbf{F}$ to $\mathbf{4 . 7} \mathbf{F}$ | $\mathbf{1 0 ~ F}$ to $\mathbf{5 0} \mathbf{F}$ |
| SG, SD, NF | $200 \mu \mathrm{~A}$ | $300 \mu \mathrm{~A}$ | 1 mA | - | - |
| F | $200 \mu \mathrm{~A}$ | $300 \mu \mathrm{~A}$ | $300 \mu \mathrm{~A}$ | - | - |
| EN | - | $10 \mu \mathrm{~A}$ | - | - | - |
| HW | - | - | 100 mA | 300 mA | 1 A |

## Back-up Time Example

Back-up time is the time it takes for the applied voltage to decay to the cut-off voltage set by the user after applying the application's maximum voltage at application maximum temperature.

Example: An F Type EDL, P/N EDLF105B5R5C (Rated at $5.5 \mathrm{~V}, 1.0 \mathrm{~F}$ ) is charged to 5.0 Vdc . The circuit requirement is such that it must maintain a memory circuit with a current drain of $10 \mu \mathrm{~A}$ in an ambient temperature of $+40^{\circ} \mathrm{C}$. The memory RTC cut-off voltage is 2.0 Vdc .

Using minimum capacitance, calculate the back-up time as
follows:
$\mathrm{t}=\mathrm{C} \Delta \mathrm{V} / \mathrm{I}=\mathrm{C}\left[\mathrm{V}_{0}-(\mathrm{i} \cdot \mathrm{R})-\mathrm{V}_{1}\right] /\left(\mathrm{i}+\mathrm{i}_{\mathrm{L}}\right)$
$\mathrm{C}=1.0 \mathrm{~F}-20 \%=0.8 \mathrm{~F}, \mathrm{R}=50 \Omega, \mathrm{~V}_{0}=5 \mathrm{~V} . \mathrm{V}_{1}=2 \mathrm{~V}, \mathrm{i}=10 \mu \mathrm{~A}$
Therefore,
$\mathrm{t}=0.8(5-0.0005-2) /(10+2) / 10^{-6}=55$ hours
And thus the initial back-up time is 55 hours. After 1000 hours,
t: Back-up time (s)
C: Capacitance of Type EDL (F)
$\mathrm{V}_{0}$ : Applied voltage (V)
$\mathrm{V}_{1}$ : Cut-off voltage (V)
i: Current during back-up (A)
$\mathrm{i}_{\mathrm{L}}$ : Leakage current (A)
R: Internal resistance $(\Omega)$ at 1 kHz

## Life Design for Electric Double Layer Supercapacitors

Type EDL supercapacitors have a useful lifetime that decreases with increasing operating temperature, humidity, applied-voltage, current and backup-time requirements.

Expected lifetime is the product of four factors:

$$
\text { Expected Life }=(\text { Lifetime }) \cdot(\text { Temperature Factor }) \cdot(\text { Voltage Factor) }) \cdot(\text { Moisture Factor })
$$

## Type EDL Electric Double Layer Supercapacitors

## Lifetime

The minimum rated life at $85^{\circ} \mathrm{C}$ with 5.5 Vdc applied is 1000 hours with maximum permitted end-of-life change of $-30 \%$ capacitance and a 4 times increase in internal resistance.

## Temperature Factor

To determine the effect of temperature on expected life of a supercapacitor, use the fact that expected lifetime doubles for each $10^{\circ} \mathrm{C}$ that the operating temperature is reduced. As an illustration, at $85{ }^{\circ} \mathrm{C}$ and full voltage the rated lifetime is 1000 hours. So, at $40^{\circ} \mathrm{C}$ the expected lifetime would be multiplied by $2^{(85-40) / 10}=$ $2^{4.5}=22.6$ times. The Temperature Factor is 22.6 , and for $1000-\mathrm{h}, 85^{\circ} \mathrm{C}$ rated life, the expected $40^{\circ} \mathrm{C}$ life would be 22600 hours.

## Voltage Factor

The rate of change of capacitance decreases with decreasing applied voltage. The effect on life extension is roughly proportional to the voltage derating, e.g., 5 V applied to 5.5 V rated supercapacitors extends the life 1.1 times.

## Moisture Factor

Expected life of these supercapacitors is considerably shortened by operation in high humidity. The applications discussed here assume that the relative humidity is no more that $50 \%$.

## Expected Life Example

So, for a 5.5 V supercapacitor at $40^{\circ} \mathrm{C}$ charged to $5 . \mathrm{V}$ in less than $50 \% \mathrm{RH}$ the expected life is

$$
\begin{aligned}
& \text { Expected Life }=(\text { Lifetime })(\text { Temperature Factor) (Voltage Factor) (Moisture Factor) } \\
& \quad=(1000 \mathrm{~h})(22.6)(1.1)(1) \\
& =24800 \text { hours } \\
& =2.8 \text { years }
\end{aligned}
$$

## Type EDL Electric Double Layer Supercapacitors

## Performance Data

Self-Discharging Characteristics Versus Charging Time
Part number EDLF105B5R5C (5.5 V 1.0 F) Charge voltage: 5 V


## Charging Characteristics

Part number EDLF105B5R5C (5.5 V 1.0 F ) @ $+20^{\circ} \mathrm{C}$


