

D/A Converter Series for Electronic Adjustments

Standard 8bit



2ch · 3ch Type D/A Converters BH2219FVM, BH2220FVM

Description

The BH2219FVM and BH2220FVM are 8bit R-2R-type D/A converters with 2 and 3 channels, respectively. A compact package allows adjacent placement, thereby eliminating deterioration of the D/A converter due to wire pattern. Furthermore, a built-in RESET function ensures that the output voltage at all channels is Low during power up. A broad power supply voltage range (2.7V-5.5V) is available, providing design flexibility.

Features

- 1) Compact package enabling adjacent placement
- 2) Built-in RESET function
- 3) High speed output response characteristics
- 4) 3-line serial interface
- 5) Broad power supply voltage range: 2.7V-5.5V

Applications

DVCs, DSCs, DVDs, CD-Rs, CD-RWs

Lineup		
Parameter	BH2219FVM	BH2220FVM
Power source voltage range	2.7V to 5.5V	2.7V to 5.5V
Number of channels	2ch	3ch
Current consumption	0.6mA	0.6mA
Differential non linearity error	±1.0LSB	±1.0LSB
Integral non linearity error	±1.5LSB	±1.5LSB
Output current performance	±1.0mA	±1.0mA
Settling time	100µs	100µs
Maximum data transfer frequency	10MHz	10MHz
Input method	CMOS	CMOS
Data latch method	LD method	LD method
Package	MSOP8	MSOP8

● Lineup

Absolute Maximum Ratings

(Ta=25°C)

Parameter	Symbol	Limits	Unit	Remarks
Power source voltage	VCC	-0.3 to 7.0	V	-
Terminal voltage	VIN	-0.3 to VCC	V	-
Storage temperature range	TSTG	-55 to 125	°C	-
Power dissipation	PD	470*1	mW	-

*1 Derated at 4.7mW/C at Ta>25°C

*2 These products are not robust against radiation

Recommended Operating Conditions

(Ta=25°C) Limits Symbol Parameter Unit Remarks MIN. MAX. TYP. VCC power source voltage VCC 2.7 5.5 V --Terminal input voltage range VIN 0 -VCC V -Analog output current 10 -1.0 -1.0 mΑ -TOPR °C Action temperature range -30 -85 -Serial clock frequency FSCLK 1.0 10.0 MHz --Limit load capacity CL --0.1 μF -

• Electrical Characteristics

(Unless otherwise specified, VCC=3.0V, RL=OPEN, CL=0pF, Ta=25°C)

Parameter	Currente e l	Limits			1.1	
	Symbol	MIN.	TYP.	MAX.	Unit	Conditions
<current consumption=""></current>						
VCC system	ICC	-	0.4	0.8	mA	CLK=1MHz, 80H setting
<logic interface=""></logic>						
L input voltage	VIL	GND	-	0.2VCC	V	
H input voltage	VIH	0.8VCC	-	VCC	V	
Input current	IIN	-10	-	10	μA	
<buffer amplifier=""></buffer>						
	ZS1	GND	-	0.1	V	00H setting, at no load
Output zero scale voltage	ZS2	GND	-	0.3	V	00H setting, IOH=1.0mA
	FS1	VCC-0.1	-	VCC	V	FFH setting, at no load
Output full scale voltage	FS2	VCC-0.3	-	VCC	V	FFH setting, IOL=1.0mA
<d a="" converter="" precision=""></d>						
Differential non linearity error	DNL	-1.0	-	1.0	LSB	Input code 02H to FDH
Integral non linearity error	INL	-1.5	-	1.5	LSB	Input code 02H to FDH
VCC power source voltage rise time	trVCC	100	-	-	μs	VCC=0→2.7V
Power ON reset release voltage	VPOR	-	1.9	-	V	

Timing Chart

Parameter	Question		Limits		Unit	Conditions
	Symbol	MIN.	TYP.	MAX.		
CLK L level time	tCLKL	50	-	-	ns	
CLK H level time	tCLKH	50	-	-	ns	
DI setup time	tsDI	20	-	-	ns	
DI hold time	thDI	40	-	-	ns	
LD setup time	tsLD	50	-	-	ns	
LD hold time	thLD	50	-	-	ns	
LD H level time	tLDH	50	-	-	ns	
Output settling time	tOUT	-	-	100	μs	CL=50pF, RL=10kΩ

(Unless otherwise specified, VCC = 3.0V, Ta = $25^{\circ}C$)

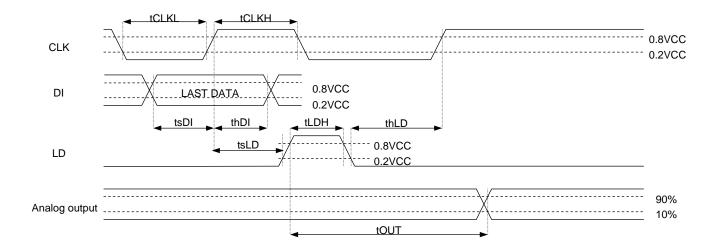


Fig.1

Terminal Description / Block Diagrams

(BH2219FVM)

(BH2220FVM)

Terminal

1

2

3

4

5

6

7

8

Terminal

name

AO1

AO2

AO3

VCC

GND

DI

CLK

LD

	,	F (;
Terminal	Terminal	Function
Torrinia	name	
1	AO1	Analog output terminal
2	AO2	Analog output terminal
3	TEST	Test terminal
3	MONI	(OPEN at normal use)
4	VCC	Power source terminal
5	GND	Ground terminal
6	DI	Serial data input terminal
7	CLK	Serial clock input terminal
0	LD	Serial data load input
8	LD	terminal

Function

Analog output terminal

Power source terminal

Serial data input terminal

Serial clock input terminal Serial data load input

Ground terminal

terminal

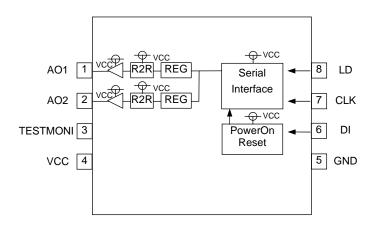
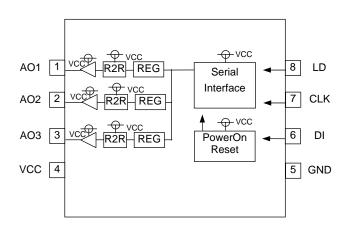


Fig.2





Equivalent Circuits

Terminal	Equivalent circuit	Terminal	Equivalent circuit
AO1		DI	
AO2		CLK	vcc <u>♀</u> , <u>♀</u> vcc
AO3		LD	

Fig.4 Equivalent circuit

Operation Description

Serial Interface

The Control command consists of 3 lines of 12bit serial input data (MSB first). DI data is read at the rising edge of the CLK, and is held in the LD Low area. The output data is determined in LD High area.

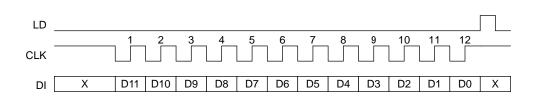


Fig.5

Data Sett	ings							
D0	D1	D2	D3	D4	D5	D6	D7	Setting
0	0	0	0	0	0	0	0	GND
1	0	0	0	0	0	0	0	(VCC-GND)/256x1
0	1	0	0	0	0	0	0	(VCC-GND)/256x2
1	1	0	0	0	0	0	0	(VCC-GND)/256x3
0	0	1	0	0	0	0	0	(VCC-GND)/256x4
				~				
0	1	1	1	1	1	1	1	(VCC-GND)/256x254
1	1	1	1	1	1	1	1	(VCC-GND)/256x255

Channel Setting (BH2219FVM)

D8	D9	D10	D11	Setting
0	0	Х	Х	AO1
1	0	Х	Х	AO2
0	1	Х	Х	Inconsequential
1	1	Х	Х	Inconsequential

Channel setting (BH2220FVM)

D8	D9	D10	D11	Setting				
0	0	Х	Х	AO1				
1	0	Х	Х	AO2				
0	1	Х	Х	AO3				
1	1	Х	Х	Inconsequential				

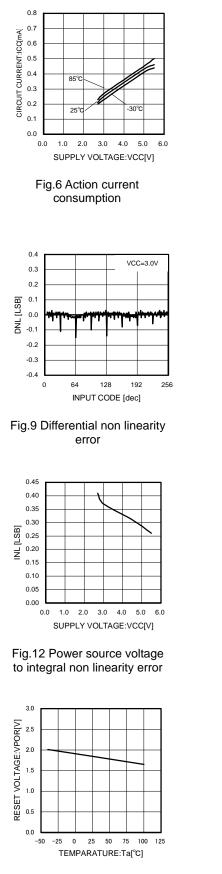


Fig.15 Reset release voltage

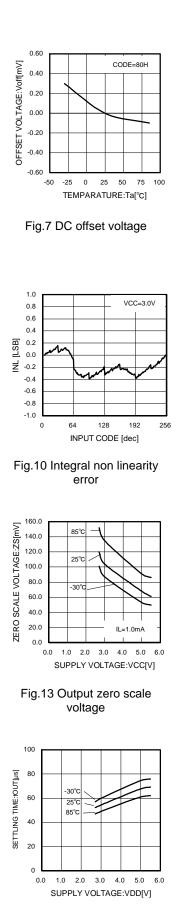
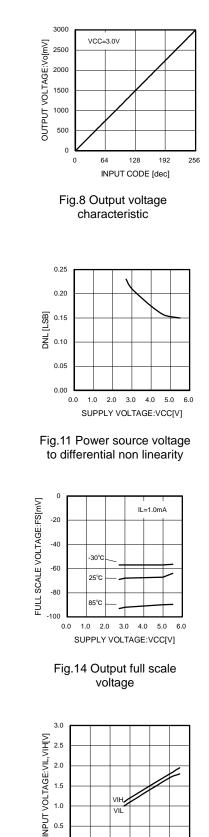


Fig.16 Settling time



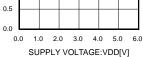


Fig.17 Input voltage

Precautions

- (1) Numbers and data in entries are representative design values and are not guaranteed values of the items.
- (2) Although we are confident in recommending the sample application circuits, carefully check their characteristics further when using them. When modifying externally attached component constants before use, determine them so that they have sufficient margins by taking into account variations in externally attached components and the Rohm LSI, not only for static characteristics but also including transient characteristics.

(3) Absolute maximum ratings

Operating or testing the device over the maximum specifications may damage the part itself as well as peripheral components. Therefore, please ensure that the specifications are not exceeded.

(4)GND potential

Ensure that the GND terminal is at the lowest potential under all operating conditions.

(5) Thermal design

Use a thermal design that allows for a sufficient margin regarding power dissipation (Pd) under actual operating conditions.

(6) Terminal shorts and mis-mounting

Incorrect orientation or misalignment of the IC when mounting to the PCB may damage part. Short-circuits caused by the introduction of foreign matter between the output terminals or across the output and power supply or GND may also result in destruction.

(7) Operation in a strong magnetic field

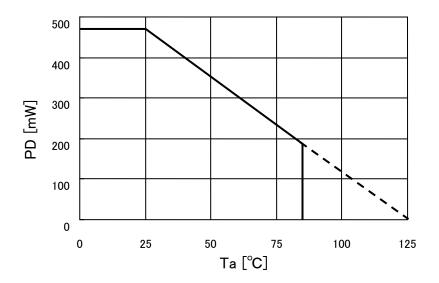
Operation in a strong electromagnetic field may cause malfunction.

(8) Reset circuit

The power on reset circuit, which initializes internal settings, may malfunction during abrupt power ons. Therefore, set the time constant so as to satisfy the power source rise time.

•Thermal Derating Curve

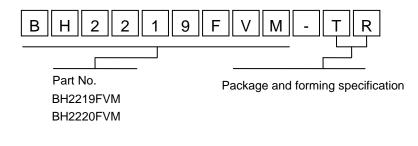
· MSOP8

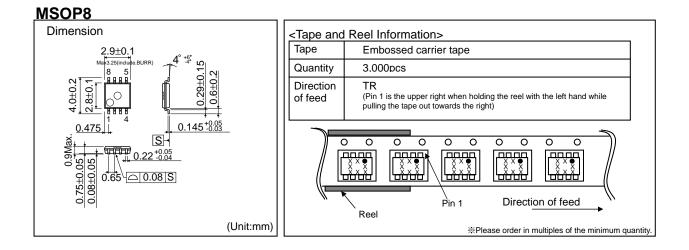


Board size: 70 x 70 x 1.6mm Material : FR4 glass epoxy board (copper foil area less than 3%)

Fig.18

Part Number Explanation





Notes

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Appendix1-Rev2.0

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