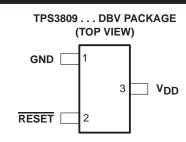
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- 3-Pin SOT-23 Package
- Supply Current of 9 μA (Typical)
- Precision Supply Voltage Monitor 2.5 V, 3 V, 3.3 V, 5 V
- Power-On Reset Generator With Fixed Delay Time of 200 ms
- Pin-For-Pin Compatible With MAX 809
- Temperature Range . . . –40°C to 85°C



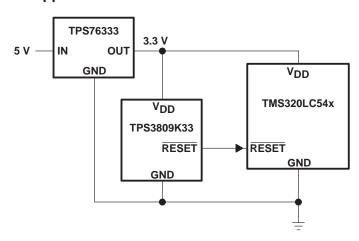
description

The TPS3809 family of supervisory circuits provides circuit initialization and timing supervision, primarily for DSPs and processor-based systems.

During power-on, \overline{RESET} is asserted when the supply voltage V_{DD} becomes higher than 1.1 V. Thereafter, the supervisory circuit monitors V_{DD} and keeps \overline{RESET} active as long as V_{DD} remains below the threshold voltage V_{IT} . An internal timer delays the return of the output to the inactive state (high) to ensure proper system reset. The delay time, $t_{d(typ)} = 200$ ms, starts after V_{DD} has risen above the threshold voltage V_{IT} . When the supply voltage drops below the threshold voltage V_{IT} , the output becomes active (low) again. No external components are required. All the devices of this family have a fixed sense-threshold voltage V_{IT} set by an internal voltage divider.

The product spectrum is designed for supply voltages of 2.5 V, 3 V, 3.3 V, and 5 V. The circuits are available in a 3-pin SOT-23. The TPS3809 devices are characterized for operation over a temperature range of –40°C to 85°C.

typical applications



- Applications Using DSPs, Microcontrollers, or Microprocessors
- Wireless Communication Systems
- Portable/Battery-Powered Equipment
- Programmable Controls
- Intelligent Instruments
- Industrial Equipment
- Notebook/Desktop Computers
- Automotive Systems



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.



TPS3809J25, TPS3809L30, TPS3809K33, TPS3809I50 3-PIN SUPPLY VOLTAGE SUPERVISORS

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AVAILABLE OPTIONS

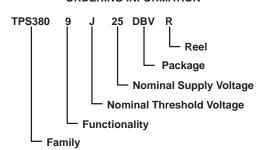
T _A	DEVICE	NAME	THRESHOLD VOLTAGE	MARKING
	TPS3809J25DBVR†	TPS3809J25DBVT‡	2.25 V	PCZI
-40°C to 85°C	TPS3809L30DBVR†	TPS3809L30DBVT‡	2.64 V	PDAI
-40°C to 85°C	TPS3809K33DBVR†	TPS3809K33DBVT‡	2.93 V	PDBI
	TPS3809I50DBVR†	TPS3809I50DBVT [‡]	4.55 V	PDCI

[†] The DBVR passive indicates tape and reel of 3000 parts.

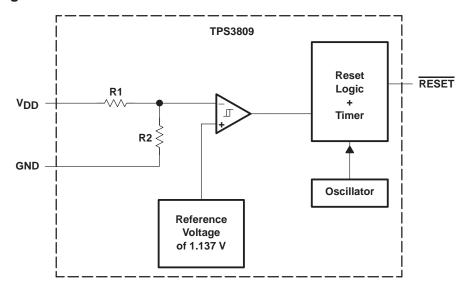
FUNCTION/TRUTH TABLE, TPS3809

V _{DD} >V _{IT}	RESET
0	L
1	н

ORDERING INFORMATION

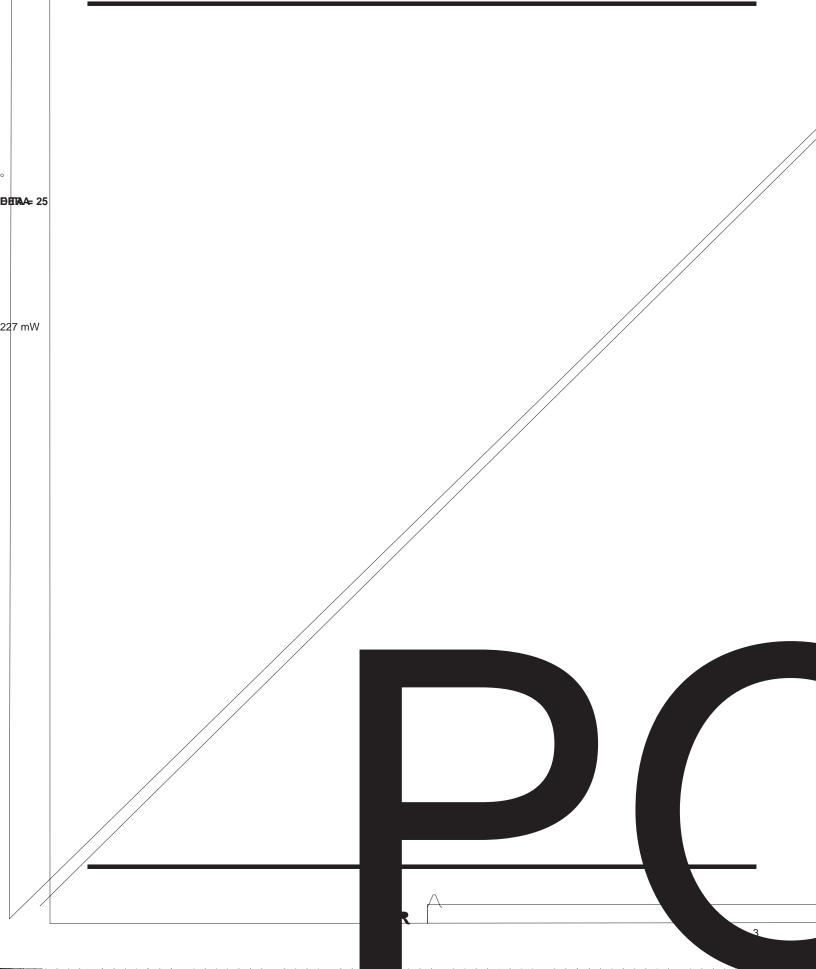


functional block diagram



[‡] The DBVT passive indicates tape and reel of 250 parts.

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TPS3809J25, TPS3809L30, TPS3809K33, TPS3809I50 3-PIN SUPPLY VOLTAGE SUPERVISORS

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electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

	PARAMETER	TEST CO	NDITIONS	MIN	TYP	MAX	UNIT	
			$V_{DD} = 2.5 \text{ V to 6}$	V, I _{OH} = -500 μA	V _{DD} -0.2			
VOH High-level output voltage			$V_{DD} = 3.3 \text{ V},$	$I_{OH} = -2 \text{ mA}$	V _{DD} -0.4			V
		$V_{DD} = 6 V$,	$I_{OH} = -4 \text{ mA}$	V _{DD} -0.4				
			$V_{DD} = 2 V \text{ to } 6 V,$	I _{OL} = 500 μA			0.2	
VOL	Low-level output voltage		$V_{DD} = 3.3 V$,	$I_{OL} = 2 \text{ mA}$			0.4	V
			$V_{DD} = 6 V$,	I _{OL} = 4 mA			0.4	
	Power-up reset voltage (see Note	2)	$V_{DD} \ge 1.1 \text{ V},$	I _{OL} = 50 μA			0.2	V
		TPS3809J25			2.20	2.25	2.30	
\	Negative-going input threshold	TPS3809L30	T. 400C to 950C		2.58	2.64	2.70	V
VIT-	voltage (see Note 3) TPS38	TPS3809K33	T _A - 40°C to 85°C	2.87	2.93	2.99		
		TPS3809I50			4.45	4.55	4.65	
		TPS3809J25				30		
\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	Lhyatarasia	TPS3809L30				35		mV
V _{hys}	Hysteresis	TPS3809K33	1			40		IIIV
		TPS3809I50	1			60		
Inn	L Complete company		V _{DD} = 2 V, Outpo	ut unconnected		9	12	
IDD	Supply current		V _{DD} = 6 V, Outpu	ut unconnected		20	25	μΑ
Ci	Input capacitance		$V_I = 0 V \text{ to } V_{DD}$			5	·	pF

NOTES: 2. The lowest supply voltage at which \overline{RESET} becomes active. $t_{r,\ VDD} \ge 15\ \mu s/V$.

timing requirements at R_L = 1 M Ω , C_L = 50 pF, T_A = 25°C

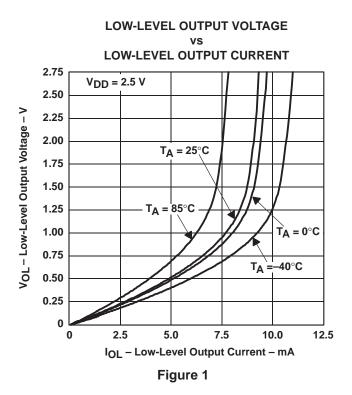
PARAMETER				TEST CONDITIONS	MIN	TYP	MAX	UNIT
t _W	Pulse width	at V _{DD}	$V_{DD} = V_{IT-} + 0.2 V,$	$V_{DD} = V_{IT-} - 0.2 V$	3			μs

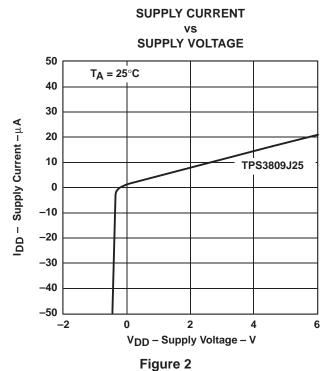
switching characteristics at RL = 1 M Ω , CL = 50 pF, TA = 25°C

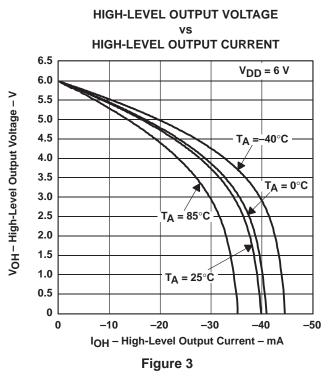
	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
t _d	Delay time	$V_{DD} \ge V_{IT-} + 0.2 \text{ V},$ See timing diagram	120	200	280	ms	
tPHL	Propagation (delay) time, high-to-low-level output	V _{DD} to RESET delay	V _{IL} = V _{IT} - 0.2 V, V _{IH} = V _{IT} + 0.2 V		1		μs

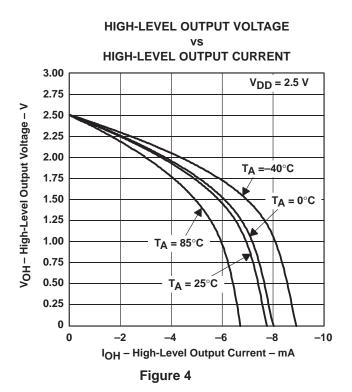
^{3.} To ensure best stability of the threshold voltage, a bypass capacitor (0.1 μ F ceramic) should be placed near the supply terminals.

TYPICAL CHARACTERISTICS

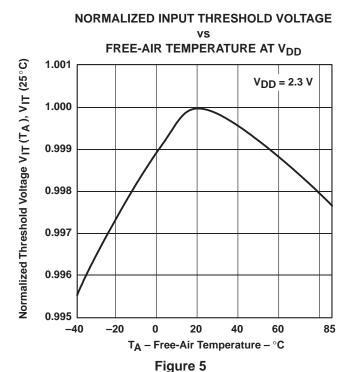


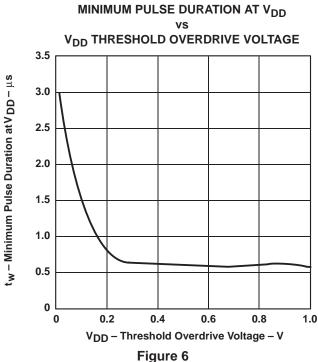






TYPICAL CHARACTERISTICS





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PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	e Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
TPS3809I50DBVR	ACTIVE	SOT-23	DBV	3	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TPS3809I50DBVRG4	ACTIVE	SOT-23	DBV	3	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TPS3809I50DBVT	ACTIVE	SOT-23	DBV	3	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TPS3809I50DBVTG4	ACTIVE	SOT-23	DBV	3	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TPS3809J25DBVR	ACTIVE	SOT-23	DBV	3	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TPS3809J25DBVRG4	ACTIVE	SOT-23	DBV	3	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TPS3809J25DBVT	ACTIVE	SOT-23	DBV	3	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TPS3809K33DBVR	ACTIVE	SOT-23	DBV	3	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TPS3809K33DBVRG4	ACTIVE	SOT-23	DBV	3	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TPS3809K33DBVT	ACTIVE	SOT-23	DBV	3	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TPS3809K33DBVTG4	ACTIVE	SOT-23	DBV	3	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TPS3809L30DBVR	ACTIVE	SOT-23	DBV	3	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TPS3809L30DBVRG4	ACTIVE	SOT-23	DBV	3	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TPS3809L30DBVT	ACTIVE	SOT-23	DBV	3	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TPS3809L30DBVTG4	ACTIVE	SOT-23	DBV	3	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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PACKAGE OPTION ADDENDUM

30-Mar-2005

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