

# AM26C31 QUADRUPLE DIFFERENTIAL LINE DRIVER

SLLS103K – DECEMBER 1990 – REVISED SEPTEMBER 2004

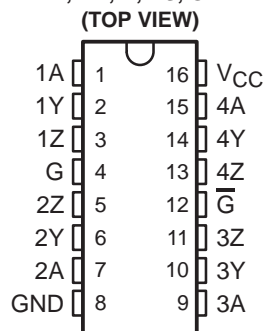
- Meets or Exceeds the Requirements of TIA/EIA-422-B and ITU Recommendation V.11
- Low Power,  $I_{CC} = 100 \mu\text{A Typ}$
- Operates From a Single 5-V Supply
- High Speed,  $t_{pLH} = t_{pHL} = 7 \text{ ns Typ}$
- Low Pulse Distortion,  $t_{sk(p)} = 0.5 \text{ ns Typ}$
- High Output Impedance in Power-Off Conditions
- Improved Replacement for AM26LS31
- Available in Q-Temp Automotive
  - High-Reliability Automotive Applications
  - Configuration Control/Print Support
  - Qualification to Automotive Standards

## description/ordering information

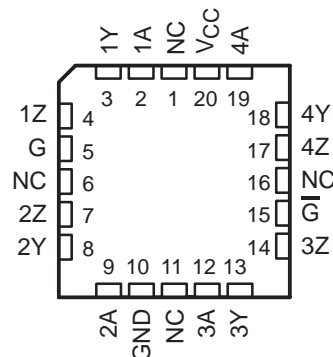
The AM26C31 is a differential line driver with complementary outputs, designed to meet the requirements of TIA/EIA-422-B and ITU (formerly CCITT). The 3-state outputs have high-current capability for driving balanced lines, such as twisted-pair or parallel-wire transmission lines, and they provide the high-impedance state in the power-off condition. The enable functions are common to all four drivers and offer the choice of an active-high (G) or active-low ( $\bar{G}$ ) enable input. BiCMOS circuitry reduces power consumption without sacrificing speed.

The AM26C31C is characterized for operation from 0°C to 70°C, the AM26C31I is characterized for operation from –40°C to 85°C, the AM26C31Q is characterized for operation over the automotive temperature range of –40°C to 125°C, and the AM26C31M is characterized for operation over the full military temperature range of –55°C to 125°C.

AM26C31M . . . J OR W PACKAGE  
AM26C31Q . . . D PACKAGE  
AM26C31C . . . D, DB, N, OR NS PACKAGE  
AM26C31I . . . D, DB, N, NS, OR PW PACKAGE



AM26C31M . . . FK PACKAGE  
(TOP VIEW)



NC – No internal connection



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

 **TEXAS  
INSTRUMENTS**

POST OFFICE BOX 655303 • DALLAS, TEXAS 75265

Copyright © 2004, Texas Instruments Incorporated  
On products compliant to MIL-PRF-38535, all parameters are tested unless otherwise noted. On all other products, production processing does not necessarily include testing of all parameters.

# AM26C31 QUADRUPLE DIFFERENTIAL LINE DRIVER

SLLS103K – DECEMBER 1990 – REVISED SEPTEMBER 2004

## description/ordering information (continued)

### ORDERING INFORMATION

TA	PACKAGE†		ORDERABLE PART NUMBER	TOP-SIDE MARKING
0°C to 70°C	PDIP (N)	Tube of 25	AM26C31CN	AM26C31CN
	SOIC (D)	Tube of 40	AM26C31CD	AM26C31C
		Reel of 2500	AM26C31CDR	
	SOP (NS)	Reel of 2000	AM26C31CNSR	26C31
	SSOP (DB)	Reel of 2000	AM26C31CDBR	26C31
-40°C to 85°C	PDIP (N)	Tube of 25	AM26C31IN	AM26C31IN
	SOIC (D)	Tube of 40	AM26C31ID	AM26C31C
		Reel of 2500	AM26C31IDR	
	SOP (NS)	Reel of 2000	AM26C31INSR	26C31
	SSOP (DB)	Reel of 2000	AM26C31IDBR	26C31
TSSOP (PW)	Tube of 90	AM26C31IPW	26C31I	
-40°C to 125°C	SOIC (D)	Tube of 40	AM26C31QD	AM26C31QD
		Reel of 2500	AM26C31QDR	
-55°C to 125°C	CDIP (J)	Tube of 25	AM26C31MJ	AM26C31MJ
	CFP (W)	Tube of 150	AM26C31MW	AM26C31MW
	LCCC (FK)	Tube of 55	AM26C31MFK	AM26C31MFK

† Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at [www.ti.com/sc/package](http://www.ti.com/sc/package).

### FUNCTION TABLE (each driver)

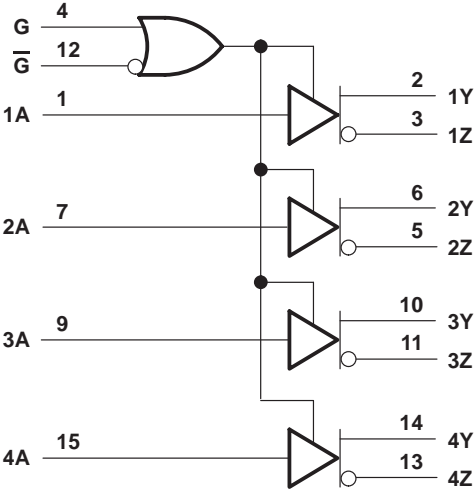
INPUT A	ENABLES		OUTPUTS	
	G	$\bar{G}$	Y	Z
H	H	X	H	L
L	H	X	L	H
H	X	L	H	L
L	X	L	L	H
X	L	H	Z	Z

H = High level, L = Low level, X = Irrelevant,  
Z = High impedance (off)

# AM26C31 QUADRUPLE DIFFERENTIAL LINE DRIVER

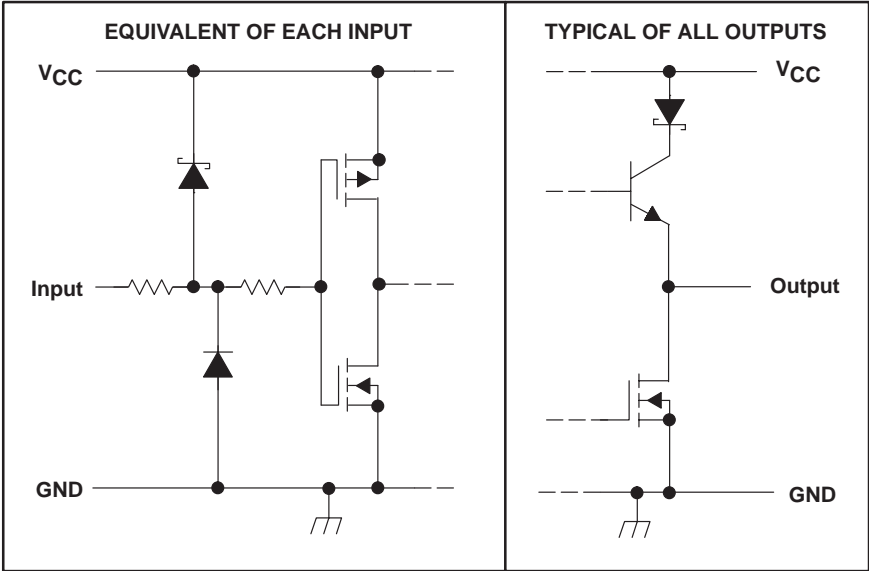
SLLS103K – DECEMBER 1990 – REVISED SEPTEMBER 2004

**logic diagram (positive logic)**



Pin numbers shown are for the D, DB, J, N, NS, PW, and W packages.

**schematics of inputs and outputs**



# AM26C31

## QUADRUPLE DIFFERENTIAL LINE DRIVER

SLLS103K – DECEMBER 1990 – REVISED SEPTEMBER 2004

### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage range, $V_{CC}$ (see Note 1)	–0.5 V to 7 V
Input voltage range, $V_I$	–0.5 V to $V_{CC} + 0.5$ V
Differential input voltage range, $V_{ID}$	–14 V to 14 V
Output voltage range, $V_O$	–0.5 V to 7 V
Input or output clamp current, $I_{IK}$ or $I_{OK}$	±20 mA
Output current, $I_O$	±150 mA
$V_{CC}$ current	200 mA
GND current	–200 mA
Package thermal impedance, $\theta_{JA}$ (see Notes 2 and 3):	
D package	73°C/W
DB package	82°C/W
N package	67°C/W
NS package	64°C/W
PW package	108°C/W
Operating virtual junction temperature, $T_J$	150°C
Storage temperature range, $T_{stg}$	–65°C to 150°C

† Stresses beyond those listed under “absolute maximum ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- NOTES:
1. All voltage values, except differential output voltage ( $V_{OD}$ ), are with respect to the network ground terminal.
  2. Maximum power dissipation is a function of  $T_J(\text{max})$ ,  $\theta_{JA}$ , and  $T_A$ . The maximum allowable power dissipation at any allowable ambient temperature is  $P_D = (T_J(\text{max}) - T_A)/\theta_{JA}$ . Operating at the absolute maximum  $T_J$  of 150°C can affect reliability.
  3. The package thermal impedance is calculated in accordance with JESD 51-7.

### recommended operating conditions

		MIN	NOM	MAX	UNIT
$V_{CC}$	Supply voltage	4.5	5	5.5	V
$V_{ID}$	Differential input voltage		±7		V
$V_{IH}$	High-level input voltage	2			V
$V_{IL}$	Low-level input voltage			0.8	V
$I_{OH}$	High-level output current			–20	mA
$I_{OL}$	Low-level output current			20	mA
$T_A$	Operating free-air temperature	AM26C31C	0	70	°C
		AM26C31I	–40	85	
		AM26C31Q	–40	125	
		AM26C31M	–55	125	



# AM26C31 QUADRUPLE DIFFERENTIAL LINE DRIVER

SLLS103K – DECEMBER 1990 – REVISED SEPTEMBER 2004

**electrical characteristics over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted)**

PARAMETER	TEST CONDITIONS	AM26C31C AM26C31I			UNIT
		MIN	TYP†	MAX	
V <sub>OH</sub> High-level output voltage	I <sub>O</sub> = -20 mA	2.4	3.4		V
V <sub>OL</sub> Low-level output voltage	I <sub>O</sub> = 20 mA		0.2	0.4	V
V <sub>OD</sub>   Differential output voltage magnitude	R <sub>L</sub> = 100 Ω, See Figure 1	2	3.1		V
Δ V <sub>OD</sub>   Change in magnitude of differential output voltage‡	R <sub>L</sub> = 100 Ω, See Figure 1			±0.4	V
V <sub>OC</sub> Common-mode output voltage	R <sub>L</sub> = 100 Ω, See Figure 1			3	V
Δ V <sub>OC</sub>   Change in magnitude of common-mode output voltage‡	R <sub>L</sub> = 100 Ω, See Figure 1			±0.4	V
I <sub>I</sub> Input current	V <sub>I</sub> = V <sub>CC</sub> or GND			±1	μA
I <sub>O(off)</sub> Driver output current with power off	V <sub>CC</sub> = 0 V <sub>O</sub> = 6 V V <sub>O</sub> = -0.25 V			100 -100	μA
I <sub>OS</sub> Driver output short-circuit current	V <sub>O</sub> = 0	-30		-150	mA
I <sub>OZ</sub> High-impedance off-state output current	V <sub>O</sub> = 2.5 V V <sub>O</sub> = 0.5 V			20 -20	μA
I <sub>CC</sub> Quiescent supply current	I <sub>O</sub> = 0 V <sub>I</sub> = 0 V or 5 V V <sub>I</sub> = 2.4 V or 0.5 V, See Note 4		1.5	3	mA
C <sub>i</sub> Input capacitance			6		pF

† All typical values are at V<sub>CC</sub> = 5 V and T<sub>A</sub> = 25°C.

‡ Δ|V<sub>OD</sub>| and Δ|V<sub>OC</sub>| are the changes in magnitude of V<sub>OD</sub> and V<sub>OC</sub>, respectively, that occur when the input is changed from a high level to a low level.

NOTE 4: This parameter is measured per input. All other inputs are at 0 or 5 V.

**switching characteristics over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted)**

PARAMETER	TEST CONDITIONS	AM26C31C AM26C31I			UNIT
		MIN	TYP†	MAX	
t <sub>PLH</sub> Propagation delay time, low- to high-level output	S1 is open, See Figure 2	3	7	12	ns
t <sub>PHL</sub> Propagation delay time, high- to low-level output	S1 is open, See Figure 2	3	7	12	ns
t <sub>sk(p)</sub> Pulse skew time ( t <sub>PLH</sub> - t <sub>PHL</sub>  )	S1 is open, See Figure 2		0.5	4	ns
t <sub>r(OD)</sub> , t <sub>f(OD)</sub> Differential output rise and fall times	S1 is open, See Figure 3		5	10	ns
t <sub>pZH</sub> Output enable time to high level	S1 is closed, See Figure 4		10	19	ns
t <sub>pZL</sub> Output enable time to low level	S1 is closed, See Figure 4		10	19	ns
t <sub>PHZ</sub> Output disable time from high level	S1 is closed, See Figure 4		7	16	ns
t <sub>PLZ</sub> Output disable time from low level	S1 is closed, See Figure 4		7	16	ns
C <sub>pd</sub> Power dissipation capacitance (each driver) (see Note 5)	S1 is open, See Figure 2		170		pF

† All typical values are at V<sub>CC</sub> = 5 V and T<sub>A</sub> = 25°C.

NOTE 5: C<sub>pd</sub> is used to estimate the switching losses according to P<sub>D</sub> = C<sub>pd</sub> × V<sub>CC</sub><sup>2</sup> × f, where f is the switching frequency.



# AM26C31

## QUADRUPLE DIFFERENTIAL LINE DRIVER

SLLS103K – DECEMBER 1990 – REVISED SEPTEMBER 2004

**electrical characteristics over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted)**

PARAMETER	TEST CONDITIONS	AM26C31Q AM26C31M			UNIT
		MIN	TYP†	MAX	
V <sub>OH</sub> High-level output voltage	I <sub>O</sub> = -20 mA	2.2	3.4		V
V <sub>OL</sub> Low-level output voltage	I <sub>O</sub> = 20 mA		0.2	0.4	V
V <sub>OD</sub>   Differential output voltage magnitude	R <sub>L</sub> = 100 Ω, See Figure 1	2	3.1		V
Δ V <sub>OD</sub>   Change in magnitude of differential output voltage‡	R <sub>L</sub> = 100 Ω, See Figure 1			±0.4	V
V <sub>OC</sub> Common-mode output voltage	R <sub>L</sub> = 100 Ω, See Figure 1			3	V
Δ V <sub>OC</sub>   Change in magnitude of common-mode output voltage‡	R <sub>L</sub> = 100 Ω, See Figure 1			±0.4	V
I <sub>I</sub> Input current	V <sub>I</sub> = V <sub>CC</sub> or GND			±1	μA
I <sub>O(off)</sub> Driver output current with power off	V <sub>CC</sub> = 0	V <sub>O</sub> = 6 V		100	μA
		V <sub>O</sub> = -0.25 V		-100	
I <sub>OS</sub> Driver output short-circuit current	V <sub>O</sub> = 0			-170	mA
I <sub>OZ</sub> High-impedance off-state output current	V <sub>O</sub> = 2.5 V			20	μA
	V <sub>O</sub> = 0.5 V			-20	
I <sub>CC</sub> Quiescent supply current	I <sub>O</sub> = 0	V <sub>I</sub> = 0 V or 5 V		100	μA
	I <sub>O</sub> = 0	V <sub>I</sub> = 2.4 V or 0.5 V, See Note 4		3.2	
C <sub>i</sub> Input capacitance				6	pF

† All typical values are at V<sub>CC</sub> = 5 V and T<sub>A</sub> = 25°C.

‡ Δ|V<sub>OD</sub>| and Δ|V<sub>OC</sub>| are the changes in magnitude of V<sub>OD</sub> and V<sub>OC</sub>, respectively, that occur when the input is changed from a high level to a low level.

NOTE 4: This parameter is measured per input. All other inputs are at 0 V or 5 V.

**switching characteristics over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted)**

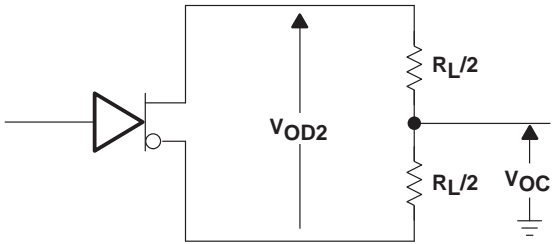
PARAMETER	TEST CONDITIONS	AM26C31Q AM26C31M			UNIT
		MIN	TYP†	MAX	
t <sub>PLH</sub> Propagation delay time, low- to high-level output	S1 is open, See Figure 2		7	12	ns
t <sub>PHL</sub> Propagation delay time, high- to low-level output	S1 is open, See Figure 2		6.5	12	ns
t <sub>sk(p)</sub> Pulse skew time ( t <sub>PLH</sub> - t <sub>PHL</sub>  )	S1 is open, See Figure 2		0.5	4	ns
t <sub>r(OD)</sub> , t <sub>f(OD)</sub> Differential output rise and fall times	S1 is open, See Figure 3		5	12	ns
t <sub>PZH</sub> Output enable time to high level	S1 is closed, See Figure 4		10	19	ns
t <sub>PZL</sub> Output enable time to low level	S1 is closed, See Figure 4		10	19	ns
t <sub>PHZ</sub> Output disable time from high level	S1 is closed, See Figure 4		7	16	ns
t <sub>PLZ</sub> Output disable time from low level	S1 is closed, See Figure 4		7	16	ns
C <sub>pd</sub> Power dissipation capacitance (each driver) (see Note 5)	S1 is open, See Figure 2		100		pF

† All typical values are at V<sub>CC</sub> = 5 V and T<sub>A</sub> = 25°C.

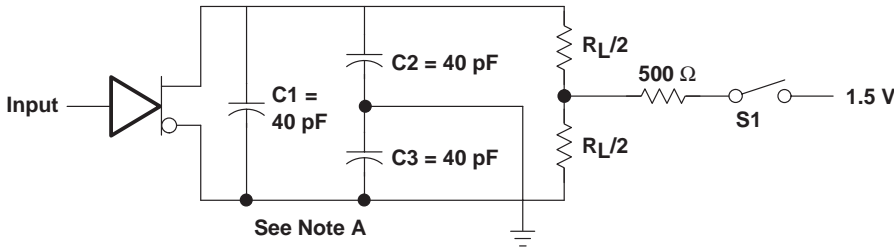
NOTE 5: C<sub>pd</sub> is used to estimate the switching losses according to P<sub>D</sub> = C<sub>pd</sub> × V<sub>CC</sub><sup>2</sup> × f, where f is the switching frequency.



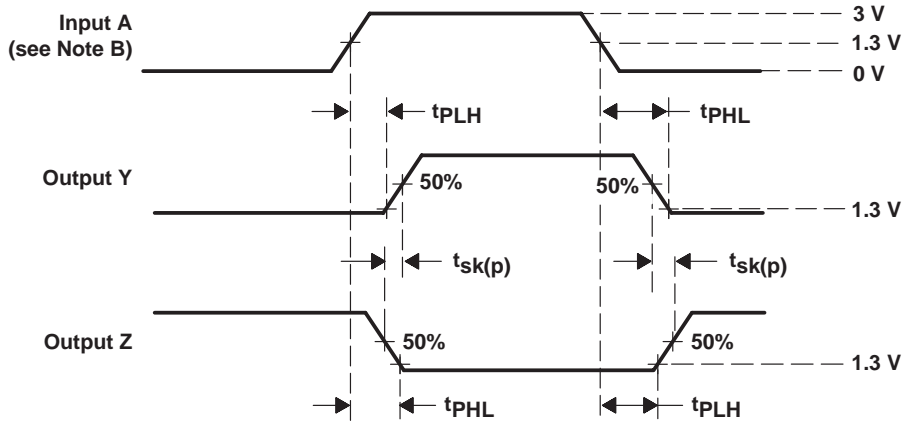
**PARAMETER MEASUREMENT INFORMATION**



**Figure 1. Differential and Common-Mode Output Voltages**



**TEST CIRCUIT**



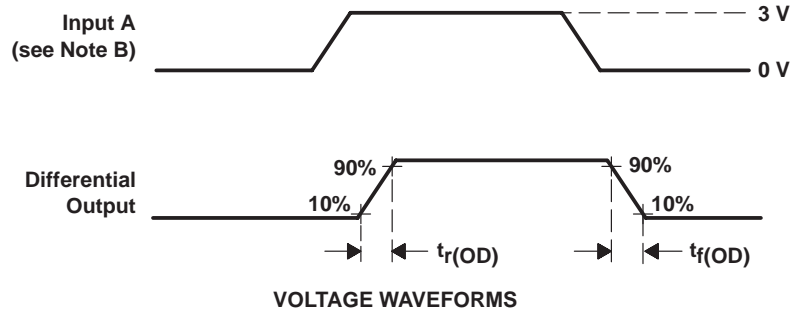
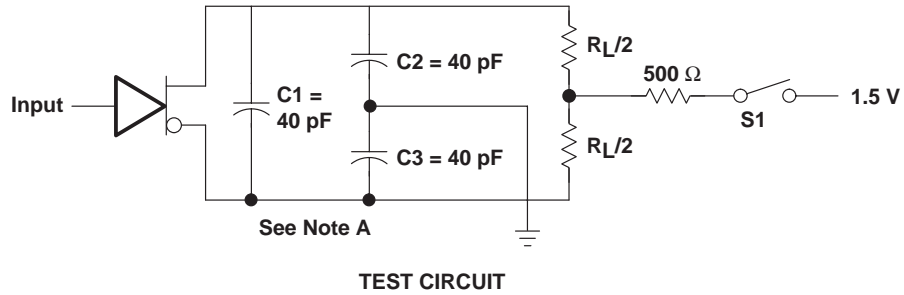
- NOTES: A.  $C1$ ,  $C2$ , and  $C3$  include probe and jig capacitance.  
 B. All input pulses are supplied by generators having the following characteristics:  $PRR \leq 1 \text{ MHz}$ , duty cycle  $\leq 50\%$ , and  $t_r, t_f \leq 6 \text{ ns}$ .

**Figure 2. Propagation Delay Time and Skew Waveforms and Test Circuit**

# AM26C31 QUADRUPLE DIFFERENTIAL LINE DRIVER

SLLS103K – DECEMBER 1990 – REVISED SEPTEMBER 2004

## PARAMETER MEASUREMENT INFORMATION

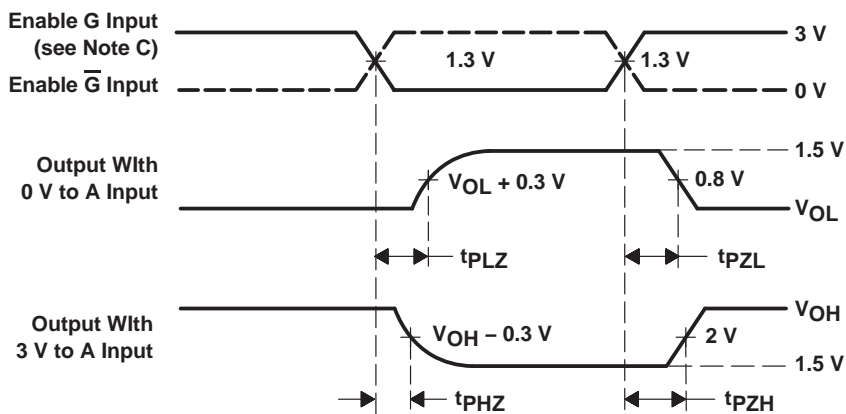
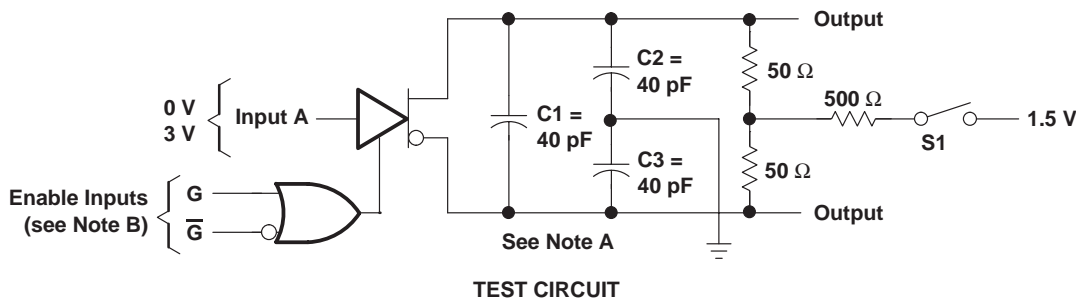


- NOTES: A. C1, C2, and C3 include probe and jig capacitance.  
 B. All input pulses are supplied by generators having the following characteristics: PRR ≤ 1 MHz, duty cycle ≤ 50%, and tr, tf ≤ 6 ns.

Figure 3. Differential-Output Rise- and Fall-Time Waveforms and Test Circuit



PARAMETER MEASUREMENT INFORMATION



- NOTES: A. C1, C2, and C3 includes probe and jig capacitance.  
 B. All input pulses are supplied by generators having the following characteristics: PRR  $\leq$  1 MHz, duty cycle  $\leq$  50%,  $t_r < 6$  ns, and  $t_f < 6$  ns.  
 C. Each enable is tested separately.

Figure 4. Output Enable- and Disable-Time Waveforms and Test Circuit

# AM26C31 QUADRUPLE DIFFERENTIAL LINE DRIVER

SLLS103K – DECEMBER 1990 – REVISED SEPTEMBER 2004

## TYPICAL CHARACTERISTICS

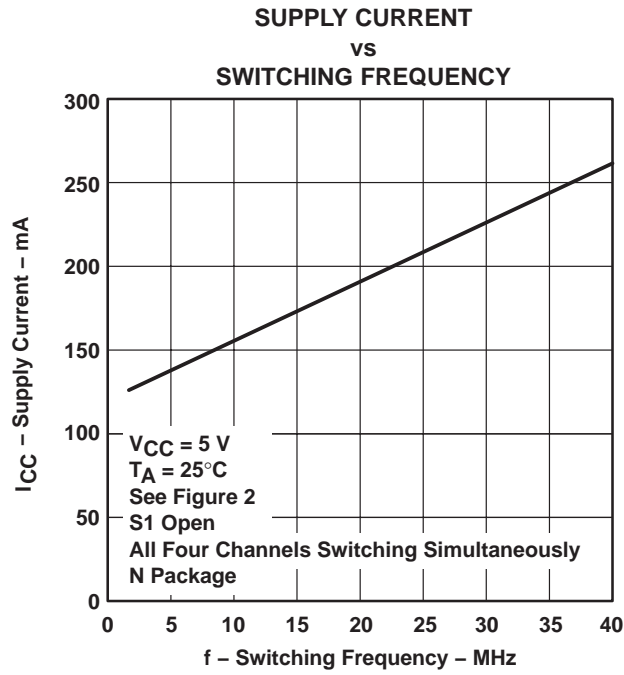


Figure 5

**PACKAGING INFORMATION**

Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins	Package Qty	Eco Plan <sup>(2)</sup>	Lead/Ball Finish	MSL Peak Temp <sup>(3)</sup>
5962-9163901Q2A	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	Level-NC-NC-NC
5962-9163901QEA	ACTIVE	CDIP	J	16	1	TBD	A42 SNPB	Level-NC-NC-NC
5962-9163901QFA	ACTIVE	CFP	W	16	1	TBD	A42 SNPB	Level-NC-NC-NC
AM26C31CD	ACTIVE	SOIC	D	16	40	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1YEAR
AM26C31CDBLE	OBSOLETE	SSOP	DB	16		TBD	Call TI	Call TI
AM26C31CDBR	ACTIVE	SSOP	DB	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
AM26C31CDBRE4	ACTIVE	SSOP	DB	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
AM26C31CDE4	ACTIVE	SOIC	D	16	40	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1YEAR
AM26C31CDR	ACTIVE	SOIC	D	16	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1YEAR
AM26C31CDRE4	ACTIVE	SOIC	D	16	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1YEAR
AM26C31CN	ACTIVE	PDIP	N	16	25	Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC
AM26C31CNSR	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
AM26C31CNSRE4	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
AM26C31ID	ACTIVE	SOIC	D	16	40	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1YEAR
AM26C31IDBLE	OBSOLETE	SSOP	DB	16		TBD	Call TI	Call TI
AM26C31IDBR	ACTIVE	SSOP	DB	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
AM26C31IDBRE4	ACTIVE	SSOP	DB	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
AM26C31IDE4	ACTIVE	SOIC	D	16	40	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1YEAR
AM26C31IDR	ACTIVE	SOIC	D	16	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1YEAR
AM26C31IDRE4	ACTIVE	SOIC	D	16	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1YEAR
AM26C31IN	ACTIVE	PDIP	N	16	25	Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC
AM26C31INE4	ACTIVE	PDIP	N	16	25	Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC
AM26C31INSR	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
AM26C31INSRG4	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
AM26C31IPW	ACTIVE	TSSOP	PW	16	90	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
AM26C31IPWE4	ACTIVE	TSSOP	PW	16	90	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
AM26C31MFKB	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	Level-NC-NC-NC

Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins	Package Qty	Eco Plan <sup>(2)</sup>	Lead/Ball Finish	MSL Peak Temp <sup>(3)</sup>
AM26C31MJB	ACTIVE	CDIP	J	16	1	TBD	A42 SNPB	Level-NC-NC-NC
AM26C31MWB	ACTIVE	CFP	W	16	1	TBD	A42 SNPB	Level-NC-NC-NC
AM26C31QD	ACTIVE	SOIC	D	16	40	TBD	CU NIPDAU	Level-1-220C-UNLIM
AM26C31QDR	ACTIVE	SOIC	D	16	2500	TBD	CU NIPDAU	Level-1-220C-UNLIM

<sup>(1)</sup> The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBsolete:** TI has discontinued the production of the device.

<sup>(2)</sup> Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Green (RoHS & no Sb/Br):** TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

<sup>(3)</sup> MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

**Important Information and Disclaimer:**The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

J (R-GDIP-T\*\*)

14 LEADS SHOWN

CERAMIC DUAL IN-LINE PACKAGE



DIM \ PINS **	14	16	18	20
A	0.300 (7,62) BSC	0.300 (7,62) BSC	0.300 (7,62) BSC	0.300 (7,62) BSC
B MAX	0.785 (19,94)	.840 (21,34)	0.960 (24,38)	1.060 (26,92)
B MIN	—	—	—	—
C MAX	0.300 (7,62)	0.300 (7,62)	0.310 (7,87)	0.300 (7,62)
C MIN	0.245 (6,22)	0.245 (6,22)	0.220 (5,59)	0.245 (6,22)



4040083/F 03/03

- NOTES:
- All linear dimensions are in inches (millimeters).
  - This drawing is subject to change without notice.
  - This package is hermetically sealed with a ceramic lid using glass frit.
  - Index point is provided on cap for terminal identification only on press ceramic glass frit seal only.
  - Falls within MIL STD 1835 GDIP1-T14, GDIP1-T16, GDIP1-T18 and GDIP1-T20.

W (R-GDFP-F16)

CERAMIC DUAL FLATPACK



- NOTES:
- All linear dimensions are in inches (millimeters).
  - This drawing is subject to change without notice.
  - This package can be hermetically sealed with a ceramic lid using glass frit.
  - Index point is provided on cap for terminal identification only.
  - Falls within MIL STD 1835 GDFP1-F16 and JEDEC MO-092AC

FK (S-CQCC-N\*\*)

LEADLESS CERAMIC CHIP CARRIER

28 TERMINAL SHOWN



- NOTES:
- A. All linear dimensions are in inches (millimeters).
  - B. This drawing is subject to change without notice.
  - C. This package can be hermetically sealed with a metal lid.
  - D. The terminals are gold plated.
  - E. Falls within JEDEC MS-004

N (R-PDIP-T\*\*)

PLASTIC DUAL-IN-LINE PACKAGE

16 PINS SHOWN



- NOTES:
- A. All linear dimensions are in inches (millimeters).
  - B. This drawing is subject to change without notice.
  - Falls within JEDEC MS-001, except 18 and 20 pin minimum body length (Dim A).
  - The 20 pin end lead shoulder width is a vendor option, either half or full width.





# MECHANICAL DATA

NS (R-PDSO-G\*\*)

PLASTIC SMALL-OUTLINE PACKAGE

14-PINS SHOWN



- NOTES:
- A. All linear dimensions are in millimeters.
  - B. This drawing is subject to change without notice.
  - C. Body dimensions do not include mold flash or protrusion, not to exceed 0,15.

DB (R-PDSO-G\*\*)

PLASTIC SMALL-OUTLINE

28 PINS SHOWN



- NOTES: A. All linear dimensions are in millimeters.  
 B. This drawing is subject to change without notice.  
 C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.  
 D. Falls within JEDEC MO-150

PW (R-PDSO-G\*\*)

PLASTIC SMALL-OUTLINE PACKAGE

14 PINS SHOWN



4040064/F 01/97

- NOTES: A. All linear dimensions are in millimeters.  
 B. This drawing is subject to change without notice.  
 C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.  
 D. Falls within JEDEC MO-153

## IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, modifications, enhancements, improvements, and other changes to its products and services at any time and to discontinue any product or service without notice. Customers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All products are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its hardware products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by government requirements, testing of all parameters of each product is not necessarily performed.

TI assumes no liability for applications assistance or customer product design. Customers are responsible for their products and applications using TI components. To minimize the risks associated with customer products and applications, customers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any TI patent right, copyright, mask work right, or other TI intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information published by TI regarding third-party products or services does not constitute a license from TI to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. Reproduction of this information with alteration is an unfair and deceptive business practice. TI is not responsible or liable for such altered documentation.

Resale of TI products or services with statements different from or beyond the parameters stated by TI for that product or service voids all express and any implied warranties for the associated TI product or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

Following are URLs where you can obtain information on other Texas Instruments products and application solutions:

<b>Products</b>		<b>Applications</b>	
Amplifiers	<a href="http://amplifier.ti.com">amplifier.ti.com</a>	Audio	<a href="http://www.ti.com/audio">www.ti.com/audio</a>
Data Converters	<a href="http://dataconverter.ti.com">dataconverter.ti.com</a>	Automotive	<a href="http://www.ti.com/automotive">www.ti.com/automotive</a>
DSP	<a href="http://dsp.ti.com">dsp.ti.com</a>	Broadband	<a href="http://www.ti.com/broadband">www.ti.com/broadband</a>
Interface	<a href="http://interface.ti.com">interface.ti.com</a>	Digital Control	<a href="http://www.ti.com/digitalcontrol">www.ti.com/digitalcontrol</a>
Logic	<a href="http://logic.ti.com">logic.ti.com</a>	Military	<a href="http://www.ti.com/military">www.ti.com/military</a>
Power Mgmt	<a href="http://power.ti.com">power.ti.com</a>	Optical Networking	<a href="http://www.ti.com/opticalnetwork">www.ti.com/opticalnetwork</a>
Microcontrollers	<a href="http://microcontroller.ti.com">microcontroller.ti.com</a>	Security	<a href="http://www.ti.com/security">www.ti.com/security</a>
		Telephony	<a href="http://www.ti.com/telephony">www.ti.com/telephony</a>
		Video & Imaging	<a href="http://www.ti.com/video">www.ti.com/video</a>
		Wireless	<a href="http://www.ti.com/wireless">www.ti.com/wireless</a>

Mailing Address: Texas Instruments  
Post Office Box 655303 Dallas, Texas 75265