

| CHANGE NOTIFICATION HISTORY | | | | |
|-----------------------------|--------------------|--|--------|--|
| Version | Date of Version | History | Remark | |
| 1 | 2005/7/1 | Resistance range: 10Ω 1M Ω , 0Ω | | |
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Customer: FARNELL IN ONE

1. Scope:

This specification for approval relates to Chip Kit Resistors (Lead Free) manufactured by ROYAL OHM 's specifications.

2. Type designation:

The type designation shall be in the following form:

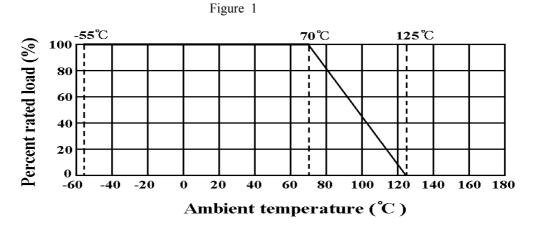
| Туре | Power Rating | Resistance tolerance | Nominal Resistance |
|----------|--------------|----------------------|--------------------|
| RMC 0805 | 1/10 W | F | 1ΚΩ |

3. Ratings:

| Туре | RMC 0805 | |
|-----------------------|----------|--|
| Power Rating | 0.10 W | |
| Max. Working Voltage | 150 V | |
| Max. Overload Voltage | 300 V | |
| Temperature Range | -55 +125 | |
| Ambient Temperature | 70 | |

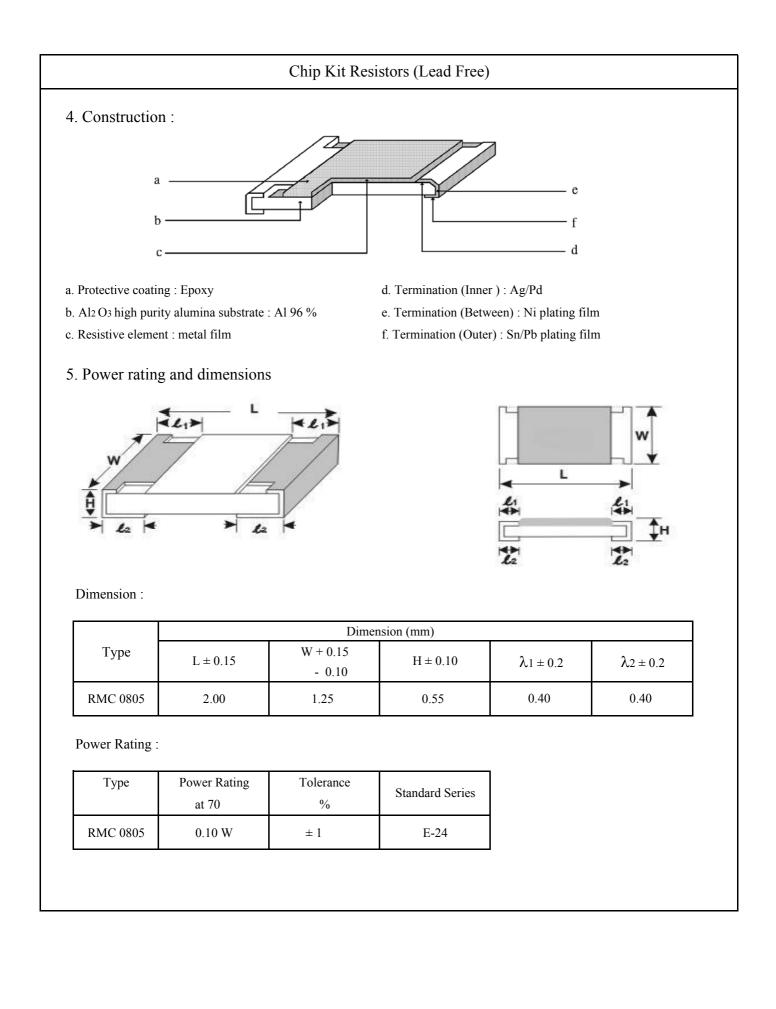
3.1 Power rating:

Resistors shall have a power rating based on continuous load operation at an ambient temperature of 70 . For temperature in excess of 70 , The load shall be derate as shown in figure 1.



3.2 Nominal Resistance

Effective figures of nominal resistance shall be in accordance with E-24 and E-96 series for 1 % and E-24 series for 2 % and 5 %



| | Chip Kit Res | sistors (Lead Fre | ee) | | |
|--------------------------------------|---|--|---|-----------------|--|
| 7. Performance s | specification : | | | | |
| Characteristics | Limits | Test Methods | | | |
| Characteristics | Linits | (JIS C 5201-1) | | | |
| | | 5.2 Natural resistance change per temp. | | | |
| | | degree centigrade. | | | |
| | | R2-R1 | | | |
| Temperature | $10\Omega 100\Omega \pm 200 \text{ PPM}/$ | x 10 ⁶ (PPM/) | | | |
| coefficient | 101Ω $1M\Omega$ ± 100 PPM/ | R1(t2-t1) | | | |
| | | | e value at room tempera | | |
| | | R2: Resistance value at room temp. plus 100 (t2) | | | |
| Short time Resistance change rate is | | 5.5 Permanent resistance change after the | | | |
| overload | $\pm (1.0\% + 0.1\Omega)$ Max. | application of a potential of 2.5 times RCWV | | S RCWV | |
| | | for 5 seconds | | | |
| Insulation | 1,000 MΩ or more | 5.6 Apply 500V DC between protective coating | | ve coating | |
| resistance | | and termination for 1 min, then measure | | | |
| Dielectric | No evidence of flashover | 5.7 Apply 500V AC between protective coating | | ve coating | |
| withstanding | mechanical damage, arcing or | and termination for 1 minute | | | |
| voltage | insulation break down | | | | |
| | | 6.1.4 Twist of | f Test Board : | | |
| Terminal bending | $\pm (1.0\% + 0.05\Omega)$ Max. | Y/X = 5/90 mm for 10 seconds | | | |
| | | 7.4 Resistance change after continuous | | IS | |
| | | 5 cycles for a | luty cycle specified belo | W : | |
| | | Step | Temperature | Time | |
| Temperature | $\pm (0.5\% + 0.05\Omega)$ Max. | 1 | -55 ± 3 | 30 mins | |
| cycling | | 2 | Room temp. | 10 15 mins | |
| | | 3 | $+125 \pm 2$ | 30 mins | |
| | | 4 | Room temp. | 10 15 mins | |
| | | | 7.9 Resistance change after 1,000 hours | | |
| Load life in | Resistance change rate is | (1.5 hours "on", 0.5 hour "off") at RCWV in a humidity chamber controlled at | | CWV | |
| humidity | $\pm (1.0\% + 0.1\Omega)$ Max. | | | | |
| | | 40 ± 2 and | 40 ± 2 and 90 to 95 % relative humidity | | |
| | | 7.10 Permanent resistance change after 1,000 hours | | | |
| Load Life | Resistance change rate is | operating at RCWV, with duty cycle of | | | |
| | $\pm (1.0\% + 0.1\Omega)$ Max. | (1.5 hours"on | ", 0.5 hour"off") at 70 | ± 2 ambient | |

| Chip Kit Resistors (Lead Free) | | | | | |
|--------------------------------|--|---|--|--|--|
| 7. Performance specification : | | | | | |
| Characteristics | Limits | Test Methods (JIS C 5201-1) | | | |
| Soldering Heat | Electrical characteristics shall be satisfied. Without distinct deformation in appearance. | Solder bath methodPre-heat : 100 to 105 , 30 ± 5 sec.Temperature : 265 ± 3 , $5 \pm 1/-0$ sec.Reflow soldering methodPeak : $250 \pm 5/-0$ 230 or higher 30 ± 10 Sec. | | | |
| | | Soldering iron method Bit temperature : 350 ± 10 Application time of soldering iron : 3 +1/-0sec. | | | |
| Solderability | 95 % coverage Min. | 6.5 Test temperature of solder : 245 ± 3 Dipping them solder : 2~3 seconds | | | |
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