

AC/DC Front End Power Supply + S1U Power Shelf



**The D1U-W-1600** is a 1600 Watt, power-factor-corrected (PFC) front-end power supply for hot-swapping redundant systems. The main output is 48V and standby output of either 12V, 5V or 3.3V. Packaged in 1U low profile, it is designed to deliver reliable bulk power to servers, workstations, storage systems or any 48V distributed power architecture systems requiring high power density. The highly efficient electrical and thermal design with internal cooling fans supports reliable operation conditions. The D1U-W-1600 is designed to auto-recover from over-temperature faults. Status information is provided with front panel LEDs, logic signals and I<sup>2</sup>C management interface. Three units can be packaged into an optional 19" 1U power shelf to provide up to 4.8kW of power.

**The S1U-3X** is a 1U x 19" EIA Rack Mount Power Shelf designed for holding three D1U Front End Power Supplies in current sharing applications. It is intended for distributed power architecture applications in the Servers, Storage Networking and Data Communications markets. There are two lug terminal connections for #2 AWG cabling for the DC output. System connection through the  $I^2C$  bus reports the performance status of the power supplies within the power shelf. Two Power Shelves can operate in parallel by an optional Shelf-to-Shelf cable, doubling the power output to the maximum capability of 9.6kW for two 48V power shelves.

SELECTION GUIDE							
Part Number	Power Output High Line AC	Power Output Low Line AC	Main Output	Standby Output	Airflow		
D1U-W-1600-48-HC2C	1600W	1200W	48V	3.3V	Back to front		
D1U-W-1600-48-HA2C	1600W	1200W	48V	5V	Back to front		
D1U-W-1600-48-HB2C	1600W	1200W	48V	12V	Back to front		
D1U-W-1600-48-HC1C	1600W	1200W	48V	3.3V	Front to back		
D1U-W-1600-48-HA1C	1600W	1200W	48V	5V	Front to back		
D1U-W-1600-48-HB1C	1600W	1200W	48V	12V	Front to back		
Part Number	Description						
S1U-3X-16-A-48-RC	Power shelf for 48V D1U						

INPUT CHARACTERISTICS					
Parameter	Conditions	Min.	Тур.	Max.	Units
Input Voltage Operating Pange	Low Line AC	90		140	Vac
Input Voltage Operating Range	High Line AC	180		264	Vac
Input Frequency		47	50/60	63	Hz
Turn-on Input Voltage	Ramp up	78.5		86.5	Vac
Turn-off Input Voltage	Ramp down	70.5		78	Vac
Maximum Input Current	Low Line AC 90Vac			15	Arms
Maximum input Guirent	High Line AC 180Vac			10	AIIIIS
Inrush Current	Cold start between 0-1msec			90	Apk
Power Factor	Output load >90%	95%			
	Output load >50%	75%			



#### **FEATURES**

- RoHS compliant
- 1600W (220Vac), 1200W (110Vac) Output power
- 48V Main output, 3.3V, 5V or 12V standby output
- 1U sized; dimensions 12" x 4.75" x 1.6"
- 17.5 Watts per cubic inch density
- N+1 redundancy capable, including hot-docking
- Active current sharing on main output
- Over-voltage, over-current, over-temperature protection
- Internal cooling fans
- I<sup>2</sup>C Bus Interface with status indicators
- Optional 1U x 19" power-shelf









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OUTPUT	VOLTAGE CHARACTERISTICS					
Output Voltage	Parameter	Conditions	Min.	Тур.	Max.	Units
	Voltage Set Point Accuracy			48		Vdc
	Line and Load Regulation		46.54		49.44	Vuc
48V	Ripple Voltage & Noise <sup>1</sup>	20MHz Bandwidth			480	mV p-p
	Output Current		2		33	Α
	Load Capacitance				10000	μF
	Voltage Set Point Accuracy			3.3		Vdc
	Line and Load Regulation		3.2		3.4	Vuc
3.3Vsb	Ripple Voltage & Noise <sup>1</sup>	20MHz Bandwidth			50	mV p-p
	Operating Range		0		4.5	Α
	Load Capacitance				1530	μF
	Voltage Set Point Accuracy			5		Vdc
	Line and Load Regulation		4.85		5.15	Vuc
5Vsb	Ripple Voltage & Noise <sup>1</sup>	20MHz Bandwidth			50	mV p-p
	Operating Range		0		4	Α
	Load Capacitance				1530	μF
	Voltage Set Point Accuracy			12		Vdc
	Line and Load Regulation		11.6		12.4	VUU
12Vsb	Ripple Voltage & Noise <sup>1</sup>	20MHz Bandwidth			120	mV p-p
	Operating Range		0		1.7	Α
	Load Capacitance				1530	μF

Parameter	Conditions	Min.	Тур.	Max.	Units
Remote Sense			240		mV
Efficiency	220Vac		90.6		%
Output Rise Monotonicity	Overshoot less than 10% for all outputs, n	o voltage negative	e between 10%	to 95% during ran	np up
Chart up Time	AC ramp up		1.5		S
Start-up Time	PS_On activated		150		ms
	48V Ramp 1A/µs, 50% load step			±2700	
Transiant Decrease	3.3Vsb Ramp 1A/µs, 50% load step			±165	\ <i>I</i>
ransient Response	5Vsb Ramp 1A/µs, 50% load step			±250	mV
	12Vsb Ramp 1A/µs, 50% load step			±600	
Current sharing accuracy (up to 6 in parallel)	At 100% load			±10	%
Hot Swap Transients	All outputs within regulation				
Hold-up Time	Max. load, nominal Vin	20			ms

GENERAL CHARACTERISTICS								
Parameter	Conditions	Min.	Тур.	Max.	Units			
Storage Temperature Range	Non-condensing	-40		70	°C			
Operating Temperature Range		0		50				
Operating Humidity	Non-condensing	10		90	%			
Storage Humidity		5		90	70			
Shock	30G non operating							
Sinusoidal Vibration	0.5G, 5 – 500 Hz operating							
MTDE	Calculated per Bellcore at Ta=30°C	200			Khrs			
MTBF	Demonstrated	200			Khrs			
Acoustic	ISO 7779-1999			60	dB LpAm			
Safety Approvals	c-CSA-us (CSA 60950-1-03/UL 60950-1, TUV approval (Bauart) EN 60950-1:2001	First Edition)						
Input Fuse	Power Supply has internal 20A/250V	fast blow fuse o	n the AC line ir	nput				
Material Flammability	UL 94V-0							
Switching Frequency	90KHz for Boost PFC Converter 165KHz for Main Output Converter 200KHz for Standby Output Converter	165KHz for Main Output Converter						
Weight	2.1kg							

<sup>&</sup>lt;sup>1</sup> Ripple and noise are measured with 0.1 uF of ceramic capacitance and 10 uF of tantalum capacitance on each of the power supply outputs. The output noise requirements apply over a 0 Hz to 20 MHz bandwidth. A short coaxial cable with 50ohm scope termination is used.



PROTECT	PROTECTION CHARACTERISTICS										
Output Voltage	Parameter	Conditions	Min.	Тур.	Max.	Units					
	Over-temperature	Auto-restart	55		65	°C					
48V	Over Voltage	Latching	54		59	V					
400	Over Current	Latching	37		42	Α					
12Vsb	Over Voltage	Latching	13		14	V					
12780	Over Current	Latching	2.5		3	Α					
3.3Vsb	Over Voltage	Latching	3.57		4.02	V					
3.3780	Over Current	Latching	6.5		8	Α					
5Vsb	Over Voltage	Latching	5.6		6	V					
3780	Over Current	Latching	5		7	Α					

ISOLATION CHARACTERISTICS						
Parameter	Conditions	Min.	Тур.	Max.	Units	
Inculation Cafety Poting / Test Voltage	Input to Output - Reinforced	3000			Vrms	
Insulation Safety Rating / Test Voltage	Input to Chassis - Basic	1500			Vrms	
Isolation	Output to Chassis					
ISOIdtion	Output to Output					
Material Flammability	UL 94V-0					
Grounding	Main Output Return and Standby Output Return are connected internally. 100kΩ resistor parallel with 100nF capacitor is connected between Return and power supply chassis. Main Output Return should be connected the System Chassis.					

CONTROL SIGNALS		
Status	Conditions	Description
	Off	No AC input to all PS
LED	Flashing Yellow	Power Supply Failure
LLD	Flashing Green	Main Output Absent
	Green	Power Supply Good
	Status	PS-ON, PGOOD, ACOK, PS_BAD, FANFAIL, OT Warning & shutdown, AC Range
	Output Fault	48V OV, 48V UV, 48V OC, Vsb Fail, Fan1 Fail, Fan2 Fail
I <sup>2</sup> C Registers	48V Output	8 bit scaled output voltage
	48V	8 bit scaled output current
	Fan1 Monitor	8 bit scaled output current
	Fan2 Monitor	8 bit scaled output current

EMISSIONS AND IMMUNITY		
Characteristic	Description	Criteria
Harmonics	IEC/EN 61000-3-2	
Voltage Fluctuation and Flicker	IEC/EN 61000-3-3	
Emission Conducted	FCC 47 CFR Parts 15/CISPR 22/EN55022	Class A, 6dB margin
Emission Radiated	FCC 47 CFR Parts 15/CISPR 22/EN55022	Class A, 6dB margin
		4kV contact discharge
ESD	IEC/EN 61000-4-2	8kV operational air discharge
		15kV non-operational air discharge
Electromagnetic Field	IEC/EN 61000-4-3	
Electrical Fast Transients/Burst	IEC/EN 61000-4-4	
Surge	IEC/EN 61000-4-5	1kV/2kV, Performance Criteria B
RF Conducted Immunity	IEC/EN 61000-4-6	3 Vac, 80% AM, 1kHz, Performance Criteria A
Magnetic Immunity	IEC/EN 61000-4-8	3 A/m
Voltage dips, interruptions	IEC/EN 61000-4-11	



OUTPUT CONNECT	OR AND S	SIGNAL SP	ECIFICATION	ON									
DC and Signal Conn	ector: Ty	/co Part # 1	1-6450332	-7, or FCI	PowerBlad	e # 51732	-028						
	P1	P2	P3	P4	P5	P6	x1	x2	>	(3	x4	х5	
							AC_OK	P_GOOD		_sb DUT	V_SB RETURN	V_sb RETURN	D
	<b>V</b> out	Vout	Vоит	Vrtn	V <sub>RTN</sub>	V <sub>RTN</sub>	PS_ON	V_SB +OUT		_sb DUT	V_SB RETURN	V_sb RETURN	С
	Voui	<b>V</b> 001	<b>V</b> 001	VRIN	VRIN	VRIN	I_SHARE	I <sup>2</sup> C ADRO	I <sup>2</sup> C A	ADR1	I <sup>2</sup> C ADR2	PS_ PRESEN	В
							PS_KILL	V <sub>OUT</sub> SENSE+		OUT NSE-	I <sup>2</sup> C DATA	I <sup>2</sup> C CLOCK	A
Pin Assignment	Sig	gnal Name		Descrip	tion					High Low I			I Max
P1, P2, P3	Vou	JΤ		Main ou	tput voltage	)							
P4, P5, P6	VRT	ΓN			tput voltage								
A2	Se	nse +		Vout remote ser		Vout remote sense, positive node input, connected to the +ve load point							
A3	Se	nse -			$\ensuremath{\text{Vout}}$ remote sense, negative node input, connected to the -ve load point				е				
C2, C3, D3	V_	SB		Standby	Standby voltage output								
C4, C5, D4, D5	V_	se Return		Standby voltage, return, tied internally to Output Return									
B1	1_8	Share		Active lo	Active load sharing bus					0 – 8V			-4 mA / +5 mA
D1	AC	C_0K			Input AC Voltage "OK" signal output (Internal pull up is $10 k \Omega$ to Vsb)					>2.4V (active, 0K) <0.4V			+4 mA -2 mA
D2	P_	Good		Power g	jood signal	output (Inte	rnal pull up i	s 10kΩ to Vsl	b)	>2.4\ <0.4\	/ (active, God /	od)	+4 mA -2 mA
A1	PS	_Kill		first-bre	pin will tur ak contact n disabling	for hot plug	ging). This si	st-make and gnal override	s		/ (open, or \ / (active, PS		N/A
B5	PS	_Present		Internal	ly tied to Vsl	b return				0 V			
C1	Internal 1K ohm pull-up to Vsb, (accepts open collect drain drive), This signal to be pulled low to turn-on pull-up to Vsb, (accepts open collect drain drive), This signal to be pulled low to turn-on pull-up to Vsb, (accepts open collect drain drive), This signal to be pulled low to turn-on pull-up to Vsb, (accepts open collect drain drive).				er		/ (open, or \ / (active, PS		-4 mA -1 mA				
A4	I <sup>2</sup> C	Data		I <sup>2</sup> C seria	al data bus					Vsb			
A5	I <sup>2</sup> C	Clock		I <sup>2</sup> C seria	al clock bus					Vsb			
B2	I <sup>2</sup> C	Adr0		Address	input 0, int	ernal pull-u	p to Vsb			>2.1\ <0.8\	/, < Vsb /		±1 mA
В3	I <sup>2</sup> C	Adr1		Address	input 1, int	ernal pull-u	p to Vsb			>2.1\ <0.8\	/, <vsb /</vsb 		±1 mA
B4	I <sup>2</sup> C	Adr2		Address	input 2, int	ernal pull-u	p to Vsb			>2.1\ <0.8\	/, <vsb /</vsb 		±1 mA

D1U MATING C	D1U MATING CONNECTORS											
48V D1U mat-	Pres	s Fit	Solder <sup>2</sup>									
ing connector	Straight	Right Angle	Straight	Right Angle								
MPS	N/A	Pending	N/A	36-0440026-0								
FCI	51742-10602000CALF	51762-10602000CBLF	51742-10602000AALF	51762-10602000ABLF								
Тусо	TBD	TBD	TBD	TBD								

 $<sup>^{\</sup>rm 2}$  Solder connector recommended for board thickness of <0.090



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CONNECTOR TO (	CUSTOMER SYSTEM			
	MOLEX # 39-28-5204 (	DR TYCO # 281282-1		
Mating Connector:	MOLEX # 0039521204			
Pin Assignment	Signal Name	Description	High Level Low Level	I Max
1	AC_0K1 <sup>1</sup>	Input AC Voltage 'OK' signal output for the 2nd shelf	open drain < 0.7V	- 2 mA + 4 mA
2	P_Good1 <sup>2</sup>	Power good signal output for the 2nd shelf	open drain < 0.7V	- 2 mA + 4 mA
3	PS_0n1³	Power enable for the 2nd shelf	> 2.1V (open, or Vsb) < 0.7V (active, PS:0n)	- 1 mA - 4 mA
4	NOT USED		, , ,	
5	AC_0K0¹	Input AC Voltage "OK" signal output for the local shelf	open drain < 0.7V	- 2 mA + 4 mA
6	P_Good0 <sup>2</sup>	Power good signal output for the local shelf	open drain < 0.7V	- 2 mA + 4 mA
7	PS_0n0³	Power enable for the local shelf	> 2.1V (open, or Vsb) < 0.7V (active, PS:0n)	- 1 mA - 4 mA
8	NOT USED		, , ,	
9	I <sup>2</sup> C Adr2	Address input 2	> 2.1V, < Vsb < 0.8V	± 1 mA
10	I <sup>2</sup> C Clock <sup>4</sup>	I <sup>2</sup> C serial clock bus	Vsb	
11	I <sup>2</sup> C Data <sup>4</sup>	I <sup>2</sup> C serial data bus	Vsb	
12	I_SHARE			
13	SENSE +5			
14	SENSE -5			
15	Vsb	Standby voltage output		
16	Vsb	Standby voltage output		
17	Vsb	Standby voltage output		
18	GND	GROUND		
19	GND	GROUND		
20	GND	GROUND		

All control signals are with respect to Ground. Negative currents exit the power supply.

<sup>&</sup>lt;sup>5</sup> Short Sense+ to +Vout and Sens- to GND at the point of load

SHELF TO SHELF CONNECTION Signal Connector: MOLEX # 39-28-5164 OR TYCO # 281281-1					
1	AC_0K1 <sup>1</sup>	Input AC Voltage 'OK' signal output for the 2nd shelf	open drain < 0.7V	- 2 mA + 4 mA	
2	P_Good1 <sup>2</sup>	Power good signal output for the 2nd shelf	open drain < 0.7V	- 2 mA + 4 mA	
3	PS_0n1 <sup>3</sup>	Power enable for the 2nd shelf	> 2.1V (open, or Vsb) < 0.7V (active, PS:0n)	- 1 mA - 4 mA	
4	NOT USED				
5	NOT USED				
6	I <sup>2</sup> C Clock⁴	I <sup>2</sup> C serial clock bus	Vsb		
7	I <sup>2</sup> C Data⁴	I <sup>2</sup> C serial data bus	Vsb		
8	I_SHARE				
9	SENSE +5				
10	SENSE -5				
11	Vsb	Standby voltage output			
12	Vsb	Standby voltage output			
13	Vsb	Standby voltage output			
14	GND	GROUND			
15	GND	GROUND			
16	GND	GROUND			

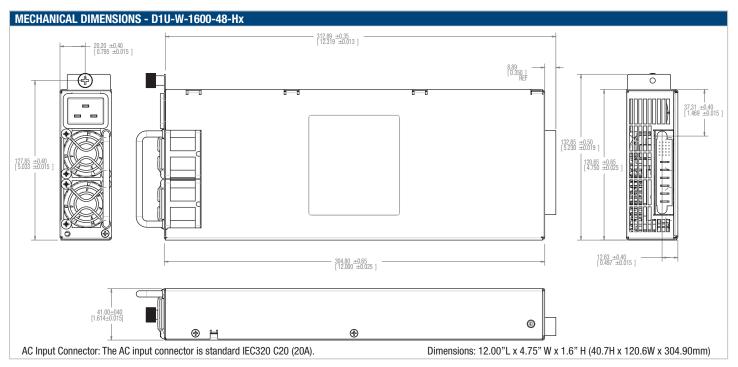
<sup>&</sup>lt;sup>1</sup> Signal goes low when any one of the three power supplies loses AC

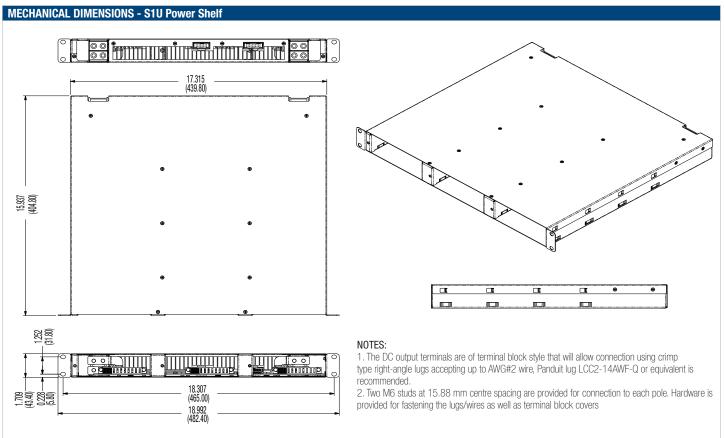
<sup>&</sup>lt;sup>2</sup> Signal goes low when any one of the three power supplies fail

In a standalone shelf (without I2C control) Pull this pin to GND to turn on three power supplies at the same time. With I2C control, leave this signal float and Use I2C to turn on one power supply at a time.

<sup>&</sup>lt;sup>4</sup> Recomended 10K0hm pull up resistor to host 3.3 or 5V rail

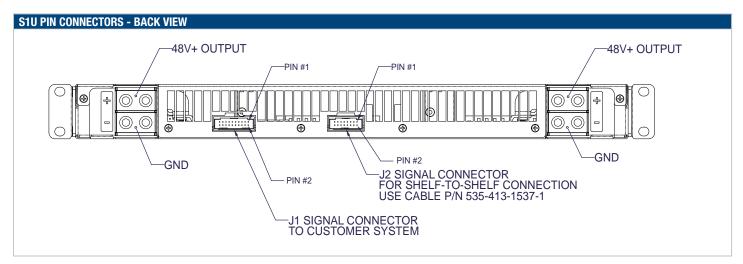


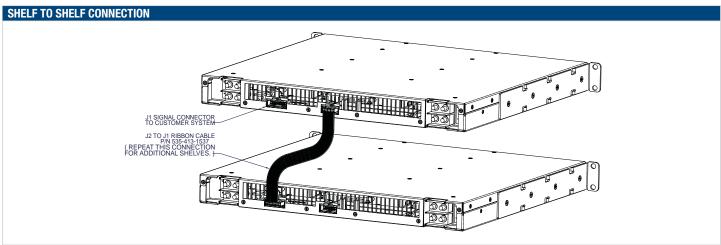






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OPTIONAL ACCESSORIES				
Description	Part Number			
48V D1U output connector card	D1U-48-CONC			
Shelf to shelf cable	535-413-1537			

APPLICATION NOTES				
Document Number	Description			
ACAN-29	D1U Communication Protocol			

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