**Technologies** 



2N5460 2N5461 2N5462

# **MMBF5460 MMBF5461**





# **P-Channel General Purpose Amplifier**

This device is designed primarily for low level audio and general purpose applications with high impedance signal sources. Sourced from Process 89.

## **Absolute Maximum Ratings\***

TA = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
$V_{DG}$	Drain-Gate Voltage	- 40	V
$V_{GS}$	Gate-Source Voltage	40	V
I <sub>GF</sub>	Forward Gate Current	10	mA
T <sub>J</sub> ,T <sub>stg</sub>	Operating and Storage Junction Temperature Range	-55 to +150	°C

<sup>\*</sup>These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

1) These ratings are based on a maximum junction temperature of 150 degrees C.

2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

## **Thermal Characteristics**

TA = 25°C unless otherwise noted

Symbol	Characteristic	Max		Units
		2N5460	*MMBF5460	
$P_D$	Total Device Dissipation Derate above 25°C	625 5.0	350 2.8	mW mW/∘C
$R_{\theta JC}$	Thermal Resistance, Junction to Case	83.3		°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	200	357	°C/W

<sup>\*</sup>Device mounted on FR-4 PCB 1.6" X 1.6" X 0.06."

# **P-Channel General Purpose Amplifier**

1.0

1.0

60

2.0

2.5

рF

dΒ

nV/ÖHz

(continued)

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
	ARACTERISTICS					
		T	40	1	1	V
$V_{(BR)GSS}$	Gate-Source Breakdown Voltage	$I_{G} = 10 \mu\text{A},  V_{DS} = 0$	40			
$I_{GSS}$	Gate Reverse Current	$V_{GS} = 20 \text{ V}, V_{DS} = 0$			5.0	nA
.,		$V_{GS} = 20 \text{ V}, V_{DS} = 0, T_A = 100^{\circ}\text{C}$			1.0	μA
$V_{GS(off)}$	Gate-Source Cutoff Voltage	$V_{DS} = 15 \text{ V}, I_D = 1.0 \mu\text{A}$ 2N54			6.0	V
		2N54			7.5 9.0	V
V <sub>GS</sub>	Gate-Source Voltage	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 0.1 mA <b>2N5</b> 4	<del>-</del>		4.0	V
VGS	Gate-Source voltage	$V_{DS} = 15 \text{ V}, I_D = 0.1 \text{ IIIA}$ <b>2N54</b> $V_{DS} = 15 \text{ V}, I_D = 0.2 \text{ mA}$ <b>2N54</b>			4.5	V
		$V_{DS} = 15 \text{ V}, I_D = 0.2 \text{ mA}$ <b>2N54</b> $V_{DS} = 15 \text{ V}, I_D = 0.4 \text{ mA}$ <b>2N54</b>			6.0	V
ON CHAP	RACTERISTICS  Zero-Gate Voltage Drain Current*	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0 2N54 2N54	<b>61</b> - 2.0		- 5.0 - 9.0	mA mA
		2N54	<b>62</b> - 4.0		- 16	mA
SMALL S	IGNAL CHARACTERISTICS Forward Transfer Conductance	$V_{DS} = 15, V_{GS} = 0, f = 1.0 \text{ kHz}$	62   -4.0		- 16	
					4000	
		$V_{DS} = 15$ , $V_{GS} = 0$ , $f = 1.0 \text{ kHz}$ 2N54 2N54	<b>60</b> 1000 <b>61</b> 1500		4000 5000	mA
	Forward Transfer Conductance	$V_{DS} = 15$ , $V_{GS} = 0$ , $f = 1.0 \text{ kHz}$ 2N54 2N54 2N54	<b>60</b> 1000 <b>61</b> 1500		4000 5000 6000	mA μmhos μmhos μmhos
		$V_{DS} = 15$ , $V_{GS} = 0$ , $f = 1.0 \text{ kHz}$ 2N54 2N54 2N54 $V_{DS} = 15$ , $V_{GS} = 0$ , $f = 1.0 \text{ kHz}$	<b>60</b> 1000 <b>61</b> 1500		4000 5000	mA μmhos μmhos
gfs	Forward Transfer Conductance	$V_{DS} = 15$ , $V_{GS} = 0$ , $f = 1.0 \text{ kHz}$ 2N54 2N54 2N54	<b>60</b> 1000 <b>61</b> 1500	5.0	4000 5000 6000	mA μmhos μmhos μmhos

 $V_{DS} = 15, V_{GS} = 0, f = 1.0 \text{ MHz}$ 

 $V_{DS} = 10$ ,  $V_{GS} = 0$ , I = 1.0 MHz  $V_{DS} = 15$  V,  $V_{GS} = 0$ ,  $R_G = 1.0$  megohm, f = 100 Hz,  $R_{DS} = 15$  V,  $R_{DS} = 0$ ,  $R_{DS} = 100$  Hz,  $R_{DS} = 15$  V,  $R_{DS} = 0$ ,  $R_{DS} = 100$  Hz,

Noise Figure

Noise Voltage

NF

 $e_{n}$ 

Reverse Transfer Capacitance

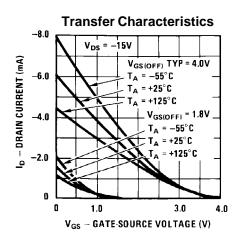
Equivalent Short-Circuit Input

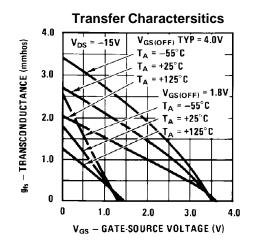
<sup>\*</sup>Pulse Test: Pulse Width £ 300 ms, Duty Cycle £ 2%

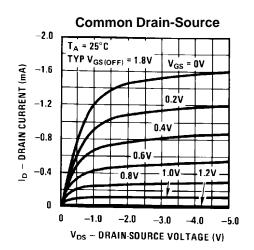
## **P-Channel General Purpose Amplifier**

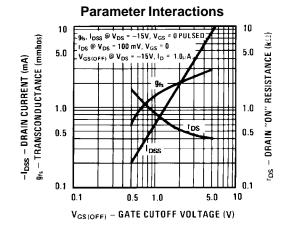
(continued)

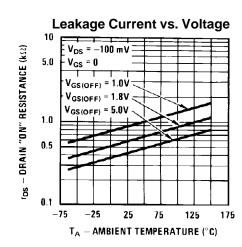
## Typical Characteristics (continued)

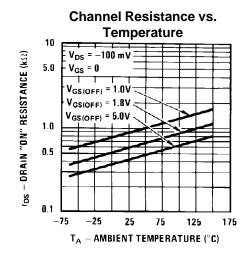








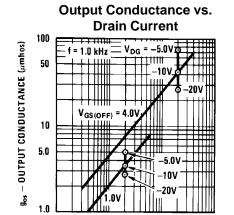




## **P-Channel General Purpose Amplifier**

(continued)

## Typical Characteristics (continued)



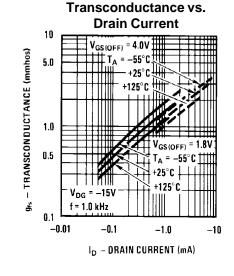
-0.1

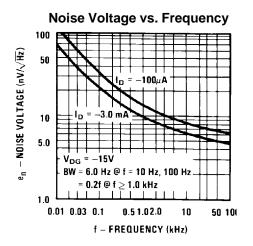
-1.0

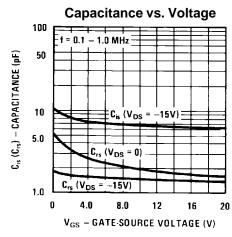
ID - DRAIN CURRENT (mA)

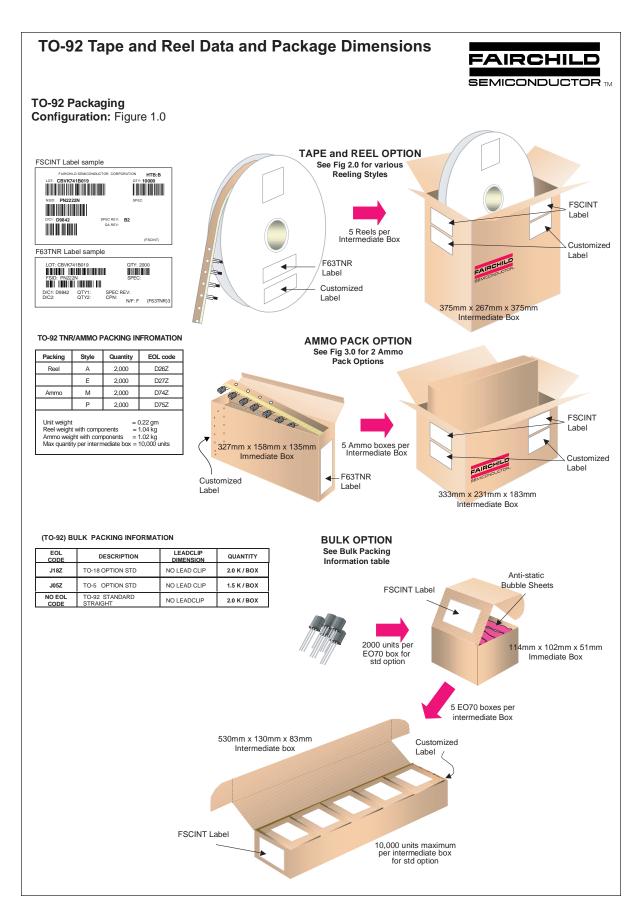
-10

-0.01





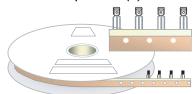




## TO-92 Tape and Reel Data and Package Dimensions, continued

# **TO-92 Reeling Style Configuration:** Figure 2.0

## Machine Option "A" (H)

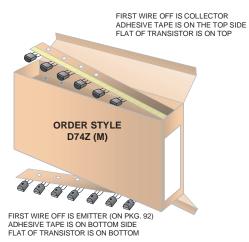


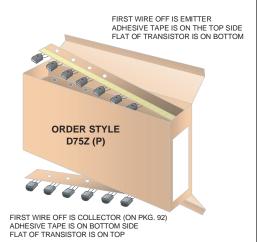
Style "A", D26Z, D70Z (s/h)

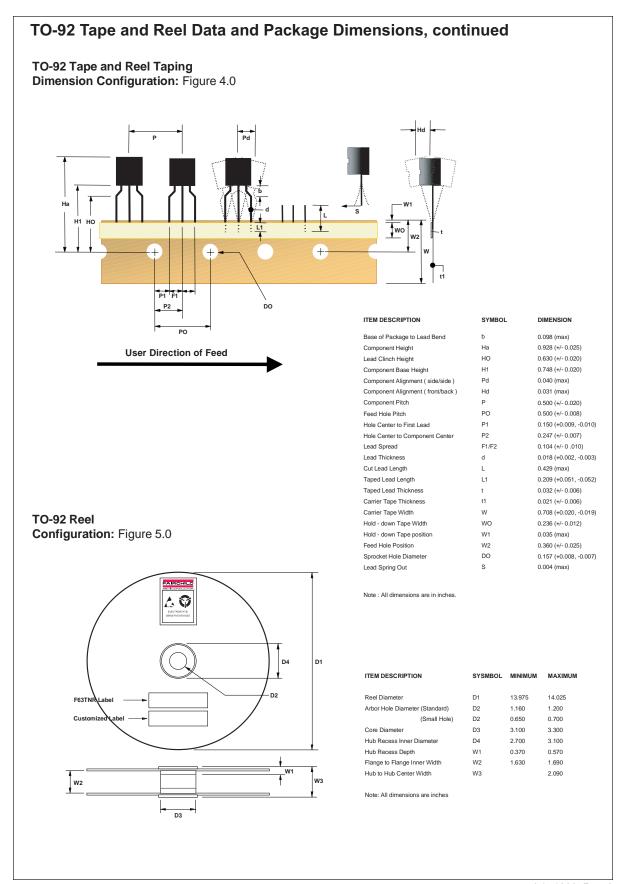
# Machine Option "E" (J)

Style "E", D27Z, D71Z (s/h)

# **TO-92 Radial Ammo Packaging Configuration:** Figure 3.0

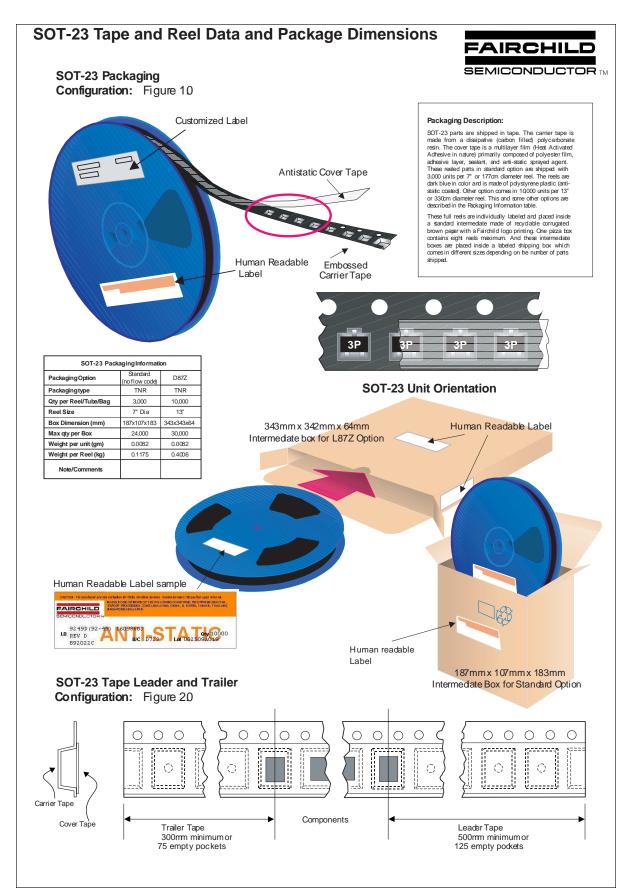






# **TO-92 Tape and Reel Data and Package Dimensions** TO-92 (FS PKG Code 92, 94, 96) Scale 1:1 on letter size paper Dimensions shown below are in: inches [millimeters] Part Weight per unit (gram): 0.1977 0.185 4.70 0.170 4.32 TO-92 (92,94,96) 96 94 В В 0.76 В G Ε Ø0.060 [Ø1.52] 0.010 [0.254] DEEP В S С 0.615 0.570 5.0°TYP.

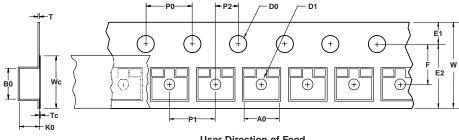
January 2000, Rev. B



## SOT-23 Tape and Reel Data and Package Dimensions, continued

## **SOT-23 Embossed Carrier Tape**

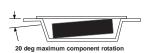
Configuration: Figure 3.0



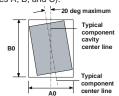
User Direction of Feed	

Dimensions are in millimeter														
Pkg type	Α0	В0	w	D0	D1	E1	E2	F	P1	P0	K0	т	Wc	Тс
<b>SOT-23</b> (8mm)	3.15 +/-0.10	2.77 +/-0.10	8.0 +/-0.3	1.55 +/-0.05	1.125 +/-0.125	1.75 +/-0.10	6.25 min	3.50 +/-0.05	4.0 +/-0.1	4.0 +/-0.1	1.30 +/-0.10	0.228 +/-0.013	5.2 +/-0.3	0.06 +/-0.02

Notes: A0, B0, and K0 dimensions are determined with respect to the EIA/Jedec RS-481 rotational and lateral movement requirements (see sketches A, B, and C).



Sketch A (Side or Front Sectional View)
Component Rotation



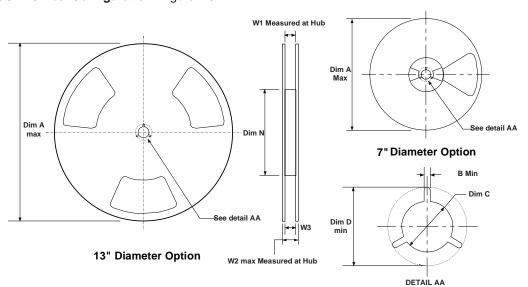
Sketch B (Top View)
Component Rotation



Sketch C (Top View)

Component lateral movement

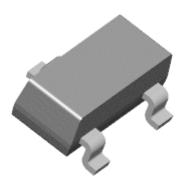
## SOT-23 Reel Configuration: Figure 4.0

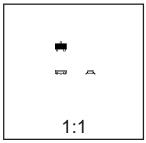


	Dimensions are in inches and millimeters								
Tape Size	Reel Option	Dim A	Dim B	Dim C	Dim D	Dim N	Dim W1	Dim W2	Dim W3 (LSL-USL)
8mm	7" Dia	7.00 177.8	0.059 1.5	512 +0.020/-0.008 13 +0.5/-0.2	0.795 20.2	2.165 55	0.331 +0.059/-0.000 8.4 +1.5/0	0.567 14.4	0.311 - 0.429 7.9 - 10.9
8mm	13" Dia	13.00 330	0.059 1.5	512 +0.020/-0.008 13 +0.5/-0.2	0.795 20.2	4.00 100	0.331 +0.059/-0.000 8.4 +1.5/0	0.567 14.4	0.311 - 0.429 7.9 - 10.9

## SOT-23 Tape and Reel Data and Package Dimensions, continued

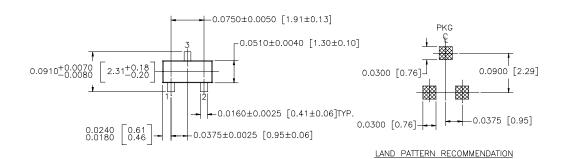
# SOT-23 (FS PKG Code 49)

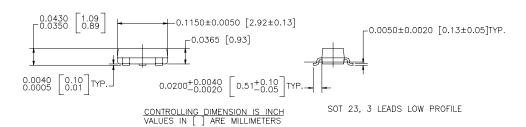




Scale 1:1 on letter size paper Dimensions shown below are in: inches [millimeters]

Part Weight per unit (gram): 0.0082





NOTE: UNLESS OTHERWISE SPECIFIED

- 1. STANDARD LEAD FINISH 150 MICROINCHES / 3.81 MICROMETERS MINIMUM TIN / LEAD (SOLDER) ON ALLOY 42
- 2. REFERENCE JEDEC REGISTRATION TO-236, VARIATION AB, ISSUE G, DATED JUL 1993

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