

## Farnell Order Code: 1641042

# FOX MODEL: FXO-HC536R-32 FOX Part Number: 767-32-9

HCMOS, CERAMIC, SMD, OSCILLATOR

**Specifications** 

| 32.000 MHz              |  |  |
|-------------------------|--|--|
| ±25 PPM                 |  |  |
|                         |  |  |
| -40°C ~ +85°C           |  |  |
| -55°C ~ +125°C          |  |  |
| 3.3V ± 5%               |  |  |
| 35 mA Max               |  |  |
| 10% V <sub>DD</sub> Max |  |  |
| 90% V <sub>DD</sub> Min |  |  |
| 45% ~ 55%               |  |  |
| 3 nS Max                |  |  |
| 3 nS Max                |  |  |
| 15 pF Max               |  |  |
| 10 ms Max               |  |  |
| 100 nS Max              |  |  |
| < 1.1pS Typ.            |  |  |
| 260°C / 10 Seconds      |  |  |
| 1                       |  |  |
| Au                      |  |  |
|                         |  |  |

<sup>&</sup>lt;sup>1</sup> Inclusive of 25°C tolerance, operating temperature range, input voltage change, load change, aging, shock, and vibration.

<sup>&</sup>lt;sup>2</sup>An internal pullup resistor from pin 1 to pin 4 allows active output if pin 1 is left open.

| ENABLE/DISABLE FUNCTION <sup>2</sup> |                |  |  |  |
|--------------------------------------|----------------|--|--|--|
| Pin 1                                | OUTPUT (Pin 3) |  |  |  |
| OPEN <sup>2</sup>                    | ACTIVE         |  |  |  |
| '1' Level VIH ≥ 70%V <sub>DD</sub>   | ACTIVE         |  |  |  |
| '0' Level VIL ≤ 30%V <sub>DD</sub>   | High Z         |  |  |  |

**NOTE**: A  $0.01\mu F$  bypass capacitor should be placed between  $V_{DD}$  (Pin4) and GND (Pin 2) to minimize power supply line noise.

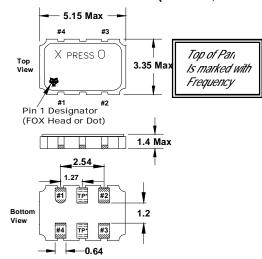
#### **RoHS Compliance Status: Compliant**

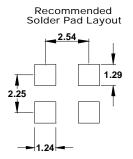


Drawing is for reference to critical specifications defined by size measurements.

Certain non-critical visual attributes, such as side castellations, reference pin shape, etc. may vary

#### Mechanical / Dimensions (millimeters):





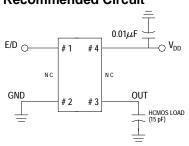
Note: XPRESSO HCMOS XOs are designed to fit on industry standard, 4 pad, layouts.

Pin Connections

#1) E/D #3 Output #2 GND #4 VDD

\*TP are test points and are NC

### **Recommended Circuit**



<u>NOTE</u>: The above specifications, having been carefully prepared and checked, is believed to be accurate at the time of publication; however, no responsibility is assumed by Fox Electronics for inaccuracies.

| Reference / Comments | Title / Description: Product Specifications |          |        |                  |  |
|----------------------|---|----------|--------|------------------|--|
|                      | Drawing Number: DWG                         | -4548-26 | Rev: 1 | Size: A          |  |
|                      | Part Number: 767-32-9                       |          |        | Cage: 61429      |  |
|                      | Draftsperson: AR Approved:                  |          | N      | Date: 08/25/2008 |  |