



CSD25301W1015

SLPS210-AUGUST 2009

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P-Channel NexFET[™] Power MOSFET

Check for Samples: CSD25301W1015

FEATURES

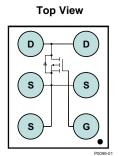
- Ultra Low Qg and Qgd
- Small Footprint
- Low Profile 0.62mm Height
- Pb Free
- RoHS Compliant
- Halogen Free
- CSP 1 × 1.5 mm Wafer Level Package

APPLICATIONS

- Battery Management
- Load Switch
- Battery Protection

DESCRIPTION

The device has been designed to deliver the lowest on resistance and gate charge in the smallest outline possible with excellent thermal characteristics in an ultra low profile.



PRODUCT SUMMARY

V _{DS}	Drain to Source Voltage	-20		V
Qg	Gate Charge Total (4.5V)	1.9		nC
Q_{gd}	Gate Charge Gate to Drain	0.4		nC
		$V_{GS} = -1.5V$	175	mΩ
R _{DS(on)}	R _{DS(on)} Drain to Source On Resistance		80	mΩ
		$V_{GS} = -4.5V$	62	mΩ
V _{GS(th)}	Voltage Threshold	-0.75		V

ORDERING INFORMATION

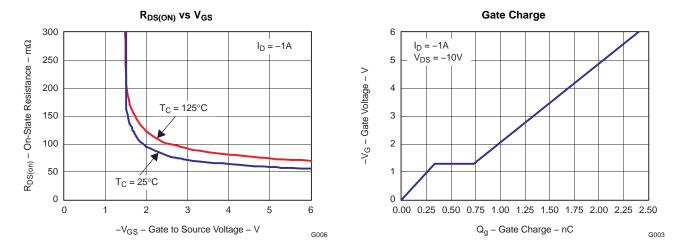
Device	Package	Media	Qty	Ship
CSD25301W1015	1 × 1.5 Wafer Level Package	7-inch reel	3000	Tape and Reel

ABSOLUTE MAXIMUM RATINGS

$T_A = 25^{\circ}C$ unless otherwise stated		VALUE	UNIT
V _{DS}	Drain to Source Voltage	-20	V
V_{GS}	Gate to Source Voltage	±8	V
ID	Continuous Drain Current, $T_C = 25^{\circ}C^{(1)}$	-2.2	А
I _{DM}	Pulsed Drain Current, $T_A = 25^{\circ}C^{(2)}$	-8.8	А
PD	Power Dissipation ⁽¹⁾	1.5	W
T _J , T _{STG}	Operating Junction and Storage Temperature Range	-55 to 150	°C

(1) $R_{\theta JA} = 85^{\circ}C/W$ on $1in^2$ Cu (2 oz.) on 0.060" thick FR4 PCB.

(2) Pulse width $\leq 300 \mu s$, duty cycle $\leq 2\%$



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AA)

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These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

ELECTRICAL CHARACTERISTICS

 $(T_A = 25^{\circ}C \text{ unless otherwise stated})$

	PARAMETER	TEST CONDITIONS	MIN TYP	MAX	UNIT
Static Cl	haracteristics				
BV _{DSS}	Drain to Source Voltage	$V_{GS} = 0V, I_D = -250\mu A$	-20		V
I _{DSS}	Drain to Source Leakage Current	$V_{GS} = 0V, V_{DS} = -16V$		-1	μA
I _{GSS}	Gate to Source Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 8V$		-100	nA
V _{GS(th)}	Gate to Source Threshold Voltage	$V_{DS} = V_{GS}, I_D = -250 \mu A$	-0.4 -0.75	-1	V
-		$V_{GS} = -1.5V, I_D = -1A$	175	220	mΩ
R _{DS(on)}	Drain to Source On Resistance	$V_{GS} = -2.5V, I_D = -1A$	80	100	mΩ
		$V_{GS} = -4.5V, I_D = -1A$	62	75	mΩ
9 _{fs}	Transconductance	$V_{DS} = -10V, I_D = -1A$	5.8		S
Dynamic	Characteristics				
C _{ISS}	Input Capacitance		210	270	pF
C _{OSS}	Output Capacitance	V _{GS} = 0V, V _{DS} = -10V, f = 1MHz	90	120	pF
C _{RSS}	Reverse Transfer Capacitance		30	40	pF
Qg	Gate Charge Total (-4.5V)		1.9	2.5	nC
Q _{gd}	Gate Charge Gate to Drain		0.4		nC
Q _{gs}	Gate Charge Gate to Source	$V_{DS} = -10V, I_D = -1A$	0.35		nC
Qg(th)	Gate Charge at Vth		0.17		nC
Q _{OSS}	Output Charge	$V_{DS} = -9.8V, V_{GS} = 0V$	1.7		nC
t _{d(on)}	Turn On Delay Time		4		ns
t _r	Rise Time	$V_{DS} = -10V, V_{GS} = -4.5V, I_{D} = -1A$	2		ns
t _{d(off)}	Turn Off Delay Time	$R_G = 20\Omega$	29		ns
t _f	Fall Time		12		ns
Diode C	haracteristics				
V_{SD}	Diode Forward Voltage	$I_{\rm S}$ = -1A, $V_{\rm GS}$ = 0V	-0.75	-1	V
Q _{rr}	Reverse Recovery Charge	V_{dd} = -9.8V, I _F = -1A, di/dt = 200A/µs	0.9		nC
t _{rr}	Reverse Recovery Time	V_{dd} = -9.8V, I _F = -1A, di/dt = 200A/µs	8.2		ns
					-

THERMAL CHARACTERISTICS

($T_A = 25^{\circ}C$ unless otherwise stated)

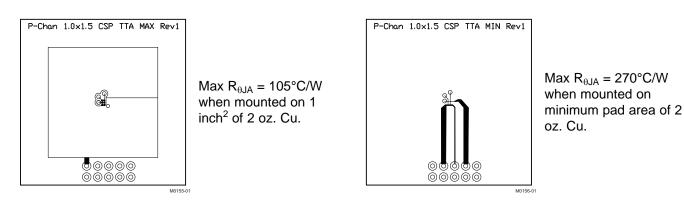
PARAMETER		MIN	TYP	MAX	UNIT
R $_{\theta JC}$	Thermal Resistance Junction to Ambient (Minimum Cu area)			270	°C/W
R $_{\theta JA}$	Thermal Resistance Junction to Ambient (1 in ² Cu area)			105	°C/W



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TYPICAL MOSFET CHARACTERISTICS

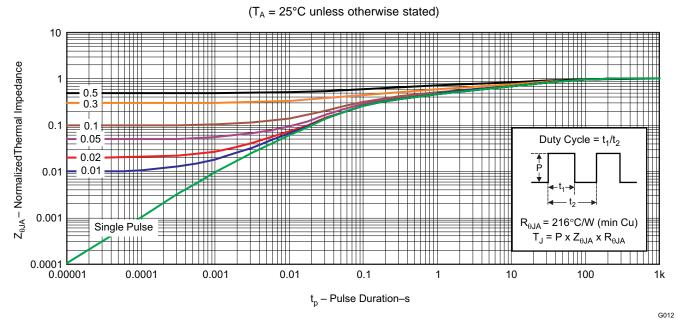


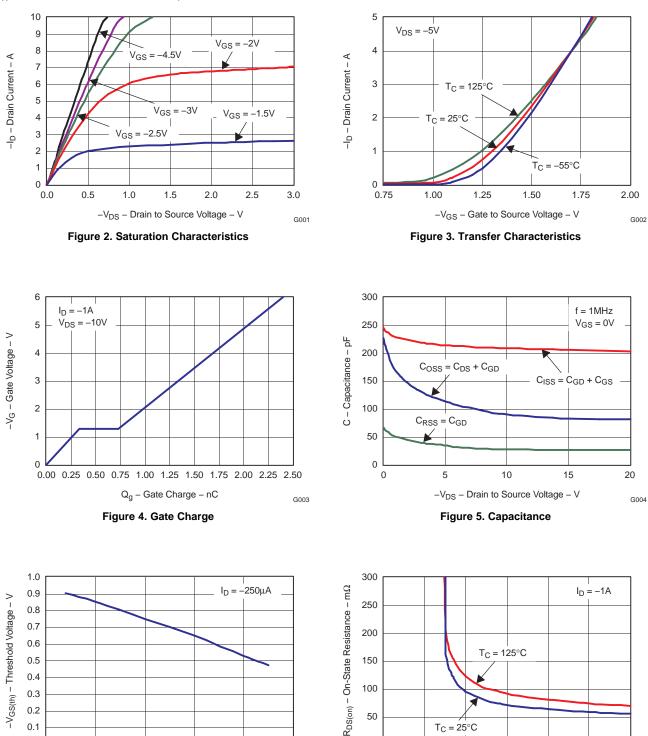
Figure 1. Transient Thermal Impedance



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TYPICAL MOSFET CHARACTERISTICS (continued)

$(T_A = 25^{\circ}C \text{ unless otherwise stated})$



T_C – Case Temperature – °C _{G005}

75

125

175

25

Figure 7. On Resistance vs. Gate Voltage

3

-V_{GS} - Gate to Source Voltage - V

2

-25

0.0

4

-75

4

5

6

G006

0

0

1



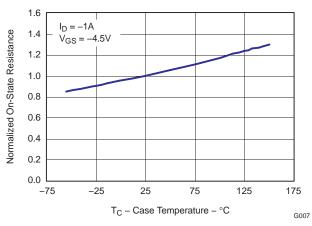
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TYPICAL MOSFET CHARACTERISTICS (continued)

$(T_A = 25^{\circ}C \text{ unless otherwise stated})$





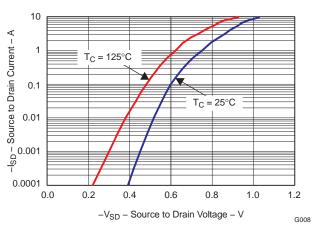


Figure 9. Typical Diode Forward Voltage

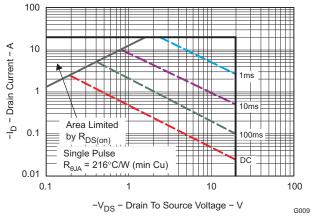


Figure 10. Maximum Safe Operating Area

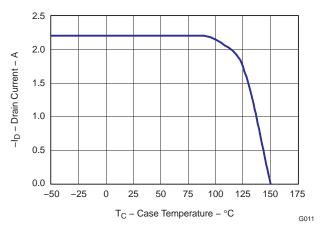


Figure 11. Maximum Drain Current vs. Temperature

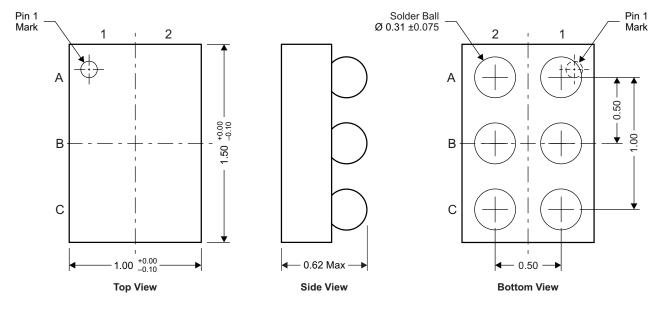


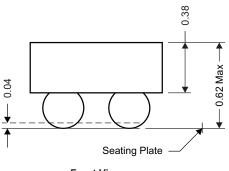
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MECHANICAL DATA

CSD25301W1015 Package Dimensions





Front View

M0157-01

NOTE: All dimensions are in mm (unless otherwise specified)

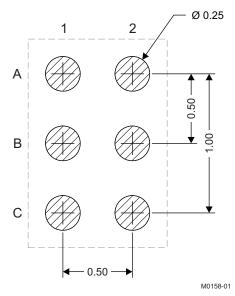
Pinout				
POSITION	DESIGNATION			
C1, C2	Drain			
A1	Gate			
A2, B1, B2	Source			

6

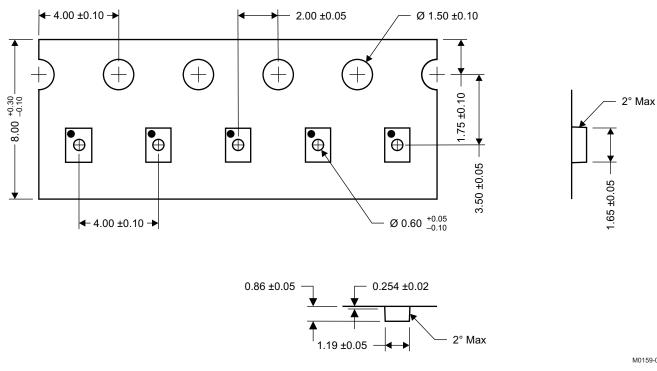


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Land Pattern Recommendation



NOTE: All dimensions are in mm (unless otherwise specified)



Tape and Reel Information

M0159-01

NOTE: All dimensions are in mm (unless otherwise specified)

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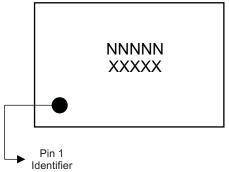
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Package Marking Information

Location

1st Line

Product Code	= NNNNN, First 5 digits after CSD (Fixed Text)
2nd Line XXXXX	= Last 5 digits of lot number



M0160-01

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